## Not Recommended for New Designs

This product was manufactured for Maxim by an outside wafer foundry using a process that is no longer available. It is not recommended for new designs. The data sheet remains available for existing users.
A Maxim replacement or an industry second-source may be available. Please see the QuickView data sheet for this part or contact technical support for assistance.
For further information, contact Maxim's Applications Tech Support.
 Step-Down, PWM, Switch-Mode DC-DC Regulators

## General Description

The MAX830/MAX831/MAX832/MAX833 are monolithic, bipolar, pulse-width-modulation (PWM), switch-mode, stepdown DC-DC regulators. Each is rated at 1A. Very few external components are needed for standard operation because the power switch, oscillator, feedback, and control circuitry are all on-chip. Employing a classic buck topology, these regulators perform high-current step-down functions.
These regulators have excellent dynamic and transient response characteristics, while featuring cycle-by-cycle current limiting to protect against overcurrent faults and shortcircuit output faults. They have a wide 8 V to 30 V input range. Outputs for the MAX831/MAX832/MAX833 are fixed at $5 \mathrm{~V} / 3.3 \mathrm{~V} / 3 \mathrm{~V}$, respectively. The MAX830 output is adjustable.
Available in 16-pin SO packages, the MAX830-MAX833 have a preset 100 kHz oscillator frequency. In addition, the preset current limit and micropower shutdown can be externally controlled. See the MAX724/MAX726 data sheet for more applications information.

## Applic ations

Distributed Power from High-Voltage Buses
High-Current, High-Voltage Step-Down Applications
Multiple-Output Buck Converter

Typical Operating Circ uit


STEP-DOWN CONVERTER

* CoilCraft DO3316-104


Ordering Information

Pin Configuration

( ) ARE FOR MAX830

* THIS THERMAL RESISTANCE NUMBER IS WITH THE DEVICE WELL MOUNTED ON 1 oz . COPPER WITH THERMAL PASTE BETWEEN THE IC AND THE UNDERLYING GROUND PLANE. LOWER THERMAL RESISTANCE IS POSSIBLE UNDERLYING GROUND PLANE,


# 5V/3.3V/3V/Adjustable-Output, 1A, Step-Down, PWM, Switch-Mode DC-DC Regulators 



Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## ELECTRICAL CHARACTERISTICS

( $\mathrm{V}_{\text {IN }}=25 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=\mathrm{T}_{\text {MIN }}$ to $\mathrm{T}_{\mathrm{MAX}}$, unless otherwise noted. Typical values are at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$.)

| PARAMETER | CONDITIONS |  | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Input Supply Voltage Range |  |  | 8 |  | 30 | V |
| Switch-On Voltage (Note 2) | ISW $=0.2 \mathrm{~A}$ |  |  |  | 1.1 | V |
|  | ISW $=1 \mathrm{~A}$ |  |  |  | 1.4 |  |
| Switch-Off Leakage | $\mathrm{V}_{\text {IN }}=25 \mathrm{~V}, \mathrm{~V}$ SW $=0 \mathrm{~V}$ | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ |  |  | 150 | $\mu \mathrm{A}$ |
|  | V IN $=30 \mathrm{~V}, \mathrm{~V}$ SW $=0 \mathrm{~V}$ |  |  |  | 250 |  |
| Supply Current (Note 3) | $\mathrm{V}_{\mathrm{IN}} \leq 30 \mathrm{~V}, \mathrm{~V}_{\text {SENSE }}=5.5 \mathrm{~V}($ MAX831/MAX832/ MAX833) or $\mathrm{V}_{\mathrm{FB}}=2.5 \mathrm{~V}$ (MAX830) |  |  | 8 | 11 | mA |
|  | $\mathrm{V}_{\text {SHUT }}=0.1 \mathrm{~V}$ (Note 4) |  |  | 140 | 500 | $\mu \mathrm{A}$ |
| Minimum Supply Voltage (Note 5) | Normal mode |  |  | 7.3 | 8.0 | V |
|  | Startup mode |  |  | 3.5 | 4.8 |  |
| Switch-Current Limit (Note 6) | ILIM open |  | 1.2 | 1.7 | 2.2 | A |
|  | RLIM $=10 \mathrm{k} \Omega$ (Note 7) | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ | 1.2 |  |  |  |
|  | RLIM $=7 \mathrm{k} \Omega$ (Note 7) |  | 0.8 |  |  |  |
| Maximum Duty Cycle |  |  | 85 | 90 |  | \% |
| Switching Frequency |  | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ | 90 | 100 | 110 | kHz |
|  |  |  | 85 |  | 120 |  |
|  | VFB or $\mathrm{V}_{\text {SENSE }}=0 \mathrm{~V}$ (Note 6) | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ |  | 20 |  |  |
| Switching-Frequency Line Regulation | $8 \mathrm{~V} \leq \mathrm{V}$ IN $\leq 30 \mathrm{~V}$ |  |  | 0.03 | 0.10 | \%/V |
| Error-Amplifier Voltage Gain | $1 \mathrm{~V} \leq \mathrm{V}_{\mathrm{C}} \leq 4 \mathrm{~V}$ | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ | 2000 |  |  | V/V |
| Error-Amplifier Transconductance |  | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ | 3000 | 5000 | 9000 | $\mu \mathrm{mho}$ |
| Error-Amplifier Source Current | $\mathrm{V}_{\mathrm{FB}}=2.0 \mathrm{~V}$ (MAX830) or VSENSE $=2.0 \mathrm{~V}$ (MAX831/832/833) | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ | 100 | 140 | 225 | $\mu \mathrm{A}$ |
| Error-Amplifier Sink Current | $\mathrm{V}_{\mathrm{FB}}=2.5 \mathrm{~V}$ (MAX830) or VSENSE $=5.5 \mathrm{~V}$ (MAX831/832/833) | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ | 0.6 | 1.0 | 1.7 | mA |

## 5V/3.3V/3V/Adjustable-Output, 1A, Step-Down, PWM, Switch-Mode DC-DC Regulators

## ELECTRICAL CHARACTERISTICS (continued)

$\left(V_{I N}=25 \mathrm{~V}, \mathrm{~T}_{A}=\mathrm{T}_{\text {MIN }}\right.$ to $\mathrm{T}_{\text {MAX }}$, unless otherwise noted. Typical values are at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$.)

| PARAMETER | CONDITIONS |  | MIN | TYP | MAX | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| SENSE Voltage | $\mathrm{VC}=2 \mathrm{~V}$ | MAX831 | 4.85 | 5.00 | 5.15 | V |
|  |  | MAX832 | 3.20 | 3.30 | 3.40 |  |
|  |  | MAX833 | 2.90 | 3.00 | 3.10 |  |
| SENSE Divider Resistance | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ | MAX831 | 3.0 | 5.0 | 8.0 | $\mathrm{k} \Omega$ |
|  |  | MAX832 | 2.5 | 4.2 | 7.0 |  |
|  |  | MAX833 | 2.2 | 3.8 | 6.5 |  |
| SENSE Voltage Tolerance (Note 8) | Vout (nominal) $=5 \mathrm{~V}$ (MAX831), 3.3 V (MAX832), or 3V (MAX833) | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ |  | $\pm 0.5$ | $\pm 2.0$ | \% |
|  |  |  |  | $\pm 0.5$ | $\pm 3.0$ |  |
| FB Bias Current | $\mathrm{V}_{\text {FB }}=\mathrm{V}_{\text {REF }}(\mathrm{MAX830})$ |  |  | 0.5 | 2.0 | $\mu \mathrm{A}$ |
| Reference Voltage | $\mathrm{V}_{\mathrm{C}}=2 \mathrm{~V}$ (MAX830) (Note 9) |  | 2.155 | 2.21 | 2.265 | V |
| Reference Voltage Tolerance (Notes 8, 9) | VREF (nominal $=2.21 \mathrm{~V})(\mathrm{MAX} 830)$ | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ |  | $\pm 0.5$ | $\pm 1.5$ | \% |
|  |  |  |  | $\pm 1.0$ | $\pm 2.5$ |  |
| Output Voltage Line Regulation | $8 \mathrm{~V} \leq \mathrm{V}$ IN $\leq 30 \mathrm{~V}$ |  |  | 0.005 | 0.020 | \%/V |
| VC Voltage | 0\% duty cycle | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ |  | 1.5 |  | V |
| $\mathrm{V}_{\mathrm{C}}$ Voltage Temperature Coefficient | 0\% duty cycle |  |  | -4 |  | $\mathrm{mV} /{ }^{\circ} \mathrm{C}$ |
| SHUT Current | $\mathrm{V}_{\text {SHUT }}=5 \mathrm{~V}$ |  |  | 10 | 20 | $\mu \mathrm{A}$ |
|  | $\mathrm{V}_{\text {SHUT }}=0.1 \mathrm{~V}$ (shutdown) |  |  |  | 50 |  |
| SHUT Threshold | (shutdown) |  | 0.10 | 0.20 | 0.50 | V |

Note 2: For switch currents between 0.2 A and 1 A , maximum switch-on voltage can be calculated via linear interpolation.
Note 3: By setting the SENSE pin to 5.5 V (or the FB pin to 2.5 V ), the $\mathrm{V}_{\mathrm{C}}$ pin is forced to its low clamp level and the switch duty cycle is forced to zero, approximating the zero load condition.
Note 4: Device shut down. Switch leakage current not included.
Note 5: For proper regulation, total voltage from $\mathrm{V}_{\mathrm{IN}}$ to GND must be $\geq 8 \mathrm{~V}$ after start-up. During start-up mode, device is switching but not regulating.
Note 6: To avoid extremely short switch-on times, the switch frequency is internally scaled down when $V_{F B}$ or $V_{\text {SENSE }}$ is less than 1.3V. Switch current limit is tested with $\mathrm{V}_{\text {SENSE }}$ or $\mathrm{V}_{\mathrm{FB}}$ adjusted to give a $1 \mu \mathrm{~s}$ minimum switch-on time.

Note 7: $R_{\text {LIM }}=\left[\frac{\mathrm{LIM}}{1 \mathrm{~A}} \times 7.6 \mathrm{k} \Omega\right]+1 \mathrm{k} \Omega$. $\begin{aligned} & \text { Typical value only. Minimum to maximum deviation in current limit will be } \pm 30 \% \text {, } \\ & \text { comparable to that with lum pin open. }\end{aligned}$
Note 8: All conditions of input voltage, output voltage, temperature, and load current.
Note 9: FB is at the reference voltage when the MAX830 output voltage is in regulation.

# 5V/3.3V/3V/Adjustable-Output, 1A, Step-Down, PWM, Switch-Mode DC-DC Regulators 

## Pin Description

| PIN | NAME | FUNCTION |
| :---: | :---: | :---: |
| 8 | $\begin{aligned} & \hline \text { SENSE } \\ & \text { (MAX831/ } \\ & 2 / 3) \end{aligned}$ | For the MAX831/MAX832/MAX833, SENSE input is the internal error amplifier's input, and should be directly connected to VOUT. SENSE also aids current limiting by reducing oscillator frequency when Vout is low. |
|  | $\begin{gathered} \text { FB } \\ \text { (MAX830) } \end{gathered}$ | For the MAX830, the FB input is the internal error amplifier's input, and should be connected to the midpoint of a potential divider between Vout and GND (Figure 1). The output voltage, during regulation, will be that value that forces 2.21 V (VREF) at the FB pin. |
| 11 | $\mathrm{V}_{\mathrm{C}}$ | Error-amplifier output. A series RC network connected to this pin compensates the device. Output swing is limited to about 5.8 V in the positive direction and -0.7 V in the negative direction. $\mathrm{V}_{\mathrm{C}}$ can also synchronize the device to an external TTL clock in the 115 kHz to 170 kHz range. |
| $\begin{gathered} 5,7 \\ 10,12 \end{gathered}$ | GND | Ground requires a short, low-noise connection to ensure good load regulation. The internal reference is referred to GND, so errors at this pin are multiplied by the error amplifier. Use thick copper for low thermal resistance. |
| $\begin{aligned} & 13,14, \\ & 15,16 \end{aligned}$ | $\mathrm{V}_{\text {SW }}$ | Internal power switch output (rated for 1A load current) |
| $\begin{aligned} & 1,2, \\ & 3,4 \end{aligned}$ | $\mathrm{V}_{\text {IN }}$ | $\mathrm{V}_{\mathrm{IN}}$ supplies power to the internal circuitry and also connects to the collector of the internal power switch. $\mathrm{V}_{\text {IN }}$ must be bypassed with a low-ESR capacitor, typically $100 \mu \mathrm{~F}$. |
| 6 | ILIM | Switch-current limit can be reduced by connecting an external resistor ( $\mathrm{R}_{\text {LIM }}$ ) from $\mathrm{I}_{\text {LIM }}$ to GND (See Note 7 in Electrical Characteristics). Leave I IIM floating for maximum current limit. |
| 9 | SHUT | Shutdown is achieved by pulling SHUT low. Leave SHUT floating for normal operation. |

## Applications Information

Although the MAX830-MAX833 are high-efficiency step-down voltage converters, certain precautions are required to avoid excessive chip temperature (the absolute maximum rating for this parameter, $\mathrm{T}_{\mathrm{j}}$, is $+125^{\circ} \mathrm{C}$ ). The 16 -pin SO package has thermal resistance of $+55^{\circ} \mathrm{C} / \mathrm{W}$ when mounted properly; this limits continuous chip power dissipation to the 1 W range.
When installing these chips:

1) Use wide and, if possible, thick copper traces to connect the leads (especially the GND pins) to reduce thermal resistance.
2) Bond the package firmly to the board or use a clipstyle heatsink. A very small drop of thermal paste between the chip package and the copper ground plane is also helpful.
3) Confirm that the range of device operation is such that the chip temperature does not exceed the allowed maximum of $\mathrm{T}_{\mathrm{j}}=+125^{\circ} \mathrm{C}$. This can be determined from Tables 1 and 2.
Tables 1 and 2 show numbers for maximum allowed load current based on not exceeding the $\mathrm{T}_{\mathrm{j}}=+125^{\circ} \mathrm{C}$ limit. If the input voltage range, ambient temperature, and/or output voltage setting allow for an unsafe level of load current, limit the load current to a safe value by connecting the appropriate resistor from the ILIM pin to GND. With no external resistor, the preset switch cur-
rent limit (typically 1.7A) will limit the load current to about 1.3A. Table 1 shows safe operating load currents for the MAX830 for various values of input and output voltage and at three different ambient temperatures of $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C},+45^{\circ} \mathrm{C}$, and $+70^{\circ} \mathrm{C}$, respectively. Table 2 shows the same information, but for the fixed output voltage MAX831, MAX832, and MAX833.
When consulting these tables, note that power surges of less than 30 sec need not be considered from a thermal standpoint. It is important for proper regulation, however, that a power surge not require a peak switch current exceeding the 1.2A (min) switch current limit. Also, the inductor's current rating should exceed IPEAK. For highest efficiency, the inductor series resistance should be $0.4 \Omega$ or less. IPEAK and ILOAD are related by the following formula:

$$
\text { IPEAK }=I_{\text {LOAD }}+\frac{[\text { VOUT } \times(\text { VIN }- \text { VOUT })]}{V_{\text {IN }} \times L \times 200,000}
$$

Example: MAX830 with VIN $=30 \mathrm{~V}$, VOUT $=15 \mathrm{~V}$, ILOAD $=0.5 \mathrm{~A}$ continuous, $\mathrm{L}=100 \mu \mathrm{H}$ and $\mathrm{T}_{\mathrm{A}}=+70^{\circ} \mathrm{C}$ :
IPEAK $=0.86 \mathrm{~A}$ (which is below 1.2A (min) preset switch current limit); and from Table 1:
maximum ILOAD allowed $=0.8 \mathrm{~A}$ continuous (i.e., operation is safe).

## 5V/3.3V/3V/Adjustable-Output, 1A, Step-Down, PWM, Switch-Mode DC-DC Regulators

Table 1. MAX830 Maximum Load Current (continuous with $\theta \mathrm{j} A=+55^{\circ} \mathrm{C} / \mathrm{W}$ ) vs. Output Voltage and Ambient Temperature

| OUTPUT VOLTAGE | CONTINUOUS LOAD CURRENT |  |  |
| :---: | :---: | :---: | :---: |
|  | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ | $\mathrm{T}_{\mathrm{A}}=+45^{\circ} \mathrm{C}$ | $\mathrm{T}_{\mathrm{A}}=+70^{\circ} \mathrm{C}$ |
| Up to 10V | 1A | 1A | 1A |
| 10 V to 15 V | 1A | 1A | 0.8A |
| 15 V to 20 V | 1A | 0.8A | 0.7A |
| 20 V to 25 V | 0.8A | 0.7A | 0.6A |

Table 2. MAX831/MAX832/MAX833 Maximum Load Current (continuous with $\theta_{\mathrm{j}} \mathrm{A}=+55^{\circ} \mathrm{C} / \mathrm{W}$ ) vs. Ambient Temperature

| OUTPUT VOLTAGE | CONTINUOUS LOAD CURRENT |  |  |
| :---: | :---: | :---: | :---: |
|  | $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ | $\mathrm{T}_{\mathrm{A}}=+45^{\circ} \mathrm{C}$ | $\mathrm{T}_{\mathrm{A}}=+70^{\circ} \mathrm{C}$ |
| $\begin{gathered} \text { Fixed: } \\ 5 \mathrm{~V}, 3.3 \mathrm{~V} \text { or } 3 \mathrm{~V} \end{gathered}$ | 1A | 1A | 1A |



Figure 1. MAX830 Typical Operating Circuit

