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DSL Line Drivers and Receivers
DSL Line Drivers

Device	BW (MHz)	SR (V/μs)	V _S (min) (V)	V _S (max) (V)	I _S (per amp) (mA)	Noise V _N (nV/√Hz)	I _{BIAS} (μA)	V _{OS} (max) (mV)	# of Devices/ Channels	I _{OUT} (mA)	V _{OUT} (V)	Rail-to-Rail		PSRR (dB)	CMRR (dB)	Gain A _V (min)	Enable	Package
												Input	Output					
EL1528	40	210	10	24	5	3.5	30	10	2	1000	22.2	N	N	75		1	Y	20 Ld HTSSOP, 24 Ld QFN
EL1526	46	500	10	24	2.5	3.5	30	10	4	1000	22.9	N	N	70		1	Y	20 Ld HTSSOP, 24 Ld QFN
EL1529	46	500	10	24	2.5	3.5	30	10	4	1000	22.9	N	N	70		1	Y	24 Ld QFN
EL1537	50	300	5	24	7.5	3.9	30	10	2	500	22.2	N	N	70		1	Y	28 Ld HTSSOP
EL1537A	50	300	5	24	7.5	3.9	30	10	2	500	22.2	N	N	70		1	Y	28 Ld HTSSOP
EL1537B	50	300	5	24	7.5	3.9	30	10	2	500	22.2	N	N	70		1	Y	28 Ld HTSSOP
EL1506	70	500	5	24	3.5	2.8	30	17	2	500	21.8	N	N	63		1	Y	24 Ld SOIC, 28 Ld HTSSOP
EL1507	70	500	5	24	7.5	2.8	30	17	2	500	21.8	N	N	63		1	Y	16 Ld QFN, 16 Ld SOIC
EL1509	70	500	5	12	7	2.8	30	20	2	450	10	N	N	50		1	N	8 Ld DFN, 8 Ld SOIC
EL1510	70	500	5	24	7.5	2.8	30	17	2	500	21.8	N	N	62		1	N	8 Ld DFN, 8 Ld SOIC
EL1511	70	500	5	15	3.5	2.8	30	20	2	450	13	N	N	50		1	Y	16 Ld QFN, 16 Ld SOIC
EL1519	70	350	5	12	7.5	2.7	30	20	2	450	10	N	N	50		1	N	8 Ld SOIC
EL1527	70	500	5	24	7.5	2.8	30	17	2	500	21.8	N	N	60		1	Y	28 Ld HTSSOP
EL1503A	80	1100	10	24	12.5	3.5	50	30	2	1000	21.2	Y	N	90		1	Y	20 Ld SOIC, 24 Ld QFN
EL1508	80	600	5	12	3.5	3.5	50	10	2	1000	21.6	N	N			1	Y	16 Ld SOIC, 20 Ld SOIC, 24 Ld QFN
ISL1535	100	500	5	30	13.3	3.9	70		2	500	21.6	N	N	50		5	Y	24 Ld QFN, 28 Ld HTSSOP
EL1517	200	800	5	12	7	6	5	25	2	450	10	N	N	90		1	Y	16 Ld QFN, 8 Ld SOIC
EL1517A	200	800	5	12	7	6	5	25	2	450	10	N	N	90		1	Y	10 Ld MSOP
EL1517B	200	800	5	12	7	6	5	25	2	450	10	N	N	90		1	Y	10 Ld HMSOP, 16 Ld QFN, 8 Ld SOIC
ISL1571	250	1200	4.5	13.2	15	6	3	7.5	2	1000	10	N	N	95		1	Y	10 Ld HMSOP, 16 Ld QFN
ISL1572	250	1200	4.5	13.2	15	6	3	7.5	2	1000	10	N	N	95		1	Y	10 Ld HMSOP, 16 Ld QFN
ISL1557	300	1200	4.5	13.2	15	6	3	7.5	2	1000	10	N	N	95	95	1	Y	10 Ld HMSOP, 16 Ld QFN
ISL1557A	300	1200	4.5	13.2	15	8.2	3	7.5	2	1000	10	N	N	95	95	1	Y	10 Ld HMSOP, 16 Ld QFN
EL1516	350	128	5	12	5.5	1.3	6.5	3	2	100	9.7	N	N	80	105	2	N	8 Ld MSOP, 8 Ld SOIC
EL1516A	350	128	5	12	5.5	1.3	6.5	3	2	100	9.7	N	N	80	105	2	Y	10 Ld MSOP

DSL Line Receivers

Device	iSim	# of Devices/ Channels	BW (MHz)	SR (V/μs)	V _S (min) (V)	V _S (max) (V)	V _N (nV/√Hz)	Rail -to- Rail	Gain A _V (min)	I _S (per amp) (mA)	I _{BIAS} (μA)	I _{OUT} (mA)	V _{OUT} (V)	V _{OS} (max) (mV)	A _{VOL} or A _{ZOL} (dB or V/mV)	Enable	CMRR (dB)	PSRR (dB)	Package
EL2227	Y	2	115	50	5	24	1.9	N	2	4.8	3.4	100	20.8	3	87	N	94	95	8 Ld MSOP, 8 Ld SOIC
EL1516		2	220	120	4.5	12	1.3	N	2	4.5		100		3	75	N	105	80	8 Ld MSOP, 8 Ld SOIC
EL1516A		2	220	120	4.5	12	1.3	N	2	4.5		100		3	75	Y	105	80	10 Ld MSOP

Power Line Communications (PLC)

Line Drivers

Device	BW (MHz)	V _S (min) (V)	V _S (max) (V)	I _S (per amp) (mA)	SR (V/μs)	I _{OUT} (mA)	V _{OUT} (V)	Harmonic Distortion (dBc)	Package
ISL1571	250	4.5	13.2	11	1200	750	10 @ 50Ω	-75 @ 17MHz	10 Ld HMSOP, 16 Ld QFN
ISL1572	250	4.5	13.2	11	1200	750	10 @ 50Ω	-75 @ 17MHz	10 Ld HMSOP, 16 Ld QFN

10G Ethernet

Device	Data Rate (G)	RMS Jitter (ps)	Half Rate	V _S (V)	Power (W)	10 GigE Compliant	JTAG	Loop Back Mode	BIST	Package
BBT3420	2.5 to 3.125	<6	X	1.5/3.3	1.4	X	X	X	X	289 Ld EBGA
BBT3821	2.5 to 3.2	<6	X	1.5/2.5	1.6	X	X	X	X	192 Ld BGA
ISL35822	2.5 to 3.2	<6	X	1.5/2.5	1.5	X	X	X	X	112 Ld BGA

Voice Over IP

Subscriber Line Interface Circuits (SLICs)

Long Loop

Device	Longitudinal Balance (dB)	Polarity Reversal	Tip Open Ground Start	Integrated Battery Switch	Package
HC5503PRC	58				24 Ld SOIC, 32 Ld QFN
HC55120	53			X	28 Ld PLCC, 28 Ld SOIC
HC55121	53	X		X	28 Ld PLCC, 28 Ld SOIC
HC55130	63			X	28 Ld PLCC, 28 Ld SOIC
HC5513B	53				28 Ld PLCC
HC55140	63	X	X	X	28 Ld PLCC, 28 Ld SOIC
HC55142	63	X	X	X	28 Ld PLCC, 28 Ld SOIC
HC55150	55	X		X	28 Ld PLCC, 28 Ld SOIC
HC5515	53				28 Ld PLCC
HC5523	58				28 Ld PLCC
HC5526	53				22 Ld PDIP, 28 Ld PLCC
HC5549	53	X	X	X	28 Ld PLCC

Short Loop PBX

Device	Longitudinal Balance (dB)	Supply Voltages	Loop Current Limit (mA)	Ring Relay Drivers	Package
HC5503	53	-24, +5	25	1	24 Ld SOIC
HC5503T	53	-24, +5	30		24 Ld SOIC
HC5504B	58	-48, +5	40	1	24 Ld SOIC

Ringing SLICs

Device	Longitudinal Balance (dB)	High Battery VBH	Loop Current (mA)	Integrated Battery Switch	Polarity Reversal	Package
HC55183	45	75	15 to 45	X		28 Ld PLCC
HC55184	45	75	15 to 45	X	X	28 Ld PLCC
HC55185	58/53	100/85/75	15 to 45	X	X	28 Ld PLCC, 32 Ld QFN
ISL5585	58/53	100/85/75	15 to 45	X	X	28 Ld PLCC, 32 Ld QFN
ISL5586	58/53	100/85/75	15 to 45	X	X	28 Ld PLCC

Line Card Access Switches

Device	Prot SCR	Package
ISL5571A	Yes	16 Ld SOIC

Analog Crosspoint Switches

Device	Configuration	r _{DS(ON)} (Ω)	Freq. Response Typ. -3dB 14V (MHz)	Crosstalk Typ. -40dB 14V (MHz)	V _{CC} Range (±V)	Package
CD22M3494	16x8	36	50	10	4 to 15	40 Ld PDIP, 44 Ld PLCC

SERDES

Device	Function	V _S (V)	I _S (mA)	Clock Rate (MHz)	Format	Pre-emphasis	Equalization	Diff Input Impedance (Ω)	Interface	Package
ISL34340	Transceiver	3.3	117	40	21-Bit Data + 2-Bit Control	Y	Y	100	I ² C	64 Ld EPTQFP

Laser Diode Control

Device	# of Devices/ Channels	# of Taps	Interface Type	Resistance Options (kΩ)	Single Supply	V _S Range 2.7V to 3.3V	V _S Range 2.7V to 5.5V	V _S Range 3.0V to 5.5V	V _S Range 4.5V to 5.5V	Resistance Taper	V _L - V _H (V)	Wiper Current (I _W)	Wiper Resistance (R _W @ I _W)	Standby Current I _{sb} (μA)	Package
X9520	Triple	64, 100, 256	2-Wire	10, 10, 100	Y	Y	Y	Y	Y	Linear	0V to V _{CC}	4mA	400 @ 1mA	50	Eval Board, 20 Ld TSSOP
X9521	Dual	100, 256	2-Wire	10, 100	Y	Y	Y	Y	Y	Linear	0V to V _{CC}	4mA	400 @ 1mA	50	Eval Board, 20 Ld TSSOP
X9522	Triple	64, 100, 256	2-Wire	10, 10, 100	Y	Y	Y	Y	Y	Linear	0V to V _{CC}	4mA	400 @ 1mA	50	Eval Board, 20 Ld TSSOP
X9523	Dual	100, 256	2-Wire	10, 100	Y	Y	Y	Y	Y	Linear	0V to V _{CC}	4mA	400 @ 1mA	50	Eval Board, 20 Ld TSSOP
X9525	Dual	100, 256	2-Wire	10, 50	Y	Y	Y	Y	Y	Linear	0V to V _{CC}	4mA	400 @ 1mA	50	Eval Board, 20 Ld TSSOP

Device	Interface Type	Internal Temperature Sensor	ADC with Anti-Dither Filter Option	External Sensor Input	Internal Voltage Reference	V _{REF} Input/ Output	General Purpose EEPROM	Look Up Table Organization	Number of DACs	FSO Current DAC Setting Resistors	Current Drive Outputs	Package
X9530	2-Wire	Y	Y	Y	Y	Y	Y	Dual Bank	Dual	Ext/Int	Sink/Source	14 Ld TSSOP