

**Switches/MUXes/Crosspoints  
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## USB Switches

### Full Speed

Device	Config	Switch Control	Audio		Video	Bandwidth (MHz)		V <sub>DD</sub> (V)	I <sub>DD</sub> (μA)	Packages
			Input Range (V)	THD 32Ω (%)		USB	Video			
ISL54400	USB/MP3	V <sub>BUS</sub>	±1.5	0.007		239		2.5 to 3.6	5	10 Ld μTQFN, 10 Ld TDFN
ISL54401	USB/MP3	V <sub>BUS</sub>	±1.5	0.007		239		2.5 to 3.6	5	10 Ld μTQFN, 10 Ld TDFN
ISL54402	USB/MP3	Logic Level	±1.5	0.007		239		1.8 to 5.5	5	10 Ld μTQFN, 10 Ld TDFN
ISL54415	USB/Video	V <sub>BUS</sub>	±1.5	0.007	0.04/0.03	239	264	2.5 to 3.6	5	10 Ld μTQFN, 10 Ld TDFN
ISL54416	USB/Video	V <sub>BUS</sub>	±1.5	0.007	0.04/0.03	239	264	2.5 to 3.6	5	10 Ld μTQFN, 10 Ld TDFN
ISL54417	USB/Video	Logic Level	±1.5	0.007	0.04/0.03	239	264	1.8 to 5.5	5	10 Ld μTQFN, 10 Ld TDFN

### High Speed

Device	Config	Switch Control	Audio		Video	Bandwidth (MHz)		V <sub>DD</sub> (V)	I <sub>DD</sub> Active/PDN (μA)	Packages
			Input Range (V)	THD 32Ω (%)		USB	Video			
ISL54205A	USB/MP3	V <sub>BUS</sub>	±1.5	0.06		630		2.7 to 3.6	6.0/0.001	10 Ld μTQFN, 10 Ld TDFN
ISL54206	USB/MP3	Logic Level	±1.5	0.06		630		1.8 to 5.5	6.0/0.001	10 Ld μTQFN, 10 Ld TDFN
ISL54207	USB/Video	V <sub>BUS</sub>	±1.5	0.06	0.28/0.04	630	338	2.7 to 3.6	6.0/0.001	10 Ld μTQFN, 10 Ld TDFN
ISL54208	USB/Video	Logic Level	±1.5	0.06	0.28/0.04	630	338	1.8 to 5.5	6.0/0.001	10 Ld μTQFN, 10 Ld TDFN

### High/Full Speed

Device	Config	Switch Control	Bandwidth (MHz)		V <sub>DD</sub> (V)	I <sub>DD</sub> (μA)	Packages
			High	Full			
ISL54200	USB/MP3	Logic Level	630	394	2.7 to 5.5	0.020	10 Ld μTQFN, 10 Ld TDFN

### Low Voltage (Sub 0.5Ω)

## Switches

### Single

Device	Switch or MUX	Configuration	Type of Switch	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
ISL43L110	Switch	Single NO	SPST	0.24	8	5	89	30	68	160	50n	+1.1 to +4.5	5 Ld SC-70 T+R
ISL43L111	Switch	Single NC	SPST	0.24	8	5	89	30	68	160	50n	+1.1 to +4.5	5 Ld SC-70 T+R
ISL43L210	Switch	Single MIX	SPDT	0.38	7	3	25	30	40	110	50n	+1.1 to +4.5	6 Ld SC-70 T+R
ISL84714	Switch	Single MIX	SPDT	0.44	7.5	2.9	20	2	40	100	18n	+1.65 to +3.6	6 Ld SC-70 T+R
ISL84715	Switch	Single NO	SPST	0.26	9	5	70	3	68	160	18n	+1.65 to +3.6	5 Ld SC-70 T+R
ISL84716	Switch	Single NC	SPST	0.26	9	5	70	3	68	160	18n	+1.65 to +3.6	5 Ld SC-70 T+R

Dual

Device	Switch or MUX	Configuration	Type of Switch	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
ISL43L120	Switch	Dual NO	SPST	0.16	13	13	125	3	182	290	30n	+1.65 to +3.6	8 Ld DFN, 8 Ld MSOP
ISL43L121	Switch	Dual NC	SPST	0.16	13	13	125	3	182	290	30n	+1.65 to +3.6	8 Ld DFN, 8 Ld MSOP
ISL43L122	Switch	Dual MIX	SPST	0.16	13	13	125	3	182	290	30n	+1.65 to +3.6	8 Ld DFN, 8 Ld MSOP
ISL43L220	Switch/MUX	Dual 2x1	SPDT	0.22	12	5	128	45	115	224	60n	+1.1 to +4.5	10 Ld DFN
ISL43L410	Switch/MUX	Diff 2x1	DPDT	0.25	14	6	95	3	115	224	40n	+1.65 to +3.6	10 Ld DFN, 10 Ld MSOP
ISL43L710	Switch	Diff NO	SPST	0.17	15	15	125	3	182	290	30n	+1.65 to +3.6	8 Ld DFN, 8 Ld MSOP
ISL43L711	Switch	Diff NC	SPST	0.17	15	15	125	3	182	290	30n	+1.65 to +3.6	8 Ld DFN, 8 Ld MSOP
ISL43L712	Switch	Diff MIX	SPST	0.17	15	15	125	3	182	290	30n	+1.65 to +3.6	8 Ld DFN, 8 Ld MSOP
ISL54050	Switch/MUX	Dual 2x1	SPDT	0.3	40	20	170	100	62	176	0.1μ	+1.65 to +4.5	10 Ld QFN T+R
ISL84684	Switch/MUX	Dual 2x1	SPDT	0.29	14	6	95	3	115	224	40n	+1.65 to +3.6	10 Ld DFN, 10 Ld MSOP
ISL84684II	Switch/MUX	Dual 2x1	SPDT	0.27	43	27	200	40	140	355	20	+1.8 to +4.5	10 Ld CSP
ISL84762	Switch/MUX	Dual 2x1	SPDT	0.35	14	6	95	3	115	224	40n	+1.65 to +3.6	10 Ld DFN, 10 Ld MSOP
ISL8484	Switch/MUX	Dual 2x1	SPDT	0.23	20	15	128	45	115	224	80n	+1.65 to +4.5	10 Ld DFN, 10 Ld MSOP

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Quad

Device	Switch or MUX	Configuration	Type of Switch	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
ISL43L420	Switch/MUX	Quad 2x1	SPDT (Dual DPDT)	0.25	8	5	120	50	106	212	60n	+1.1 to +4.5	16 Ld QFN
ISL54056	Switch/MUX	Quad 2x1	SPDT (Dual DPDT)	0.4	33	16	248	70	38	102	0.1μ	+1.65 to +4.5	16 Ld QFN T+R
ISL83699	Switch/MUX	Quad 2x1	SPDT (Dual DPDT)	0.3	11	8	82	4	106	212	50n	+1.65 to +3.6	16 Ld QFN, 16 Ld TSSOP
ISL84780	Switch/MUX	Quad 2x1	SPDT (Dual DPDT)	0.4	12	8	97	4	62	125	50n	+1.65 to +3.6	16 Ld QFN, 16 Ld TSSOP
ISL8499	Switch/MUX	Quad 2x1	SPDT (Dual DPDT)	0.26	15	13	120	50	106	212	60n	+1.65 to +4.5	16 Ld QFN, 16 Ld TSSOP

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## Switches/MUXes

### Dual

Device	Switch or MUX	Configuration	Type of Switch	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
ISL43L220	Switch/MUX	Dual 2x1	SPDT	0.22	12	5	128	45	115	224	60n	+1.1 to +4.5	10 Ld DFN
ISL43L410	Switch/MUX	Diff 2x1	DPDT	0.25	14	6	95	3	115	224	40n	+1.65 to +3.6	10 Ld DFN, 10 Ld MSOP
ISL54050	Switch/MUX	Dual 2x1	SPDT	0.3	40	20	170	100	62	176	0.1μ	+1.65 to +4.5	10 Ld QFN T+R
ISL84684	Switch/MUX	Dual 2x1	SPDT	0.29	14	6	95	3	115	224	40n	+1.65 to +3.6	10 Ld DFN, 10 Ld MSOP
ISL84684II	Switch/MUX	Dual 2x1	SPDT	0.27	43	27	200	40	140	355	20	+1.8 to +4.5	10 Ld CSP
ISL84762	Switch/MUX	Dual 2x1	SPDT	0.35	14	6	95	3	115	224	40n	+1.65 to +3.6	10 Ld DFN, 10 Ld MSOP
ISL8484	Switch/MUX	Dual 2x1	SPDT	0.23	20	15	128	45	115	224	80n	+1.65 to +4.5	10 Ld DFN, 10 Ld MSOP

### Quad

Device	Switch or MUX	Configuration	Type of Switch	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
ISL43L420	Switch/MUX	Quad 2x1	SPDT	0.25	8	5	120	50	106	212	60n	+1.1 to +4.5	16 Ld QFN
ISL54056	Switch/MUX	Quad 2x1	SPDT (Dual DPDT)	0.4	33	16	248	70	38	102	0.1μ	+1.65 to +4.5	16 Ld QFN T+R
ISL83699	Switch/MUX	Quad 2x1	SPDT (Dual DPDT)	0.3	11	8	82	4	106	212	50n	+1.65 to +3.6	16 Ld QFN, 16 Ld TSSOP
ISL84780	Switch/MUX	Quad 2x1	SPDT (Dual DPDT)	0.4	12	8	97	4	62	125	50n	+1.65 to +3.6	16 Ld QFN, 16 Ld TSSOP
ISL8499	Switch/MUX	Quad 2x1	SPDT (Dual DPDT)	0.26	15	13	120	50	106	212	60n	+1.65 to +4.5	16 Ld QFN, 16 Ld TSSOP

## MUXes

### Single

Device	Switch or MUX	Configuration	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
ISL84781	MUX	Single 8x1	0.41	16	14	39	15	65	485	50n	+1.65 to +3.6	16 Ld QFN, 16 Ld TSSOP

Dual

Device	Switch or MUX	Configuration	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
ISL84782	MUX	Diff 4x1	0.5	16	14	65	8	62	232	50n	+1.65 to +3.6	16 Ld QFN, 16 Ld TSSOP
ISL43L840	MUX	Dual 4x1	0.5	19	19	96	8	62	232	50n	+1.65 to +3.6	16 Ld QFN, 16 Ld TSSOP
ISL54057	MUX	Diff 4x1	0.43	27	18		8.5	44	201	50n	+1.6V to +3.6V	16 Ld QFN T+R
ISL54058	MUX	Dual 4x1	0.43	29	29		8	44	201	50n	+1.6V to +3.6V	16 Ld QFN T+R
ISL84782	MUX	Diff 4x1	0.5	16	14	65	8	62	232	50n	+1.65 to +3.6	16 Ld QFN, 16 Ld TSSOP
ISL43L840	MUX	Dual 4x1	0.5	19	19	96	8	62	232	50n	+1.65 to +3.6	16 Ld QFN, 16 Ld TSSOP
ISL43L841	MUX	Diff 4x1	0.47	24	14	65	50	62	232	50n	+1.65 to +4.5	16 Ld QFN

Medium Voltage (2V-12V)

Switches

Single

Device	Switch or MUX	Configuration	Type of Switch	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
ISL43110	Switch	Single NO	SPST	7	37	21	8	0.01	15	40	1μ	+2.4 to +12	5 Ld SOT-23 T+R, 8 Ld SOIC
ISL43111	Switch	Single NC	SPST	7	37	21	8	0.01	15	40	1μ	+2.4 to +12	5 Ld SOT-23 T+R, 8 Ld SOIC
ISL43112	Switch	Single NO	SPST	15	47	25	7	0.01	13	30	15μ	±1.5V to ±6V	5 Ld SOT-23 T+R, 8 Ld SOIC
ISL43113	Switch	Single NC	SPST	15	47	25	7	0.01	13	30	15μ	±1.5V to ±6V	5 Ld SOT-23 T+R, 8 Ld SOIC
ISL84514	Switch	Single NO	SPST	10 (Max)	150 (Max)	100 (Max)	8	4	14	30	2μ	+2.4 to +12	5 Ld SOT-23 T+R, 8 Ld SOIC
ISL84515	Switch	Single NC	SPST	10 (Max)	150 (Max)	100 (Max)	8	4	14	30	2μ	+2.4 to +12	5 Ld SOT-23 T+R, 8 Ld SOIC
ISL84516	Switch	Single NO	SPST	13	40	30	10	2	9	22	40μ	±1.5V to ±6V	5 Ld SOT-23, 8 Ld SOIC
ISL84517	Switch	Single NC	SPST	13	40	30	10	2	9	22	40μ	±1.5V to ±6V	5 Ld SOT-23 T+R, 8 Ld SOIC

Dual

Device	Switch or MUX	Configuration	Type of Switch	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
ISL43120	Switch	Dual NO	SPST	11	25	17	5	0.01	8	21	0.05μ	+2.7 to +12	8 Ld SOT-23 T+R
ISL43121	Switch	Dual NC	SPST	11	25	17	5	0.01	8	21	0.05μ	+2.7 to +12	8 Ld SOT-23 T+R
ISL43122	Switch	Dual MIX	SPST	11	25	17	5	0.01	8	21	0.05μ	+2.7 to +12	8 Ld SOT-23 T+R
ISL5120	Switch	Dual NO	SPST	11	25	17	5	0.2	8	21	1μ (Max)	+2.7 to +12	8 Ld SOIC, 8 Ld SOT-23 T+R
ISL5121	Switch	Dual NC	SPST	11	25	17	5	0.2	8	21	1μ (Max)	+2.7 to +12	8 Ld SOIC, 8 Ld SOT-23 T+R
ISL5122	Switch	Dual MIX	SPST	11	25	17	5	0.2	8	21	1μ (Max)	+2.7 to +12	8 Ld SOIC, 8 Ld SOT-23

Dual (Continued)

Device	Switch or MUX	Configuration	Type of Switch	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
ISL8323	Switch	Dual NO	SPST	60 (Max)	150 (Max)	100 (Max)	5	0.2	9	22	0.1n	+2.7 to +12	8 Ld SOIC
ISL8324	Switch	Dual NC	SPST	60 (Max)	150 (Max)	100 (Max)	5	0.2	9	22	0.1n	+2.7 to +12	8 Ld SOIC
ISL8325	Switch	Dual MIX	SPST	60 (Max)	150 (Max)	100 (Max)	5	0.2	9	22	0.1n	+2.7 to +12	8 Ld SOIC
ISL84541	Switch	Dual NO	SPST	30	35	25	1	0.2	8	13	0.1n	+2.7 to +12	8 Ld MSOP, 8 Ld PDIP, 8 Ld SOIC, 8 Ld SOT-23 T+R
ISL84542	Switch	Dual NC	SPST	30	35	25	1	0.2	8	13	0.1n	+2.7 to +12	8 Ld PDIP, 8 Ld SOIC, 8 Ld SOT-23 T+R
ISL84543	Switch	Dual MIX	SPST	30	35	25	1	0.2	8	13	0.1n	+2.7 to +12	8 Ld PDIP, 8 Ld SOIC, 8 Ld SOT-23 T+R

Quad

Device	Switch or MUX	Configuration	Type of Switch	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
ISL43140	Switch	Quad NC	SPST	50	40	15	1	0.01	7	14	0.05μ	+2 to +12, ±2 to ±6	16 Ld QFN, 16 Ld SOIC, 16 Ld TSSOP
ISL43141	Switch	Quad NO	SPST	50	40	15	1	0.01	7	14	0.05μ	+2 to +12, ±2 to ±6	16 Ld QFN, 16 Ld SOIC, 16 Ld TSSOP
ISL43142	Switch	Quad MIX	SPST	50	40	15	1	0.01	7	14	0.05μ	+2 to +12, ±2 to ±6	16 Ld QFN, 16 Ld SOIC, 16 Ld TSSOP
ISL43143	Switch	Quad NC	SPST	18	52	40	5	0.2	10	34	0.01μ	+2 to +12, ±2 to ±6	16 Ld QFN, 16 Ld TSSOP
ISL43144	Switch	Quad NO	SPST	18	52	40	5	0.2	10	34	0.01μ	+2 to +12, ±2 to ±6	16 Ld QFN, 16 Ld TSSOP
ISL43145	Switch	Quad MIX	SPST	18	52	40	5	0.2	10	34	0.01μ	+2 to +12, ±2 to ±6	16 Ld QFN, 16 Ld TSSOP
ISL8391	Switch	Quad NC	SPST	20	60	30	5	0.2	12	34	0.01μ	+2 to +12, ±2 to ±6	16 Ld SOIC
ISL8392	Switch	Quad NO	SPST	20	60	30	5	0.2	12	34	0.01μ	+2 to +12, ±2 to ±6	16 Ld SOIC
ISL8393	Switch	Quad MIX	SPST	20	60	30	5	0.2	12	34	0.01μ	+2 to +12, ±2 to ±6	16 Ld SOIC
ISL84521	Switch	Quad NC	SPST	65	45	15	1	0.01	2	5	50n	+2 to +12, ±2 to ±6	16 Ld SOIC, 16 Ld TSSOP
ISL84522	Switch	Quad NO	SPST	65	45	15	1	0.01	2	5	50n	+2 to +12, ±2 to ±6	16 Ld SOIC, 16 Ld TSSOP
ISL84523	Switch	Quad MIX	SPST	65	45	15	1	0.01	2	5	50n	+2 to +12, ±2 to ±6	16 Ld SOIC, 16 Ld TSSOP

## Switch/MUXes

### Single

Device	Switch or MUX	Configuration	Type of Switch	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
ISL43210	Switch/MUX	Single 2x1	SPDT	11	25	17	5	0.01	8	28	0.05μ	+2.7 to +12	6 Ld SOT-23 T+R
ISL43410	Switch/MUX	Diff 2x1	DPDT	45	25	24	3	1	4	12	0.1n	+2 to +12	10 Ld MSOP, 16 Ld QFN
ISL5123	Switch/MUX	Single 2x1	SPDT	11	25	17	5	0.2	8	28	1μ (Max)	+2.7 to +12	6 Ld SOT-23 T+R, 8 Ld SOIC
ISL84525	Switch/MUX	Diff 2x1	DPDT	92	90	40	0.8	1	6	12	1μ (Max)	+2 to +12	10 Ld MSOP
ISL84544	Switch/MUX	Single 2x1	SPDT	30	35	25	1	0.2	8	20	0.1n	+2.7 to +12	6 Ld SOT-23 T+R, 8 Ld PDIP, 8 Ld SOIC

### Triple

Device	Switch or MUX	Configuration	Type of Switch	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
ISL43231	Switch/MUX	Triple 2x1	SPDT	44	35	22	0.3	0.002	3	14	7μ	+2 to +12, ±2 to ±6	20 Ld QFN
ISL84053	Switch/MUX	Triple 2x1	SPDT	60	50	40	2	0.002	3	14	0.1μ	+2 to +12, ±2 to ±6	16 Ld QSOP, 16 Ld SOIC

### Quad

Device	Switch or MUX	Configuration	Type of Switch	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
ISL43240	Switch/MUX	Quad 2x1	SPDT	18	52	40	5	0.4	10	30	0.01μ	+2 to +12, ±2 to ±6	20 Ld QFN, 20 Ld SSOP
ISL8394	Switch/MUX	Quad 2x1	SPDT	17	50	30	5	0.4	12	39	0.01μ	+2 to +12, ±2 to ±6	20 Ld SOIC

## MUXes

### Single

Device	Switch or MUX	Configuration	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
ISL43681	MUX	Single 8x1	39	32	18	0.3	0.002	3	26	7μ	+2 to +12, ±2 to ±6	20 Ld QFN
ISL84051	MUX	Single 8x1	60	50	40	2	0.002	3	26	0.1μ	+2 to +12, ±2 to ±6	16 Ld QSOP, 16 Ld SOIC
ISL43640	Switch/MUX	Single 4x1	45	25	24	1.2	1	4	20	0.1n	+2 to +12	10 Ld MSOP, 16 Ld QFN
ISL84524	Switch/MUX	Single 4x1	92	90	40	0.8	1	14	20	1μ (Max)	+2 to +12	10 Ld MSOP

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Dual

Device	Switch or MUX	Configuration	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
ISL43741	MUX	Diff 4x1	39	32	18	0.3	0.002	3	18	7μ	+2 to +12, ±2 to ±6	20 Ld OFN
ISL84052	MUX	Diff 4x1	60	50	40	2	0.002	3	18	0.1μ	+2 to +12, ±2 to ±6	16 Ld OSOP, 16 Ld SOIC
ISL43840	MUX	Dual 4x1	39	32	18	0.3	0.002	3	18	0.1μ	+2 to +12, ±2 to ±6	20 Ld QFN
ISL43841	MUX	Dual 4x1	39	32	18	0.3	0.002	3	18	0.1μ	+2 to +12, ±2 to ±6	20 Ld OFN
ISL84582	MUX	Diff 4x1	44	35	22	0.3	0.002	3	18	2μ	+2 to +12, ±2 to ±6	16 Ld TSSOP
ISL43741	MUX	Diff 4x1	39	32	18	0.3	0.002	3	18	0.1μ	+2 to +12, ±2 to ±6	20 Ld QFN
ISL84052	MUX	Diff 4x1	60	50	40	2	0.002	3	18	0.1μ	+2 to +12, ±2 to ±6	16 Ld OSOP, 16 Ld SOIC

High Voltage (5V-34V)

Switches

Single

Device	Switch or MUX	Configuration	Type of Switch	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
HI-5042	Switch	Single MIX	DPST	50	370	280	50	0.8	11	22	0.1m	±5 (Typ)	16 Ld CerDIP
HI-5047	Switch	Single NO	4PST	50	370	280	50	0.8	11	22	0.1m	±15 (Typ)	16 Ld CerDIP

Dual

Device	Switch or MUX	Configuration	Type of Switch	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
DG401	Switch	Dual NO	SPST	20	100	60	60	0.04	12	39	0.01μ	+5 to +34, ±5 to ±20	16 Ld SOIC, 16 Ld TSSOP
HI-200	Switch	Dual NC	SPST	55	240	500	35	1	5.5	11	0.5m	±15 (Typ)	14 Ld PDIP
HI-303	Switch	Dual MIX	DPST	35	210	160	30	0.04	16	35	0.09m	+5 to +34, ±5 to ±20	14 Ld CerDIP, 14 Ld PDIP, 14 Ld SOIC
DG403	Switch	Dual MIX	DPST	20	100	60	60	0.04	12	39	0.01μ	+5 to +34, ±5 to ±20	16 Ld SOIC, 16 Ld TSSOP
HI-390	Switch	Dual MIX	DPST	35	210	160	30	0.04	16	35	0.09m	+5 to +34, ±5 to ±20	16 Ld CerDIP
HI-5043	Switch	Dual MIX	DPST	50	370	280	50	0.8	11	22	0.1m	±15 (Typ)	16 Ld CerDIP, 16 Ld PDIP, 16 Ld SOIC
HI-5049	Switch	Dual NO	DPST	25	370	280	50	0.8	11	22	0.1m	±15 (Typ)	16 Ld CerDIP
HI-5051	Switch	Dual MIX	DPST	25	370	280	50	0.8	11	22	0.1m	±15 (Typ)	16 Ld CerDIP, 16 Ld PDIP, 16 Ld SOIC

**Quad**

Device	Switch or MUX	Configuration	Type of Switch	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
DG411	Switch	Quad NC	SPST	25	110	100	5	0.1	9	35	0.1n	+5 to +34, ±5 to ±20	16 Ld PDIP, 16 Ld SOIC, 16 Ld TSSOP
DG412	Switch	Quad NO	SPST	25	110	100	5	0.1	9	35	0.1n	+5 to +34, ±5 to ±20	16 Ld PDIP, 16 Ld SOIC, 16 Ld TSSOP
DG413	Switch	Quad MIX	SPST	25	110	100	5	0.1	9	35	0.1n	+5 to +34, ±5 to ±20	16 Ld PDIP, 16 Ld SOIC, 16 Ld TSSOP
DG441	Switch	Quad NC	SPST	50	150	90	1	0.08	4	16	15μ	+5 to +34, ±5 to ±20	16 Ld PDIP, 16 Ld SOIC, 16 Ld TSSOP
DG442	Switch	Quad NO	SPST	50	150	110	1	0.08	4	16	15μ	+5 to +34, ±5 to ±20	16 Ld PDIP, 16 Ld SOIC, 16 Ld TSSOP
DG444	Switch	Quad NC	SPST	50	120	110	1	0.08	4	16	1n	+5 to +34, ±5 to ±20	16 Ld SOIC
DG445	Switch	Quad NO	SPST	50	120	160	1	0.08	4	16	1n	+5 to +34, ±5 to ±20	16 Ld SOIC
HI-201	Switch	Quad NC	SPST	55	185	220	10	2	5.5	11	0.5m	±15 (Typ)	16 Ld CerDIP, 16 Ld PDIP, 16 Ld SOIC, 20 Ld PLCC
HI-201HS	Switch	Quad NC	SPST	30	30	40	10	0.3	10	30	4.5m	±15 (Typ)	16 Ld CerDIP, 16 Ld PDIP, 16 Ld SOIC

**MUXes**

Device	Switch or MUX	Configuration	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
DG406	MUX	Single 16x1	50	150	70	40	0.04	8	180	80μ	+5 to +34, ±5 to ±20	28 Ld PDIP, 28 Ld SOIC
DG407	MUX	Diff 8x1	50	150	70	40	0.04	8	90	80μ	+5 to +34, ±5 to ±20	28 Ld PDIP, 28 Ld SOIC
DG408	MUX	Single 8x1	40	115	105	20	1	3	37	0.2m	+5 to +34, ±5 to ±20	16 Ld CerDIP, 16 Ld PDIP, 16 Ld SOIC, 16 Ld TSSOP
DG409	MUX	Diff 4x1	40	115	105	20	1	3	25	0.2m	+5 to +34, ±5 to ±20	16 Ld PDIP, 16 Ld SOIC, 16 Ld TSSOP
HI-524	MUX	Single 4x1	700	180	180		0.7	4	10	12m (Max)	±15 (Typ)	18 Ld CerDIP
HI-1818A	MUX	Single 8x1	250	300	300	7	250 (Max)	4	20	1m (Max)	±15 (Typ)	16 Ld CerDIP
HI-506	MUX	Single 16x1	180	250	250	15	0.3	10	52	1.5m	±15 (Typ)	28 Ld CerDIP, 28 Ld PLCC, 28 Ld SOIC
HI-507	MUX	Diff 8x1	180	250	250	15	0.3	10	30	1.5m	±15 (Typ)	28 Ld CerDIP, 28 Ld PDIP
HI-508	MUX	Single 8x1	180	250	250	15	0.3	10	17	1.5m	±15 (Typ)	16 Ld CerDIP, 16 Ld PDIP, 16 Ld SOIC, 20 Ld LCC
HI-509	MUX	Diff 4x1	180	250	250	15	0.3	10	12	1.5m	±15 (Typ)	16 Ld CerDIP, 16 Ld PDIP, 16 Ld SOIC, 20 Ld PLCC
HI-516	MUX	Single 16x1, Diff 8x1 or Dual 8x1	620	120	140	2	0.04	10	25	30m (Max)	±15 (Typ)	28 Ld PDIP
HI-518	MUX	Single 8x1, Diff 4x1, or Dual 4x1	480	120	140	2.5	0.015	5	10	18m (Max)	±15 (Typ)	18 Ld PDIP
HI-539	MUX	Diff 4x1	650	250	160	3	0.15	5	17	0.15m	±5 to ±18	16 Ld CerDIP

### Overvoltage Protected Multiplexer

Device	Switch or MUX	Configuration	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>CC</sub> (A)	V <sub>CC</sub> Range (±V)	Package
HI-506A	MUX	Single 16x1	1200	300	300	4	0.1	10	52	0.5m	±15 (Typ)	28 Ld CerDIP, 28 Ld PDIP
HI-507A	MUX	Diff 8x1	1200	300	300	4	0.1	10	30	0.5m	±15 (Typ)	28 Ld PDIP
HI-508A	MUX	Single 8x1	1200	300	300	4	0.1	10	25	0.5m	±15 (Typ)	16 Ld CerDIP, 16 Ld PDIP
HI-509A	MUX	Diff 4x1	1200	300	300	4	0.1	10	12	0.5m	±15 (Typ)	16 Ld CerDIP, 16 Ld PDIP
HI-546	MUX	Single 16x1	1200	300	300	4	0.1	10	52	0.5m	±5 to ±18	28 Ld CerDIP, 28 Ld PDIP, 28 Ld PLCC, 28 Ld SOIC
HI-547	MUX	Diff 8x1	1200	300	300	4	0.1	10	30	0.5m	±5 to ±18	28 Ld CerDIP, 28 Ld PDIP, 28 Ld PLCC, 28 Ld SOIC
HI-548	MUX	Single 8x1	1200	300	300	4	0.1	10	25	0.5m	±5 to ±18	16 Ld CerDIP, 16 Ld PDIP, 16 Ld SOIC, 20 Ld PLCC
HI-549	MUX	Diff 4x1	1200	300	300	4	0.1	10	12	0.5m	±5 to ±18	16 Ld CerDIP, 16 Ld PDIP, 16 Ld SOIC, 20 Ld PLCC

### Video MUXes with Integrated Op Amps

### Wideband MUXes with Integrated Op Amps

Device	Configuration	BW @ -3dB (MHz)	Slew Rate (V/μs)	I <sub>S</sub> (mA)	Gain A <sub>V</sub> (min) (V)	I <sub>OUT</sub> (mA)	Diff Gain (%)	Diff Phase (°)	Package
ISL59420	2 to 1	420	966	11	1	130	0.01	0.02	10 Ld MSOP
ISL59421	2 to 1	865	1417	14.5	1	130	0.01	0.02	10 Ld MSOP
ISL59440	4 to 1	1100	1063	14.5	1	130	0.01	0.02	16 Ld QSOP
ISL59441	4 to 1	900	1349	17	1	130	0.01	0.02	16 Ld QSOP
ISL59442	4 to 1	1000	1452	18	1	130	0.01	0.02	14 Ld SOIC
ISL59444	4 to 1	1000	1515	18	1	120	0.01	0.02	16 Ld SOIC
HA4314B	4 to 1	400	1400	10.5	1	20	0.01	0.01	14 Ld PDIP, 14 Ld SOIC, 16 Ld QSOP
HA4344B	4 to 1	350	1400	10.5	1	20	0.01	0.01	16 Ld SOIC
HA4404B	4 to 1	330	1250	10.5	1	20	0.01	0.01	16 Ld SOIC T+R
ISL59481	Dual, Triple 4 to 1	500	870	92	1	135	0.02	0.02	48 Ld QFN
ISL59482	Dual, Triple 4 to 1	500	1600	16	2	135	0.01	0.04	48 Ld QFN
EL4340	Triple 2 to 1	500	870	30	1	135	0.02	0.02	24 Ld QSOP
EL4332	Triple 2 to 1	300	650		2	40	0.04	0.08	16 Ld SOIC
ISL59424	Triple 2 to 1	1000	1200	39	1	130	0.02	0.02	24 Ld QFN
ISL59448	Triple 2 to 1	500	1600	30	2	135	0.02	0.02	24 Ld QSOP
EL4342	Triple 4 to 1	500	870	46	1	135	0.02	0.02	32 Ld QFN
ISL59445	Triple 4 to 1	1000	1200	53	1	130	0.02	0.02	32 Ld QFN
ISL59446	Triple 4 to 1	500	1600	46	2	135	0.02	0.02	24 Ld QFN

## Video Crosspoints

### Buffered

Device	Configuration	Input Type	BW @ -3dB (MHz)	Slew Rate (V/ $\mu$ s)	I <sub>S</sub> (mA)	Gain A <sub>V</sub> (min) (V)	I <sub>OUT</sub> (mA)	Diff Gain (%)	Diff Phase (°)	Package
EL4544	Triple 16x4	Differential	300	1000	400	1 to 4	60	0.05	0.05	356 Ld BGA
HA456	8x8	Single-Ended	120	200	68	1		0.05	0.05	44 Ld PLCC
ISL59532	32x32	Singled-Ended	320	520	600	1 or 2	100	0.025	0.05	356 Ld BGA
ISL59533	32x32	Differential	320	520	600	1 or 2	100	0.025	0.05	356 Ld BGA
ISL59534	32x16	Singled-Ended	320	520	600	1 or 2	100	0.025	0.05	356 Ld BGA
ISL59535	32x16	Differential	320	520	600	1 or 2	100	0.025	0.05	356 Ld BGA
ISL59530	16x16	Singled-Ended	320	520	140	1 or 2	100	0.025	0.05	356 Ld BGA
ISL59531	16x16	Differential	320	520	140	1 or 2	100	0.025	0.05	356 Ld BGA

### Unbuffered

Device	Configuration	r <sub>DS(ON)</sub> ( $\Omega$ )	Freq. Response Typ. -3dB 14V (MHz)	Crosstalk Typ. -40dB 14V (MHz)	V <sub>CC</sub> Range ( $\pm$ V)	Package
CD22M3494	16x8	36	50	10	4 to 15	40 Ld PDIP, 44 Ld PLCC

### DVI/HDMI

Device	Number of Channels	Programmable Pre-Emphasis	Programmable Equalization	Topology	Input Termination ( $\Omega$ )	Maximum Pixel Rate (MHz)	Supply Voltage (V)	Package
ISL54102	4	Y	Y	TMDS Regenerator	50, 100 or disconnected	165	3.3	128 Ld QFP
ISL54100	1	Y	Y	TMDS Regenerator	50, 100 or disconnected	165	3.3	128 Ld QFP
ISL54101	2	Y	Y	TMDS Regenerator	50, 100 or disconnected	165	3.3	128 Ld QFP