



\*For a complete listing of Amplifiers/Buffers, refer to Section 1.

\*For a complete listing of Switches/MUXes/Crosspoints, refer to Section 13.

## Analog Crosspoint Switches\*

### Buffered Crosspoint Switches

Device	Configuration	Input Type	BW (MHz)	SR (V/ $\mu$ s)	I <sub>S</sub> (mA)	Gain A <sub>V</sub> (min)	I <sub>OUT</sub> (mA)	Diff Gain (%)	Diff Phase (°)	Package
HA456	8x8	Single-Ended	120	200	68	1		0.05	0.05	44 Ld PLCC
EL4544	Triple 16x4	Differential	300	1000	400	1 to 4	60	0.05	0.05	356 Ld BGA
ISL59532	32x32	Singled-Ended	320	520	600	1 or 2	100	0.025	0.05	356 Ld BGA
ISL59534	32x16	Singled-Ended	320	520	600	1 or 2	100	0.025	0.05	356 Ld BGA
ISL59530	16x16	Singled-Ended	320	520	140	1 or 2	100	0.025	0.05	356 Ld BGA

### Unbuffered Crosspoint Switches

Device	Configuration	r <sub>DS(ON)</sub> ( $\Omega$ )	Frequency Response Typ. -3dB 14V (MHz)	Crosstalk Typ. -40dB 14V (MHz)	V <sub>CC</sub> Range ( $\pm$ V)	Package
CD22M3494	16x8	36	50	10	4 to 15	40 Ld PDIP, 44 Ld PLCC

\*For a complete listing of Switch/MUXes/Crosspoints, refer to Section 13.

## Analog Front Ends

Device	Resolution (Bits)	Max Conversion Rate (MSPS)	Typical PLL Jitter (ps) (p-p)	Sync Measurement	Auto Phase Adjust	Typical Power Dissipation @ Max Conversion Rate (mW)	Package
ISL98001-140	8	140	250	N	N	950	128 Ld MQFP
ISL98001-170	8	170	250	N	N	1050	128 Ld MQFP
ISL98001-210	8	210	250	N	N	1100	128 Ld MQFP
ISL98001-240	8	240	250	N	N	1150	128 Ld MQFP
ISL98001-275	8	275	250	N	N	1200	128 Ld MQFP
ISL98002-140	8	140	250	N	N	535	72 Ld QFN
ISL98002-170	8	170	250	N	N	535	72 Ld QFN
X98014	8	140	250	N	N	990	128 Ld MQFP
X98017	8	170	250	N	N	1030	128 Ld MQFP
X98021	8	210	250	N	N	1090	128 Ld MQFP
X98024	8	240	250	N	N	1150	128 Ld MQFP
X98027	8	275	250	N	N	1180	128 Ld MQFP
ISL51002-110	10	110	250	Y	Y	940	128 Ld MQFP
ISL51002-150	10	150	250	Y	Y	970	128 Ld MQFP
ISL51002-165	10	165	250	Y	Y	980	128 Ld MQFP

CAT-5 Video Line

Differential Drivers and Receivers

Device	iSim	BW (MHz)	SR (V/μs)	V <sub>S</sub> (min) (V)	V <sub>S</sub> (max) (V)	I <sub>S</sub> (per amp) (mA)	Noise V <sub>N</sub> (nV/√Hz)	I <sub>BIAS</sub> (μA)	V <sub>OS</sub> (max) (mV)	# of Devices/ Channels	I <sub>OUT</sub> (mA)	V <sub>OUT</sub> (V)	Rail-to-Rail		PSRR (dB)	CMRR (dB)	Gain A <sub>V</sub> (min)	Diff Gain (%)	Diff Phase (°)	Enable	Package
													Input	Output							
EL5170		100	1100	5	10	7.4	30	6	25	1	85	6.9	N	N	83	84	2	0.1	0.1	Y	8 Ld MSOP, 8 Ld SOIC
EL5370		100	1100	5	10	7	30	6	25	3	85	6.9	N	N	83	84	2	0.1	0.1	Y	24 Ld QSOP
EL5171		250	800	5	10	8	26	6	25	1	90	6.8	N	N	84	82	1	0.1	0.5	N	8 Ld SOIC
EL5172		250	800	5	10	6	26	6	25	1	95	7.5	N	N	58	95	1	0.04	0.4	Y	8 Ld MSOP, 8 Ld SOIC
EL5176		250	800	4.75	11	7.5	26	6	2.5	1	40	7.8	N	N	84	82	1	0.1	0.5	Y	10 Ld MSOP
EL5371		250	800	5	10	8	26	6	25	3	90	6.8	N	N	84	82	1	0.1	0.5	N	28 Ld QSOP
EL5372		250	800	5	10	6	26	6	25	3	95	7.5	N	N	58	95	1	0.04	0.4	Y	24 Ld QSOP
EL4543		350	1200	5	12	14.5	27	15	10	3	60		N	N	80	80	2	0.01	0.01	Y	20 Ld QFN, 24 Ld QSOP
EL5173		450	900	5	11	12	25	11	30	1	55	7.7	N	N	73	80	2	0.06	0.13	Y	8 Ld MSOP, 8 Ld SOIC
EL5373		450	1100	4.75	11	12	25	13	30	3	55	7.7	N	N	73	80	2	0.06	0.13	Y	24 Ld QSOP
EL5174		550	1100	4.75	11	12.5	21	14	25	1	60	7.6	N	N	75	78	1	0.06	0.13	N	8 Ld SOIC
EL5175	Y	550	600	4.75	11	9.6	21	12.5	40	1	67	7.49	N	N	56	95	1	0.1	0.1	Y	8 Ld MSOP, 8 Ld SOIC
EL5177		550	1100	4.75	11	12.5	21	14	25	1	50	7.6	N	N	75	78	1	0.06	0.13	Y	10 Ld MSOP
EL5374		550	1100	4.75	11	12.5	21	14	25	3	60	7.6	N	N	75	78	1	0.06	0.13	Y	28 Ld QSOP
EL5375		550	900	4.75	11	9.6	21	12.5	30	3	67	7.49	N	N	56	95	1	0.1	0.1	Y	24 Ld QSOP
ISL59311		650	1500	4.5	5.5	13	15	6	1	3	60	4.4	N	N	75	75	2	0.01	0.01	Y	32 Ld QFN
EL5178		700	850	4.75	11	12.5	18	14	30	1	100	7.4	N	N	75	78	2	0.06	0.13	N	8 Ld MSOP, 8 Ld SOIC
EL5378		700	1000	4.75	11	12.5	18	14	30	3	60	7.4	N	N	75	78	2	0.06	0.13	Y	28 Ld QSOP

Frequency Compensation

Device	BW (MHz)	SR (V/μs)	V <sub>S</sub> (min) (V)	V <sub>S</sub> (max) (V)	I <sub>S</sub> (per amp) (mA)	I <sub>BIAS</sub> (μA)	V <sub>OS</sub> (max) (mV)	# of Devices/ Channels	I <sub>OUT</sub> (mA)	V <sub>OUT</sub> (V)	Rail-to-Rail		PSRR (dB)	CMRR (dB)	THD (dB)	Gain A <sub>V</sub> (min)	Enable	Package
											Input	Output						
EL9110	150	1500	10	12	33	1	250	1	60	7	N	N	60	60	50	1	Y	16 Ld QSOP

Device	# of Devices/ Channels	SR (V/μs)	V <sub>S</sub> (min) (V)	V <sub>S</sub> (max) (V)	BW (MHz)	Max Equalization (dB)	Gain A <sub>V</sub> (min) (V)	I <sub>S</sub> (per amp) (mA)	I <sub>BIAS</sub> (μA)	I <sub>OUT</sub> (mA)	V <sub>OUT</sub> (V)	V <sub>OS</sub> (max) (mV)	CMRR (dB)	PSRR (dB)	Package
EL9111	3	1200	±5	±6	150	45	1	36	1	60	±3.5	78	80	65	28 Ld QFN
EL9112	3	1200	±5	±6	150	45	1	36	1	60	±3.5	78	80	65	28 Ld QFN

## Analog Delay Line Skew Compensation

Device	# of Channels	SR (V/μs)	V <sub>S</sub> (min) (V)	V <sub>S</sub> (max) (V)	BW (MHz)	Delay Increment (ns)	Max Delay (ns)	Gain A <sub>V</sub> (min) (V)	I <sub>S</sub> (per amp) (mA)	I <sub>BIAS</sub> (μA)	I <sub>OUT</sub> (mA)	PSRR (dB)	Package
EL9115	3	400	±5	±6	122	2	62	2	87	1	30	53	20 Ld QFN

## DCC Accelerators

Device	V <sub>TRIP1</sub> (V)	V <sub>TRIPH</sub> (V)	Standby Pull-Up Current (μA)	Active Pull-Up Current (μA)	Boost Pull-Up Current (mA)	V <sub>S</sub> Range (V)	Package
ISL54103	0.75	V <sub>DD</sub> -0.50	80	275	2.2	2.7 to 5.5	5 Ld SOT-23 T+R

## DVI/HDMI

Device	# of Devices/ Channels	Programmable Pre-Emphasis	Programmable Equalization	Topology	Internal Input Termination (Ω)	Maximum Pixel Rate (MHz)	V <sub>S</sub> (V)	Package
ISL54102	2	Y	Y	TMDS Regenerator	50, 100 or Disconnected	165	3.3	128 Ld QFP
ISL54100	4	Y	Y	TMDS Regenerator	50, 100 or Disconnected	165	3.3	128 Ld QFP
ISL54101	1	Y	Y	TMDS Regenerator	50, 100 or Disconnected	165	3.3	128 Ld QFP

Device	Switch or MUX	Configuration	r <sub>DS(ON)</sub> (Ω)	T <sub>(ON)</sub> (ns)	T <sub>(OFF)</sub> (ns)	CHG INJ (pC)	Leakage (nA)	SRC Cap (pF)	DRN Cap (ON) (pF)	I <sub>S</sub> (A)	V <sub>CC</sub> Range (±V)	Package
ISL43640	Switch/MUX	Single 4x1	45	25	24	1.2	1	4	20	0.1n	+2 to +12	10 Ld MSOP, 16 Ld QFN
ISL43840	MUX	Dual 4x1	30	32	18	0.3	0.002	3	18	0.1μ	+2 to +12, ±2 to ±6	20 Ld QFN
ISL43841	MUX	Dual 4x1	39	32	18	0.3	0.002	3	18	0.1μ	+2 to +12, ±2 to ±6	20 Ld QFN
ISL43741	MUX	Diff 4x1	39	32	18	0.3	0.002	3	18	0.1μ	+2 to +12, ±2 to ±6	20 Ld QFN
ISL43681	MUX	Single 8x1	39	32	18	0.3	0.002	3	26	7μ	+2 to +12, ±2 to ±6	20 Ld QFN

## Buffered Video MUXes

Device	Configuration	BW (MHz)	SR (V/μs)	I <sub>S</sub> (mA)	Gain A <sub>V</sub> (min)	I <sub>OUT</sub> (mA)	Diff Gain (%)	Diff Phase (°)	Package
ISL59451	4 to 1	240	500	45	1	125	0.0025	0.08	32 Ld QFN
ISL59452	4 to 1	250	480	45	1	125	0.0013	0.035	32 Ld QFN
ISL59450	Single 6 to 2, Dual 4 to 2 and Triple 4 to 2	275	590	293.5	1	140	0.3	0.45	128 Ld MQFP
EL4332	Triple 2 to 1	300	650		2	40	0.04	0.08	16 Ld SOIC
HA4404B	4 to 1	330	1250	10.5	1	20	0.01	0.01	16 Ld SOIC T+R
HA4344B	4 to 1	350	1400	10.5	1	20	0.01	0.01	16 Ld SOIC
HA4314B	4 to 1	400	1400	10.5	1	20	0.01	0.01	14 Ld PDIP, 14 Ld SOIC, 16 Ld QSOP
ISL59440	4 to 1	411	1053	14.5	1	130	0.01	0.02	16 Ld QSOP
ISL59420	2 to 1	420	966	11	1	130	0.01	0.02	10 Ld MSOP
ISL59481	Dual, Triple 4 to 1	500	870	92	1	135	0.02	0.02	48 Ld QFN

Buffered Video MUXes (Continued)

Device	Configuration	BW (MHz)	SR (V/μs)	I <sub>S</sub> (mA)	Gain A <sub>V</sub> (min)	I <sub>OUT</sub> (mA)	Diff Gain (%)	Diff Phase (°)	Package
ISL59482	Dual, Triple 4 to 1	500	1600	16	2	135	0.01	0.04	48 Ld QFN
EL4340	Triple 2 to 1	500	870	30	1	135	0.02	0.02	24 Ld QSOP
ISL59448	Triple 2 to 1	500	1600	30	2	135	0.02	0.02	24 Ld QSOP
EL4342	Triple 4 to 1	500	870	46	1	135	0.02	0.02	32 Ld QFN
ISL59446	Triple 4 to 1	500	1600	46	2	135	0.02	0.02	32 Ld QFN
ISL59421	2 to 1	865	1417	14.5	1	130	0.01	0.02	10 Ld MSOP
ISL59441	4 to 1	900	1349	17	1	130	0.01	0.02	16 Ld QSOP
ISL59442	4 to 1	1000	1452	18	1	130	0.01	0.02	14 Ld SOIC
ISL59444	4 to 1	1000	1515	18	1	120	0.01	0.02	16 Ld SOIC
ISL59424	Triple 2 to 1	1000	1200	39	1	130	0.02	0.02	24 Ld QFN
ISL59445	Triple 4 to 1	1000	1200	53	1	130	0.02	0.02	32 Ld QFN

Video DC-Restore Amplifiers

Device	Channels	3dB Bandwidth (MHz)	V <sub>S</sub> Range (V)	I <sub>S</sub> (mA)	Restored Offset (mV)	Diff Gain (%)	Diff Phase (°)	Package
ISL4089	1	300	5	20	10	0.03	0.05	8 Ld SOIC
EL4093	1	300	+5 to ±6	9.5	1.5	0.04	0.02	16 Ld SOIC

Video Filters

Device	BW (MHz)	SR (V/μs)	V <sub>S</sub> (min) (V)	V <sub>S</sub> (max) (V)	I <sub>S</sub> (per amp) (mA)	# of Devices/ Channels	I <sub>OUT</sub> (mA)	V <sub>OUT</sub> (V)	Rail-to-Rail		PSRR (dB)	Thermal Shutdown	Total Harmonic Distortion (dB)	Gain A <sub>V</sub> (min)	Diff Gain (%)	Diff Phase (°)	Enable	Package
									Input	Output								
ISL59110	8	40	2.5	3.6	2	1	115	3.2	N	Y	90	N	56	2	0.1	0.05	Y	6 Ld SC-70
ISL59111	8	40	2.5	3.6	2	1	115	3.2	N	Y	90	N	56	2	0.1	0.05	Y	6 Ld CSP

Device	Input Channels	Output Channels	-3dB Filter Cut-Off (MHz)	V <sub>S</sub> Range (V)	I <sub>S</sub> (per amp) (mA)	Rail-to-Rail	Gain A <sub>V</sub> (min)	Diff Gain (%)	Diff Phase (°)	Enable	Package
ISL59114	2	3	9	+2.5 to +3.6	1.5	Y	2	0.1	0.5	Y	10 Ld μTOFN
ISL59115	3	3	9	+2.5 to +3.6	1.5	Y	2	0.1	0.5	Y	10 Ld μTOFN
ISL59116	2	3	9	+2.5 to +3.6	1.5	Y	2	0.1	0.5	Y	9 Ld CSP T+R
ISL59117	3	3	9	+2.5 to +3.6	1.5	Y	2	0.1	0.5	Y	9 Ld CSP T+R
ISL59118	2	2	9	+2.5 to +3.6	2.3	Y	2	0.1	0.5	Y	10 Ld QFN T+R
ISL59119	3	3	8	+3.0 to +3.6	2.8	Y	2	0.2	0.5	N	8 Ld SOIC
ISL59123	3	3	18	+2.5 to +3.6	1.3	Y	2	0.1	0.5	Y	9 Ball WLCSP
ISL59832	2	2	9	+3.0 to +3.6	7	Y	2	0.45	0.15	Y	16 Ld TOFN
ISL59834	4	4	9	+3.0 to +3.6	7	Y	2	0.45	0.15	Y	44 Ld QFN

## Video Genlocks

Device	PAL Frequency Clocks per Horizontal Line				NTSC Frequency Clocks per Horizontal Line				Logic Levels	V <sub>CO</sub> Control Range (V)	Supply Current (mA)	Package
	3 F <sub>SC</sub>	CCIR 601	Sq Pixels	4 F <sub>SC</sub>	3 F <sub>SC</sub>	CCIR 601	Sq Pixels	4 F <sub>SC</sub>				
EL4584	851	864	944	1135	682	858	780	910	TTL/CMOS	0 to 5	2	16 Ld PDIP, 16 Ld SOIC

Device	PAL Frequency Clocks per Horizontal Line				NTSC Frequency Clocks per Horizontal Line				Logic Levels	V <sub>CO</sub> Control Range (V)	Supply Current (mA)	Package
	6 F <sub>SC</sub>	CCIR 601	Sq Pixels	8 F <sub>SC</sub>	6 F <sub>SC</sub>	CCIR 601	Sq Pixels	8 F <sub>SC</sub>				
EL4585	1702	1728	1888	2270	1364	1716	1560	1820	TTL/CMOS	0 to 5	2	16 Ld PDIP, 16 Ld SOIC

## Video Sync Separators

Device	Slicing Fixed (70mV)	Slicing Input Adaptive	Color Burst Filter - Internal	Color Burst Filter - External	Outputs Composite	Outputs Horizontal	Outputs Vertical	Outputs Burst	Outputs Odd/Even	Outputs Sync Amplitude	Composite Prop Delay (ns)	Supply Current (mA)	Package
EL1881	X			X	X		X	X	X		30	1.2	8 Ld PDIP, 8 Ld SOIC
EL1883	X			X	X	X	X	X			30	1.2	8 Ld SOIC
EL4501		X	Variable		X	X	X	X	X	X	225	10.5	24 Ld QSOP
EL4511		X	Variable		X	X	X	X	X	X		2.1	24 Ld QSOP
EL4581		X	Fixed		X		X	X	X		260	1.7	8 Ld PDIP, 8 Ld SOIC
EL4583		X	Variable		X	X	X	X	X	X	250	2.5	16 Ld PDIP, 16 Ld SOIC
ISL59885	X		Fixed		X	X	X	X	X		30	10	8 Ld SOIC

## Video Amplifiers\*

### High Speed (>50MHz)

#### Current Feedback

Device	iSim	BW (MHz)	SR (V/μs)	V <sub>S</sub> (min) (V)	V <sub>S</sub> (max) (V)	I <sub>S</sub> (per amp) (mA)	Noise V <sub>N</sub> (nV/√Hz)	I <sub>BIAS</sub> (μA)	V <sub>OS</sub> (max) (mV)	# of Devices/Channels	V <sub>OUT</sub> (V)	Rail-to-Rail		CMRR (dB)	Gain A <sub>V</sub> (min)	Enable	Package
												Input	Output				
ISL59112		40	85	2.5	3.6	2		5		1	3.2	N	N		6	N	6 Ld SC-70
EL2276		70	800	3	12.6	1	5.1	15	15	2	8	N	N	50	1	Y	14 Ld SOIC
HA-5020	Y	100	800	10	30	10	4.5	3	8	1	25.4	N	N	60	1	Y	20 Ld LCC, 8 Ld PDIP, 8 Ld SOIC
HA5023	Y	125	475	9	30	7.5	4.5	4	3	2	6	N	N	53	1	N	8 Ld PDIP, 8 Ld SOIC
HA5024	Y	125	475	9	30	7.5	4.5	4	3	4	6	N	N	53	1	Y	20 Ld PDIP, 20 Ld SOIC
EL5160	Y	200	1700	5	10	0.75	4	5	5	1	6.8	N	N	62	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5161	Y	200	1700	5	10	0.75	4	5	5	1	6.8	N	N	62	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5197	Y	200	2200	5	10	4	4.4	1	10	1	7.4	N	N		1	Y	5 Ld SOT-23
EL5260	Y	200	1700	5	10	0.75	4	5	5	2	6.8	N	N	62	1	Y	10 Ld MSOP

\*For a complete listing of Amplifiers/Buffers, refer to Section 1.

**Current Feedback (Continued)**

Device	iSim	BW (MHz)	SR (V/μs)	V <sub>S</sub> (min) (V)	V <sub>S</sub> (max) (V)	I <sub>S</sub> (per amp) (mA)	Noise V <sub>N</sub> (nV/√Hz)	I <sub>BIAS</sub> (μA)	V <sub>OS</sub> (max) (mV)	# of Devices/ Channels	V <sub>OUT</sub> (V)	Rail-to-Rail		CMRR (dB)	Gain A <sub>V</sub> (min)	Enable	Package
												Input	Output				
EL5261	Y	200	1700	5	10	0.75	4	5	5	2	6.8	N	N	62	1	N	8 Ld MSOP, 8 Ld SOIC
EL5360	Y	200	1700	5	10	0.75	4	5	5	3	6.8	N	N	62	1	Y	16 Ld QSOP, 16 Ld SOIC
EL5397A		200	2100	5	10	4	4.8	1	10	3	7.4	N	N		1	Y	16 Ld QSOP, 16 Ld SOIC
EL2480	Y	250	1200	3	12	3	3	15	10	4	8	N	N	50	1	N	14 Ld SOIC
EL5193	Y	300	2600	5	10	4	4.4	1	10	1	7.4	N	N	50	1	Y	5 Ld SOT-23 T+R, 6 Ld SOT-23 T+R
EL5293	Y	300	2200	5	10	4	4.4	1	10	2	7.4	N	N	50	1	Y	8 Ld MSOP, 8 Ld SOIC
EL5393A	Y	300	2200	5	10	4	4.4	1	10	3	7.4	N	N	50	1	Y	16 Ld QSOP, 16 Ld SOIC
HFA1105		330	1000	9	11	5.8	3.5	2	5	1	6.8	N	N	50	1	N	8 Ld SOIC
HFA1145		330	1000	9	11	5.9	3.5	6	6	1	6.8	N	N	50	1	Y	8 Ld PDIP, 8 Ld SOIC
HFA1412		350	1650	9	11	5.9	7	1	10	4	6.4	N	N	45	1	N	14 Ld PDIP, 14 Ld SOIC
HFA1135		360	1200	9	11	6.9	3.5	6	5	1	6.8	N	N	50	1	N	8 Ld SOIC
HFA1155		360	1650	4.5	11	5.5	4.7	25	6	1	6.6	N	N	46	1	N	5 Ld SOT-23 T+R
EL5396A		400	2600	5	10	9	3.8	40	15	3	7.4	N	N		1	Y	16 Ld QSOP, 16 Ld SOIC
HFA1109	Y	450	1100	9	11	10	4	4	5	1	6.4	N	N	50	1	N	8 Ld SOIC
EL5162	Y	500	4000	5	12	1.5	3	2	5	1	7.2	N	N	62	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5163	Y	500	4000	5	12	1.5	3	2	5	1	7.2	N	N	62	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5262	Y	500	4000	5	12	1.5	3	2	5	2	7.2	N	N	62	1	Y	10 Ld MSOP
EL5263	Y	500	4000	5	12	1.5	3	2	5	2	7.2	N	N	62	1	N	8 Ld MSOP, 8 Ld SOIC
EL5362	Y	500	4000	5	12	1.5	3	2	5	3	7.2	N	N	62	1	Y	16 Ld QSOP, 16 Ld SOIC
EL5462		500	4000	5	12	1.5	3	2	1.5	4	7.2	N	N	62	1	N	14 Ld SOIC
EL5164	Y	600	4700	5	12	5	2.1	2	5	1	7.6	N	N	62	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5165	Y	600	4700	5	12	5	2.1	2	5	1	7.6	N	N	62	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5192	Y	600	2800	5	10	6	4.1	3	10	1	7.4	N	N	50	1	Y	5 Ld SOT-23
EL5292	Y	600	2300	5	10	6	4.1	4	10	2	7.4	N	N	50	1	Y	8 Ld SOIC
EL5364	Y	600	4700	5	12	5	2.1	2	5	3	7.6	N	N	62	1	Y	16 Ld QSOP, 16 Ld SOIC
EL5392A	Y	600	2300	5	10	6	4.1	4	10	3	7.4	N	N	50	1	Y	16 Ld QSOP
EL5192A		600	2800	5	10	6	4.1	3	10	1	7.4	N	N	50	1	Y	6 Ld SOT-23, 8 Ld SOIC

\*For a complete listing of Amplifiers/Buffers, refer to Section 1.

**Current Feedback (Continued)**

Device	iSim	BW (MHz)	SR (V/ $\mu$ s)	V <sub>S</sub> (min) (V)	V <sub>S</sub> (max) (V)	I <sub>S</sub> (per amp) (mA)	Noise V <sub>N</sub> (nV/ $\sqrt$ Hz)	I <sub>BIAS</sub> ( $\mu$ A)	V <sub>OS</sub> (max) (mV)	# of Devices/Channels	V <sub>OUT</sub> (V)	Rail-to-Rail		CMRR (dB)	Gain A <sub>V</sub> (min)	Enable	Package
												Input	Output				
HFA1110		750	1300	9	11	26	14	10	25	1	6.6	N	N	60	1	N	8 Ld SOIC
HFA1100	Y	850	2300	4.5	11	21	4	2.5	6	1	6.6	N	N	46	1	N	8 Ld PDIP, 8 Ld SOIC
HFA1112		850	2400	9	11	21	9	25	25	1	6.6	N	N		1	N	8 Ld PDIP, 8 Ld SOIC
HFA1113		850	2400	9	11	21	9	25	25	1	6.6	N	N		1	N	8 Ld SOIC
HFA1130		850	2300	9	11	21	4	25	6	1	6.6	N	N	46	1	N	20 Ld LCC, 8 Ld SOIC
EL5191		1000	2800	5	10	9	3.8	40	15	1	7.4	N	N	50	1	Y	5 Ld SOT-23 T+R, 8 Ld SOIC
EL5191A		1000	2800	5	10	9	3.8	40	15	1	7.4	N	N	50	1	Y	8 Ld SOIC
EL5367		1000	6000	5	12	8.5	1.7	8.5	5	3	7.6	N	N	57	1	N	16 Ld OSOP
EL5166	Y	1400	6000	5	12	8.5	1.7	8.5	5	1	7.6	N	N	57	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5167	Y	1400	6000	5	12	8.5	1.7	8.5	5	1	7.6	N	N	57	1	N	5 Ld SC-70, 5 Ld SOT-23

\*For a complete listing of Amplifiers/Buffers, refer to Section 1.

**Voltage Feedback**

Device	iSim	BW (MHz)	SR (V/ $\mu$ s)	V <sub>S</sub> (min) (V)	V <sub>S</sub> (max) (V)	I <sub>S</sub> (per amp) (mA)	Noise V <sub>N</sub> (nV/ $\sqrt$ Hz)	I <sub>BIAS</sub> ( $\mu$ A)	V <sub>OS</sub> (max) (mV)	# of Devices/Channels	V <sub>OUT</sub> (V)	Rail-to-Rail		CMRR (dB)	Gain A <sub>V</sub> (min)	Enable	Package
												Input	Output				
HA-5102	Y	8	3	5	18	3	4.3	130	0.5	2	26	N	N	95	1	N	8 Ld Can, 8 Ld CerDIP
HA-5104	Y	8	3	5	18	5	4.3	130	0.5	4	26	N	N	95	1	N	14 Ld CerDIP, 16 Ld SOIC, 20 Ld LCC
HA-5127	Y	8.5	10	10	30	3.5	3	0.015	0.01	1	27	N	N	120	1	N	8 Ld SOIC
HA-5127A	Y	8.5	10	10	30	3.5	3	0.01	0.01	1	27	N	N	126	1	N	8 Ld CerDIP
HA-5137A	Y	63	20	10	40	3.5	3	0.01	0.01	1	27.6	N	N	126	5	N	8 Ld CerDIP
EL2228		80	65	5	24	4.5	4.9	4.5	3	2	20.6	N	N	90	1	N	8 Ld MSOP, 8 Ld SOIC
EL2126	Y	90	110	5	30	4.5	1.3	7	2	1	27.5	N	N	106	10	N	5 Ld SOT-23, 8 Ld SOIC
EL2045	Y	100	275	4	36	5.2	15	2.8	7	1	27.2	N	N	95	2	N	8 Ld PDIP, 8 Ld SOIC
EL5144		100	200	4.75	5.25	7	25	2	25	1	4.85	N	Y	50	1	N	5 Ld SOT-23
EL5146		100	200	4.75	5.25	7	25	2	25	1	4.85	N	Y	50	1	Y	8 Ld PDIP, 8 Ld SOIC
HA-2620	Y	100	35	10	40	3	11	0.001	0.5	1	24	N	N	100	5	N	8 Ld Can
HA-2625	Y	100	35	10	40	3	11	0.005	3	1	24	N	N	100	5	N	8 Ld PDIP, 8 Ld SOIC
HA-5160		100	120	14	36	8	35	0.00002	3	1	22	N	N	80	1	N	8 Ld Can
EL5244		100	200	4.75	5.25	7	25	2	25	2	4.85	N	Y	50	1	N	8 Ld MSOP, 8 Ld PDIP, 8 Ld SOIC

Voltage Feedback (Continued)

Device	iSim	BW (MHz)	SR (V/μs)	V <sub>S</sub> (min) (V)	V <sub>S</sub> (max) (V)	I <sub>S</sub> (per amp) (mA)	Noise V <sub>N</sub> (nV/√Hz)	I <sub>BIAS</sub> (μA)	V <sub>OS</sub> (max) (mV)	# of Devices/Channels	V <sub>OUT</sub> (V)	Rail-to-Rail		CMRR (dB)	Gain A <sub>V</sub> (min)	Enable	Package
												Input	Output				
EL5246		100	200	4.75	5.25	7	25	2	25	2	4.85	N	Y		1	Y	10 Ld MSOP, 14 Ld PDIP, 14 Ld SOIC
EL5370		100	1100	5	10	7	30	6	25	3	6.9	N	N	84	2	Y	24 Ld QSOP
EL5444		100	200	4.75	5.25	7	25	2	15	4	4.85	N	Y	50	1	N	14 Ld PDIP, 14 Ld SOIC, 16 Ld QSOP
EL2227	Y	115	50	5	24	4.8	1.9	3.4	3	2	20.8	N	N	94	2	N	8 Ld MSOP, 8 Ld SOIC
HA-5147		120	35	10	40	3.5	3.2	0.015	0.03	1	27	N	N	120	10	N	8 Ld CerDIP
EL2125	Y	175	185	5	30	10	0.83	22	2	1	27.1	N	N	106	10	N	5 Ld SOT-23, 8 Ld SOIC
EL5100	Y	200	2200	5	10	2.5	10	2	4	1	6.8	N	N	75	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5101	Y	200	2200	5	10	2.5	10	2	4	1	6.8	N	N	75	1	N	SC-70, 5 Ld SOT-23
EL5150	Y	200	67	5	12	1.4	12	0.02	1	1	5.6	N	N	100	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5151	Y	200	67	5	12	1.4	12	0.02	1	1	5.6	N	N	100	1	N	5 Ld SOT-23
EL8100	Y	200	200	3	5	2	10	1.5	6	1	4.8	N	Y	90	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL8101	Y	200	200	3	5	2	10	1.5	6	1	4.8	Y	Y	90	1	N	5 Ld SOT-23
EL5250		200	67	5	12	1.4	12	0.02	1	2	5.6	N	N	100	1	Y	14 Ld MSOP
EL5251	Y	200	67	5	12	1.4	12	0.02	1	2	5.6	N	N	100	1	N	8 Ld MSOP, 8 Ld SOIC
EL8200	Y	200	200	3	5.5	2	10	1.6	6	2	4.8	N	Y	90	1	Y	10 Ld MSOP
EL8201	Y	200	200	3	5.5	2	10	1.6	6	2	4.8	N	Y	90	1	N	8 Ld SOIC
EL5300	Y	200	2200	5	10	2.5	10	2	4	3	6.8	N	N	75	1	Y	16 Ld QSOP
EL8300	Y	200	200	3	5.5	2	10	1.4	5	3	4.83	N	Y	90	1	Y	16 Ld QSOP, 16 Ld SOIC
ISL59833		200	500	3	3.6	32.3	20		25	3	5.1	Y	Y		2	Y	16 Ld QSOP
ISL59837		200	500	3	3.6	32.3	20		25	3	5.1	Y	Y		2	Y	16 Ld QSOP
EL8401	Y	200	200	3	5.5	2	10	1.6	6	4	4.8	N	Y	90	1	N	14 Ld SOIC, 16 Ld QSOP
EL5371		250	800	5	10	8	26	6	25	3	6.8	N	N	82	1	N	28 Ld QSOP
EL5372		250	800	5	10	6	26	6	25	3	7.5	N	N	95	1	Y	24 Ld QSOP
EL5152	Y	270	180	5	12	3	12	0.12	1	1	7.4	N	N	110	1	Y	8 Ld SOIC
EL5153	Y	270	180	5	12	3	12	0.12	1	1	7.4	N	N	110	1	N	5 Ld SOT-23
EL5252	Y	270	180	5	12	3	12	0.11	1	2	7.4	N	N	110	1	Y	10 Ld MSOP
EL5102	Y	400	2200	5	10	5.2	12	2	5	1	7.8	N	N	80	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5103	Y	400	2200	5	10	5.2	6	2	5	1	7.8	N	N	80	1	N	5 Ld SC-70, 5 Ld SOT-23
HA-2540	Y	400	400	20	30	20	6	5	8	1	20	N	N	72	10	N	14 Ld CerDIP

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Voltage Feedback (Continued)

Device	iSim	BW (MHz)	SR (V/μs)	V <sub>S</sub> (min) (V)	V <sub>S</sub> (max) (V)	I <sub>S</sub> (per amp) (mA)	Noise V <sub>N</sub> (nV/√Hz)	I <sub>BIAS</sub> (μA)	V <sub>OS</sub> (max) (mV)	# of Devices/Channels	V <sub>OUT</sub> (V)	Rail-to-Rail		CMRR (dB)	Gain A <sub>V</sub> (min)	Enable	Package
												Input	Output				
EL5202	Y	400	2200	5	10	5.2	12	2	5	2	7.8	N	N	80	1	Y	10 Ld MSOP
EL5203	Y	400	2200	5	10	5.2	12	2	5	2	7.8	N	N	80	1	N	8 Ld MSOP, 8 Ld SOIC
EL5302	Y	400	2200	5	10	5.2	12	2	5	3	7.8	N	N	80	1	Y	16 Ld QSOP
EL5373		450	1100	4.75	11	12	25	13	30	3	7.7	N	N	80	2	Y	24 Ld QSOP
EL5130	Y	500	350	5	12	4	1.8	2.3	0.9	1	7.6	N	N	110	5	Y	8 Ld SOIC
EL5131	Y	500	350	5	12	4	1.8	2.3	0.9	1	7.6	N	N	110	5	N	5 Ld SOT-23
EL8102	Y	500	600	3	5	5.6	12	6	8	1	4.8	N	Y	95	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL8103	Y	500	600	3	5	5.6	12	6	8	1	4.8	N	Y	95	1	N	5 Ld SOT-23
EL8202	Y	500	600	3	5.5	5.6	12	6	8	2	4.8	N	Y	95	1	Y	10 Ld MSOP
EL8203	Y	500	600	3	5.5	5.6	12	6	8	2	4.8	N	Y	95	1	N	8 Ld MSOP, 8 Ld SOIC
EL8302	Y	500	600	3	5.5	5.6	12	6	7	3	4.85	N	Y	95	1	Y	16 Ld QSOP, 16 Ld SOIC
EL8403	Y	500	600	3	5.5	5.6	12	6	8	4	4.8	N	Y	85	1	N	14 Ld SOIC, 16 Ld QSOP
EL5374		550	1100	4.75	11	12.5	21	14	25	3	7.6	N	N	78	1	Y	28 Ld QSOP
EL5375		550	900	4.75	11	9.6	21	12.5	30	3	7.49	N	N	95	1	Y	24 Ld QSOP
EL5156	Y	600	700	5	12	6	12	0.4	1	1	7.6	N	N	108	1	Y	8 Ld SOIC
EL5157		600	700	5	12	6	12	0.4	1	1	7.6	N	N	108	1	N	5 Ld SOT-23
HA-2539	Y	600	600	20	30	20	6	5	15	1	20	N	N	72	10	N	14 Ld PDIP
EL5256	Y	600	700	5	12	6	12	0.02	1	2	7.6	N	N	108	1	Y	10 Ld MSOP
EL5257		600	700	5	12	6	12	0.02	1	2	7.6	N	N	108	1	N	8 Ld MSOP, 8 Ld SOIC
EL5134	Y	650	450	5	12	6.7	1.5	3.7	1	1	7.8	N	N	108	5	Y	8 Ld SOIC
EL5135	Y	650	450	5	12	6.7	1.5	3.7	1	1	7.8	N	N	108	5	N	5 Ld SOT-23
EL5234	Y	650	450	5	12	6.7	1.5	3.7	1	2	7.8	N	N	108	5	Y	10 Ld MSOP
EL5235	Y	650	450	5	12	6.7	1.5	3.7	1	2	7.8	N	N	108	5	N	8 Ld SOIC
EL5132	Y	670	1000	5	12	12	0.9	12	1	1	7	N	N	100	10	Y	8 Ld SOIC
EL5133	Y	670	1000	5	12	12	0.9	12	1	1	7	N	N	100	10	N	5 Ld SOT-23
EL5104	Y	700	7000	5	10	9.5	10	8	10	1	7.6	N	N	62	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5105	Y	700	7000	5	10	9.5	10	8	10	1	7.6	N	N	62	1	N	5 Ld SC-70, 5 Ld SOT-23
EL5204	Y	700	7000	5	10	9.5	10	8	10	2	7.6	N	N	62	1	Y	10 Ld MSOP
EL5205	Y	700	7000	5	10	9.5	10	8	10	2	7.6	N	N	62	1	N	8 Ld MSOP, 8 Ld SOIC
EL5304	Y	700	7000	5	10	9.5	10	8	10	3	7.6	N	N	62	1	Y	16 Ld QSOP
EL5378		700	1000	4.75	11	12.5	18	14	30	3	7.4	N	N	78	2	Y	28 Ld QSOP

**Fixed Gain**

Device	iSim	BW (MHz)	SR (V/μs)	V <sub>S</sub> (min) (V)	V <sub>S</sub> (max) (V)	I <sub>S</sub> (per amp) (mA)	Noise V <sub>N</sub> (nV/√Hz)	I <sub>BIAS</sub> (μA)	V <sub>OS</sub> (max) (mV)	# of Devices/ Channels	V <sub>OUT</sub> (V)	Rail-to-Rail		CMRR (dB)	Gain A <sub>V</sub> (min)	Enable	Package
												Input	Output				
EL5170		100	1100	5	10	7.4	30	6	25	1	6.9	N	N	84	2	Y	8 Ld MSOP, 8 Ld SOIC
EL5370		100	1100	5	10	7	30	6	25	3	6.9	N	N	84	2	Y	24 Ld QSOP
EL5197	Y	200	2200	5	10	4	4.4	1	10	1	7.4	N	N		1	Y	5 Ld SOT-23
EL5397A		200	2100	5	10	4	4.8	1	10	3	7.4	N	N		1	Y	16 Ld QSOP, 16 Ld SOIC
EL5106	Y	350	4500	5	12	1.5	2.8	1.5	10	1	7.2	N	N		1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5306	Y	350	4500	5	12	1.5	2.8	1.5	10	3	7.2	N	N		1	Y	16 Ld QSOP, 16 Ld SOIC
HFA1412		350	1650	9	11	5.9	7	1	10	4	6.4	N	N	45	1	N	14 Ld PDIP, 14 Ld SOIC
EL5396A		400	2600	5	10	9	3.8	40	15	3	7.4	N	N		1	Y	16 Ld QSOP, 16 Ld SOIC
EL5108		450	4500	5	12	3.5	2	2	5	1	7.6	N	N		1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5173		450	900	5	11	12	25	11	30	1	7.7	N	N	80	2	Y	8 Ld MSOP, 8 Ld SOIC
EL5308	Y	450	4500	5	12	3.5	2	2	5	3	7.6	N	N		1	Y	16 Ld QSOP, 16 Ld SOIC
EL5373		450	1100	4.75	11	12	25	13	30	3	7.7	N	N	80	2	Y	24 Ld QSOP
HFA1110		750	1800	9	11	26	14	10	25	1	6.6	N	N	80	1	N	8 Ld SOIC
HFA1112		850	2400	9	11	21	9	25	25	1	6.6	N	N		1	N	8 Ld PDIP, 8 Ld SOIC
HFA1113		850	2400	9	11	21	9	25	25	1	6.6	N	N		1	N	8 Ld SOIC

**Differential Drivers**

Device	iSim	BW (MHz)	SR (V/μs)	V <sub>S</sub> (min) (V)	V <sub>S</sub> (max) (V)	I <sub>S</sub> (per amp) (mA)	Noise V <sub>N</sub> (nV/√Hz)	I <sub>BIAS</sub> (μA)	V <sub>OS</sub> (max) (mV)	# of Devices/ Channels	V <sub>OUT</sub> (V)	Rail-to-Rail		CMRR (dB)	Gain A <sub>V</sub> (min)	Enable	Package
												Input	Output				
EL5170		100	1100	5	10	7.4	30	6	25	1	6.9	N	N	84	2	Y	8 Ld MSOP, 8 Ld SOIC
EL5370		100	1100	5	10	7	30	6	25	3	6.9	N	N	84	2	Y	24 Ld QSOP
EL5171		250	800	5	10	8	26	6	25	1	6.8	N	N	82	1	N	8 Ld SOIC
EL5371		250	800	5	10	8	26	6	25	3	6.8	N	N	82	1	N	28 Ld QSOP
EL5173		450	900	5	11	12	25	11	30	1	7.7	N	N	80	2	Y	8 Ld MSOP, 8 Ld SOIC
EL5373		450	1100	4.75	11	12	25	13	30	3	7.7	N	N	80	2	Y	24 Ld QSOP
EL5174		550	1100	4.75	11	12.5	21	14	25	1	7.6	N	N	78	1	N	8 Ld SOIC
EL5177		550	1100	4.75	11	12.5	21	14	25	1	7.6	N	N	78	1	Y	10 Ld MSOP
EL5374		550	1100	4.75	11	12.5	21	14	25	3	7.6	N	N	78	1	Y	28 Ld QSOP
EL5178		700	850	4.75	11	12.5	18	14	30	1	7.4	N	N	78	2	N	8 Ld MSOP, 8 Ld SOIC
EL5378		700	1000	4.75	11	12.5	18	14	30	3	7.4	N	N	78	2	Y	28 Ld QSOP

**Differential Receivers**

Device	iSim	BW (MHz)	SR (V/μs)	V <sub>S</sub> (min) (V)	V <sub>S</sub> (max) (V)	I <sub>S</sub> (per amp) (mA)	Noise V <sub>N</sub> (nV/√Hz)	I <sub>BIAS</sub> (μA)	V <sub>OS</sub> (max) (mV)	# of Devices/ Channels	V <sub>OUT</sub> (V)	Rail-to-Rail Input	Rail-to-Rail Output	CMRR (dB)	Gain A <sub>V</sub> (min)	Enable	Package
EL5172		250	800	5	10	6	26	6	25	1	7.5	N	N	95	1	Y	8 Ld MSOP, 8 Ld SOIC
EL5372		250	800	5	10	6	26	6	25	3	7.5	N	N	95	1	Y	24 Ld QSOP
EL5175	Y	550	600	4.75	11	9.6	21	12.5	40	1	7.49	N	N	95	1	Y	8 Ld MSOP, 8 Ld SOIC
EL5375		550	900	4.75	11	9.6	21	12.5	30	3	7.49	N	N	95	1	Y	24 Ld QSOP

**Rail-to-Rail**

Device	iSim	BW (MHz)	SR (V/μs)	V <sub>S</sub> (min) (V)	V <sub>S</sub> (max) (V)	I <sub>S</sub> (per amp) (mA)	Noise V <sub>N</sub> (nV/√Hz)	I <sub>BIAS</sub> (μA)	V <sub>OS</sub> (max) (mV)	# of Devices/ Channels	V <sub>OUT</sub> (V)	Rail-to-Rail		CMRR (dB)	Gain A <sub>V</sub> (min)	Enable	Package
												Input	Output				
ISL59110		8	40	2.5	3.6	2				1	3.2	N	Y		2	Y	6 Ld SC-70
ISL59111		8	40	2.5	3.6	2				1	3.2	N	Y		2	Y	6 Ld CSP
ISL59112		40	85	2.5	3.6	2		5		1	3.2	N	N		6	N	6 Ld SC-70
EL5144		100	200	4.75	5.25	7	25	2	25	1	4.85	N	Y	50	1	N	5 Ld SOT-23
EL5146		100	200	4.75	5.25	7	25	2	25	1	4.85	N	Y	50	1	Y	8 Ld PDIP, 8 Ld SOIC
EL5244		100	200	4.75	5.25	7	25	2	25	2	4.85	N	Y	50	1	N	8 Ld MSOP, 8 Ld PDIP, 8 Ld SOIC
EL5246		100	200	4.75	5.25	7	25	2	25	2	4.85	N	Y	50	1	Y	14 Ld MSOP, 14 Ld PDIP, 14 Ld SOIC
EL5444		100	200	4.75	5.25	7	25	2	15	4	4.85	N	Y	50	1	N	14 Ld PDIP, 14 Ld SOIC, 16 Ld QSOP
EL8100	Y	200	200	3	5	2	10	1.5	6	1	4.8	N	Y	90	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL8101	Y	200	200	3	5	2	10	1.5	6	1	4.8	N	Y	90	1	N	5 Ld SOT-23
EL8200	Y	200	200	3	5.5	2	10	1.6	6	2	4.8	N	Y	90	1	Y	10 Ld MSOP
EL8201	Y	200	200	3	5.5	2	10	1.6	6	2	4.8	N	Y	90	1	N	8 Ld SOIC
EL8300	Y	200	200	3	5.5	2	10	1.4	5	3	4.83	N	Y	90	1	Y	16 Ld QSOP, 16 Ld SOIC
ISL59833		200	500	3	3.6	32.3	20		25	3	5.1	Y	Y		2	Y	16 Ld QSOP
ISL59837		200	500	3	3.6	32.3	20		25	3	5.1	Y	Y		2	Y	16 Ld QSOP
EL8401	Y	200	200	3	5.5	2	10	1.6	6	4	4.8	N	Y	90	1	N	14 Ld SOIC, 16 Ld QSOP
EL8102	Y	500	600	3	5	5.6	12	6	8	1	4.8	N	Y	95	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL8103	Y	500	600	3	5	5.6	12	6	8	1	4.8	N	Y	95	1	N	5 Ld SOT-23
EL8202	Y	500	600	3	5.5	5.6	12	6	8	2	4.8	N	Y	95	1	Y	10 Ld MSOP
EL8203	Y	500	600	3	5.5	5.6	12	6	8	2	4.8	N	Y	95	1	N	8 Ld MSOP, 8 Ld SOIC

**Rail-to-Rail (Continued)**

Device	iSim	BW (MHz)	SR (V/μs)	V <sub>S</sub> (min) (V)	V <sub>S</sub> (max) (V)	I <sub>S</sub> (per amp) (mA)	Noise V <sub>N</sub> (nV/√Hz)	I <sub>BIAS</sub> (μA)	V <sub>OS</sub> (max) (mV)	# of Devices/ Channels	V <sub>OUT</sub> (V)	Rail-to-Rail		CMRR (dB)	Gain A <sub>V</sub> (min)	Enable	Package
												Input	Output				
EL8302	Y	500	600	3	5.5	5.6	12	6	7	3	4.85	N	Y	95	1	Y	16 Ld QSOP, 16 Ld SOIC
EL8403	Y	500	600	3	5.5	5.6	12	6	8	4	4.8	N	Y	85	1	N	14 Ld SOIC, 16 Ld QSOP

Device	# of Devices/ Channels	V <sub>S</sub> (V)	BW (MHz)	SR (V/μs)	Rail-to-Rail		I <sub>S</sub> (mA)	I <sub>OUT</sub> (mA)	Package
					Input	Output			
EL5120	1	4.5 to 18	12	10	Y	Y	0.5	30	5 Ld TSOT, 8 Ld HMSOP
EL5220	2	4.5 to 18	12	10	Y	Y	0.5	30	8 Ld MSOP
EL5420	4	4.5 to 18	12	10	Y	Y	0.5	30	14 Ld SOIC, 14 Ld TSSOP, 16 Ld QFN
EL5210	2	4.5 to 18	30	33	Y	Y	2.5	120	8 Ld MSOP, 8 Ld SOIC
EL5410	4	4.5 to 18	30	33	Y	Y	2.5	120	14 Ld SOIC, 14 Ld TSSOP

**Low Offset**

Device	iSim	BW (MHz)	SR (V/μs)	V <sub>S</sub> (min) (V)	V <sub>S</sub> (max) (V)	I <sub>S</sub> (per amp) (mA)	Noise V <sub>N</sub> (nV/√Hz)	I <sub>BIAS</sub> (μA)	V <sub>OS</sub> (max) (mV)	# of Devices/ Channels	V <sub>OUT</sub> (V)	Rail-to-Rail		CMRR (dB)	Gain A <sub>V</sub> (min)	Enable	Package
												Input	Output				
EL5150	Y	200	67	5	12	1.4	12	0.02	1	1	5.6	N	N	100	1	Y	6 Ld SOT-23, 8 Ld SOIC
EL5151	Y	200	67	5	12	1.4	12	0.02	1	1	5.6	N	N	100	1	N	5 Ld SOT-23
EL5250	Y	200	67	5	12	1.4	12	0.02	1	2	5.6	N	N	100	1	Y	10 Ld MSOP
EL5251	Y	200	67	5	12	1.4	12	0.02	1	2	5.6	N	N	100	1	N	8 Ld MSOP, 8 Ld SOIC
EL5152	Y	270	180	5	12	3	12	0.12	1	1	7.4	N	N	110	1	Y	8 Ld SOIC
EL5153	Y	270	180	5	12	3	12	0.12	1	1	7.4	N	N	110	1	N	5 Ld SOT-23
EL5252	Y	270	180	5	12	3	12	0.11	1	2	7.4	N	N	110	1	Y	10 Ld MSOP
EL5156	Y	600	700	5	12	6	12	0.4	1	1	7.6	N	N	108	1	Y	8 Ld SOIC
EL5157	Y	600	700	5	12	6	12	0.4	1	1	7.6	N	N	108	1	N	5 Ld SOT-23
EL5256	Y	600	700	5	12	6	12	0.02	1	2	7.6	N	N	108	1	Y	10 Ld MSOP
EL5257	Y	600	700	5	12	6	12	0.02	1	2	7.6	N	N	108	1	N	8 Ld MSOP, 8 Ld SOIC

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## Differential Amplifiers

Device	iSim	BW (MHz)	SR (V/μs)	V <sub>S</sub> (min) (V)	V <sub>S</sub> (max) (V)	I <sub>S</sub> (per amp) (mA)	Noise V <sub>N</sub> (nV/√Hz)	I <sub>BIAS</sub> (μA)	V <sub>OS</sub> (max) (mV)	# of Devices/ Channels	V <sub>OUT</sub> (V)	Rail-to-Rail		CMRR (dB)	Gain A <sub>V</sub> (min)	Enable	Package
												Input	Output				
EL5170		100	1100	5	10	7.4	30	6	25	1	6.9	N	N	84	2	Y	8 Ld MSOP, 8 Ld SOIC
EL5370		100	1100	5	10	7	30	6	25	3	6.9	N	N	84	2	Y	24 Ld QSOP
EL2141		150	800	6	12.6	11	36	6	40	1	7.2	N	N		2	N	8 Ld SOIC
EL2142		150	400	6	12.6	11	36	6	10	1	6.2	N	N	70	1	N	8 Ld SOIC
EL5171		250	800	5	10	8	26	6	25	1	6.8	N	N	82	1	N	8 Ld SOIC
EL5172		250	800	5	10	6	26	6	25	1	7.5	N	N	95	1	Y	8 Ld MSOP, 8 Ld SOIC
EL5371		250	800	5	10	8	26	6	25	3	6.8	N	N	82	1	N	28 Ld QSOP
EL5372		250	800	5	10	6	26	6	25	3	7.5	N	N	95	1	Y	24 Ld QSOP
EL5173		450	900	5	11	12	25	11	30	1	7.7	N	N	80	2	Y	8 Ld MSOP, 8 Ld SOIC
EL5373		450	1100	4.75	11	12	25	13	30	3	7.7	N	N	80	2	Y	24 Ld QSOP
EL5174		550	1100	4.75	11	12.5	21	14	25	1	7.6	N	N	78	1	N	8 Ld SOIC
EL5175	Y	550	600	4.75	11	9.6	21	12.5	40	1	7.49	N	N	95	1	Y	8 Ld MSOP, 8 Ld SOIC
EL5177		550	1100	4.75	11	12.5	21	14	25	1	7.6	N	N	78	1	Y	10 Ld MSOP
EL5374		550	1100	4.75	11	12.5	21	14	25	3	7.6	N	N	78	1	Y	28 Ld QSOP
EL5375		550	900	4.75	11	9.6	21	12.5	30	3	7.49	N	N	95	1	Y	24 Ld QSOP
EL5378		700	1000	4.75	11	12.5	18	14	30	3	7.4	N	N	78	2	Y	28 Ld QSOP

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Device	NPN or PNP	# of Devices/ Channels	V(BR) CBO	V(BR) CEO (V)	V(BR) EBO (V)	ICEO (nA)	ICBO (nA)	HFE	NF (dB)	FT (GHz)	V <sub>OS</sub> (max) (mV)	I <sub>OS</sub> (μA)	CEB (pF)	CCB (pF)	Package
CA3054	NPN	2	60	24	7	N/A	0.002	N/A	8	0.55	5	0.3	N/A	N/A	14 Ld SOIC
CA3102	NPN	2	60	24	7	N/A	0.0013	N/A	1.5	1.35	5	0.3	N/A	0.28	14 Ld SOIC
HFA3102	NPN	2	18	12	6	N/A	0.1	70	2.1	10	5	5	0.2	0.3	14 Ld SOIC