

April 2013

FGD4536 360 V PDP Trench IGBT

Features

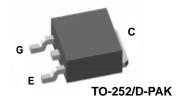
- · High Current Capability
- Low Saturation Voltage: V_{CE(sat)} = 1.59 V @ I_C = 50 A
- · High Input Impedance
- · Fast Switching
- RoHS Compliant

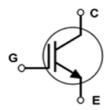
Applications

• PDP TV, Consumer Appliances

General Description

Using novel trench IGBT technology, Fairchild[®]'s new series of trench IGBTs offer the optimum performance for consumer appliances and PDP TV applications where low conduction and switching losses are essential.





Absolute Maximum Ratings

Symbol	Description		Ratings	Unit		
V _{CES}	Collector to Emitter Voltage		360	V		
V_{GES}	Gate to Emitter Voltage		Gate to Emitter Voltage		± 30	V
I _{C pulse(1)*}	Pulsed Collector Current	$@ T_C = 25^{\circ}C$	220	Α		
P _D	Maximum Power Dissipation	$@ T_C = 25^{\circ}C$	125	W		
	Maximum Power Dissipation	$@ T_C = 100^{\circ}C$	50	W		
T _J	Operating Junction Temperature		-55 to +150	°C		
T _{stg}	Storage Temperature Range		-55 to +150	°C		
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C		

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JC}(IGBT)$	Thermal Resistance, Junction to Case	-	1.0	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	-	62.5	°C/W

Notes

(1) Half Sine Wave, D < 0.01, pluse width < $1\mu sec$

^{*} Ic_pluse limited by max Tj

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FGD4536	FGD4536TM	TO252	380mm	16mm	-

Electrical Characteristics of the IGBT $T_C = 25$ °C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	teristics					
BV _{CES}	Collector to Emitter Breakdown Voltage	$V_{GE} = 0V, I_{C} = 250\mu A$	360	-	-	V
$\frac{\Delta BV_{CES}}{\Delta T_J}$	Temperature Coefficient of Breakdown Voltage	$V_{GE} = 0V, I_C = 250\mu A$	-	0.4	-	V/°C
I _{CES}	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$	-	-	100	μА
I _{GES}	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$	-	-	±400	nA
On Charac	teristics					
V _{GE(th)}	G-E Threshold Voltage	$I_{C} = 250 \mu A, V_{CE} = V_{GE}$	2.4	3.3	4.0	V
		I _C = 20A, V _{GE} = 15V	-	1.19	-	V
V05()	Collector to Emitter	I _C = 30A, V _{GE} = 15V	-	1.33	-	V
V _{CE(sat)}	Saturation Voltage	I _C = 50A, V _{GE} = 15V, T _C = 25°C	-	1.59	1.8	V
		I _C = 50A, V _{GE} = 15V, T _C = 125°C	-	1.66	-	V
Dynamic C	haracteristics	•				
C _{ies}	Input Capacitance		-	1295	-	pF
C _{oes}	Output Capacitance	$V_{CE} = 30V_{,} V_{GE} = 0V_{,}$ f = 1MHz	-	56	-	pF
C _{res}	Reverse Transfer Capacitance	- 1 - 11VII 12	-	43	-	pF
Switching	Characteristics					
t _{d(on)}	Turn-On Delay Time		-	5	-	ns
t _r	Rise Time	$V_{CC} = 200V, I_C = 20A,$	-	20	-	ns
t _{d(off)}	Turn-Off Delay Time	$R_G = 5\Omega$, $V_{GE} = 15V$, ResistiveLoad, $T_C = 25^{\circ}C$	-	41	-	ns
t _f	Fall Time		-	182	-	ns
t _{d(on)}	Turn-On Delay Time		-	5	-	ns
t _r	Rise Time	V_{CC} = 200V, I_{C} = 20A, R_{G} = 5 Ω , V_{GE} = 15V, Resistive Load, T_{C} = 125°C	-	21	-	ns
t _{d(off)}	Turn-Off Delay Time		-	43	-	ns
t _f	Fall Time		-	249	-	ns
Qg	Total Gate Charge	V = 200V L = 20A	-	47	-	nC
Q _{ge}	Gate to Emitter Charge	$V_{CE} = 200V, I_{C} = 20A,$ $V_{GE} = 15V$	-	5.4	-	nC
Q _{gc}	Gate to Collector Charge		-	15	-	nC

Figure 1. Typical Output Characteristics

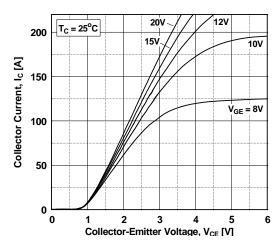


Figure 3. Typical Saturation Voltage Characteristics

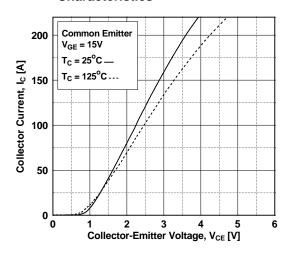


Figure 5. Saturation Voltage vs. Case
Temperature at Variant Current Level

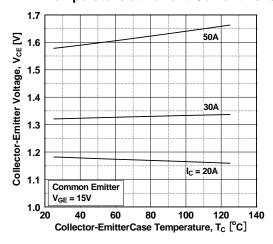


Figure 2. Typical Output Characteristics

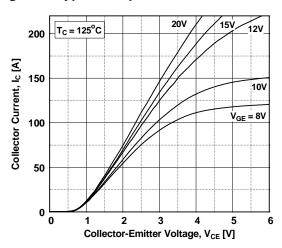


Figure 4. Transfer Characteristics

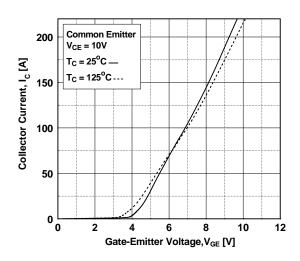


Figure 6. Saturation Voltage vs. V_{GE}

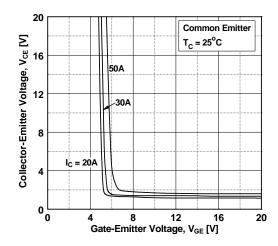


Figure 7. Saturation Voltage vs. V_{GE}

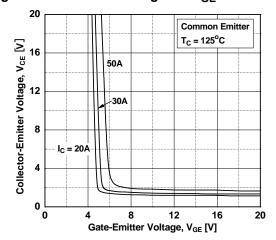


Figure 9. Gate charge Characteristics

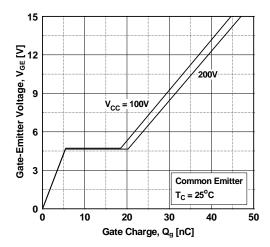


Figure 11. Turn-on Characteristics vs.
Gate Resistance

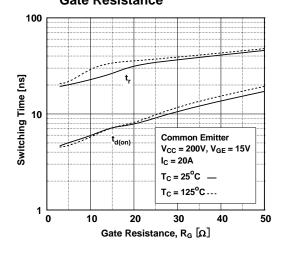


Figure 8. Capacitance Characteristics

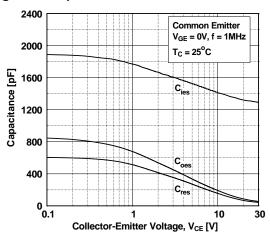


Figure 10. SOA Characteristics

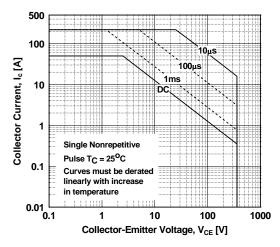


Figure 12. Turn-off Characteristics vs. Gate Resistance

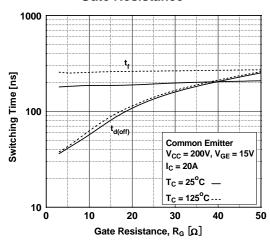


Figure 13. Turn-on Characteristics vs. Collector Current

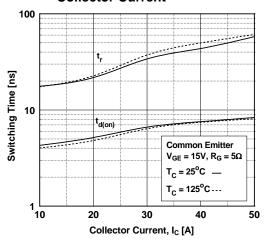


Figure 14. Turn-off Characteristics vs. Collector Current

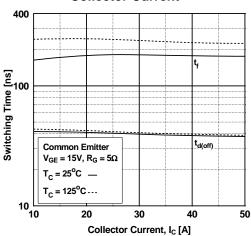


Figure 15. Switching Loss vs. Gate Resistance

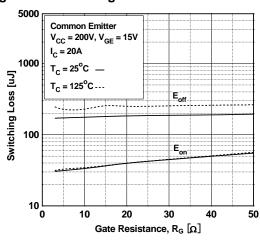


Figure 16. Switching Loss vs. Collector Current

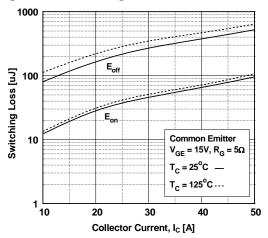


Figure 17. Turn off Switching SOA Characteristics

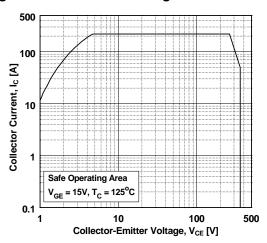
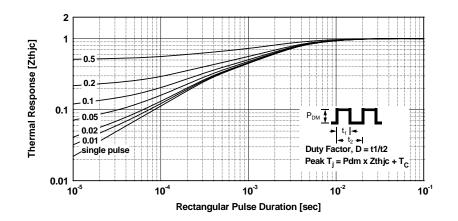
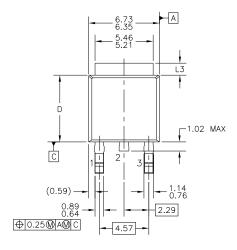


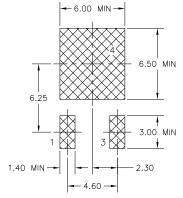
Figure 18.Transient Thermal Impedance of IGBT



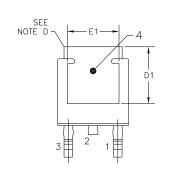
Mechanical Dimensions

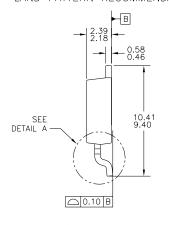
D-PAK

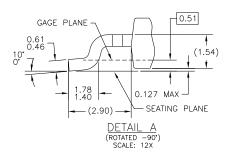




LAND PATTERN RECOMMENDATION







- NOTES: UNLESS OTHERWISE SPECIFIED

 A) ALL DIMENSIONS ARE IN MILLIMETERS.

 B) THIS PACKAGE CONFORMS TO JEDEC, TO-252, ISSUE C, VARIATION AA & AB, DATED NOV. 1999.

 C) DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.

 D) HEAT SINK TOP EDGE COULD BE IN CHAMFERED CORNERS OR EDGE PROTRUSION.

 E) DIMENSIONS L3,DL1&D1 TABLE:

 [OPTION AA | OPTION AB]
 - OPTION AA OPTION AB
 L3 0.89-1.27 1.52-2.03
 D 5.97-6.22 5.33-5.59
 E1 4.32 Min 3.81 Min
 D1 5.21 Min 4.57 Min

F) PRESENCE OF TRIMMED CENTER LEAD IS OPTIONAL.

Dimensions in Millimeters





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ТМ

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No Identification Needed	Full Production Datasheet contains final specifications. Fairchild Semiconductor reserves the make changes at any time without notice to improve the design.	
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