

Digital Attenuator, 1-Bit, 15 dB, 75 Ω DC - 1.1 GHz

Features

- Single 15-dB Step
- Insertion Loss: 0.3 dB @ 1.1 GHz
- 75 Ω Impedance
- 2.5 to 5.0 Volt Operation
- Positive or Negative Voltage Control
- Lead-Free SOT-25 Package
- Halogen-Free "Green" Mold Compound
- RoHS* Compliant and 260°C Reflow Compatible

Description

The MAAD-010305 is a 1-bit, 15-dB step GaAs MMIC digital attenuator in a lead-free SOT-25, 5 lead surface mount plastic package.

The MAAD-010305 is fabricated as a monolithic GaAs integrated circuit using a mature pHEMT process. The process features full chip passivation for performance and reliability.

This device is ideally suited for use where high accuracy, very low power consumption and low intermodulation products are required.

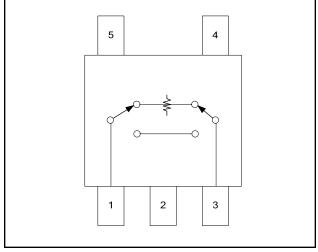
Ordering Information ^{1,2}

| Part Number | Package | |
|--------------------|-----------------|--|
| MAAD-010305-TR1000 | 1000 piece reel | |
| MAAD-010305-TR3000 | 3000 piece reel | |
| MAAD-010305-001SMB | Sample Board | |

1. Reference Application Note M513 for reel size information.

2. All sample boards include 5 loose parts.

Functional Schematic



Pin Configuration

| Pin No. | Function | | |
|---------|----------------|--|--|
| 1 | RF Input Port | | |
| 2 | Ground | | |
| 3 | RF Output Port | | |
| 4 | V1 | | |
| 5 | V2 | | |

* Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

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Electrical Specifications³: $T_A = 25^{\circ}C$, $V_C = +2.5$ Volts, $Z_0 = 75 \Omega$

| Parameter | Test Conditions | Units | Min. | Тур. | Max. |
|-------------------------------------|--|-------|------|----------|------|
| Insertion Loss (Reference State) | 1.1 GHz | dB | _ | 0.3 | 0.45 |
| Attenuation | 1.1 GHz | dB | 14.6 | 15.1 | 15.6 |
| Input Return Loss | DC - 1.1 GHz | dB | — | 14 | — |
| Output Return Loss | DC - 1.1 GHz | dB | — | 14 | _ |
| Input IP3 | 1.1 GHz Insertion Loss State d Attenuation State | | _ | 46 46 | _ |
| P1dB | 1.1 GHz Insertion Loss State Attenuation State | dBm | _ | 20 20 | _ |
| СТВ | 157 Channels, P_{IN} = 35 dBmV/ch. | dBc | _ | 70 | _ |
| CSO | 157 Channels, P_{IN} = 35 dBmV/ch. | dBc | | 66 | _ |
| Trise, Tfall | 10% to 90% RF, 90% to 10% RF | ns | | 15 | _ |
| Ton, Toff | 50% Control to 90% RF, 50% Control to 10% RF | ns | | 20 | _ |
| Transients | In-band | mV | _ | 20 | _ |
| Control Current | | μA | _ | _ | 5 |

3. For positive voltage control, external DC blocking capacitors are required on all RF ports (pins 1, 2 and 3).

Absolute Maximum Ratings ^{4,5}

| Parameter | Absolute Maximum | |
|-----------------------|------------------|--|
| Input Power | +21 dBm | |
| Control Voltage | 8 V | |
| Operating Temperature | -40°C to +85°C | |
| Storage Temperature | -65°C to +150°C | |

4. Exceeding any one or combination of these limits may cause permanent damage to this device.

5. M/A-COM Technology Solutions does not recommend sustained operation near these survivability limits.

Truth Table

| Mode (Control) | V1 | V2 | Attenuation |
|-----------------------|--------------|--------------|-----------------|
| Positive ⁶ | 0 ± 0.2V | +2.5V to +5V | 15 dB |
| | +2.5V to +5V | 0 ± 0.2V | Reference State |
| Negative ⁷ | 0 ± 0.2V | -2.5V to -5V | Reference State |
| | -2.5V to -5V | 0 ± 0.2V | 15 dB |

6. External DC blocking capacitors are required as noted.

7. If negative control is used, DC blocking capacitors are not required on RF ports and ground.

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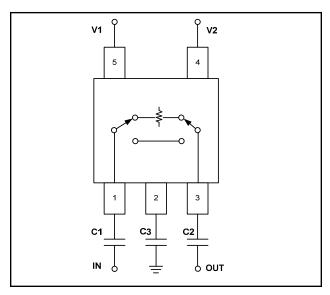
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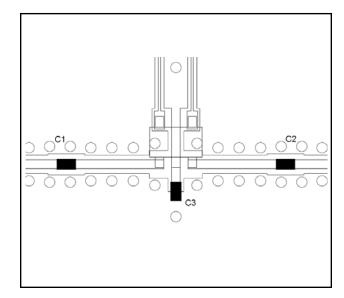
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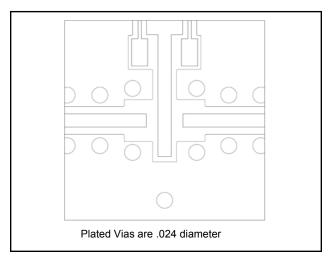
Positive Control Voltage Schematic



Recommended PCB Layout



PCB Land Pattern



Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

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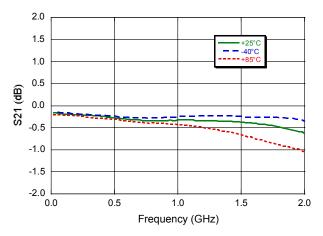
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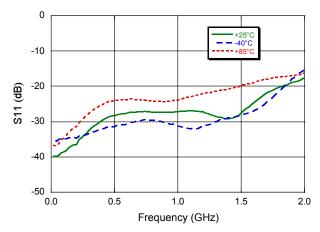
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Typical Performance Curves with C1,C2,C3 = 0.01 µF, positive or negative mode (control)

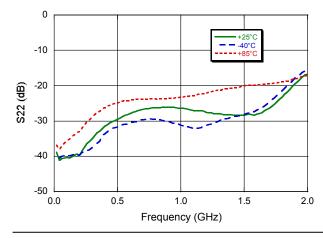
Insertion Loss



Input Return Loss



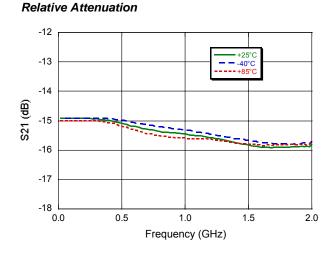
Output Return Loss



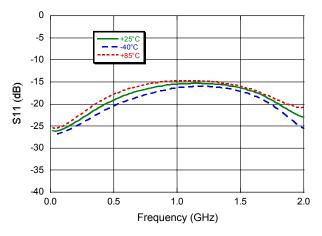
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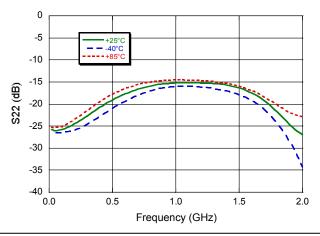
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Input Return Loss_Full Attenuation



Output Return Loss_Full Attenuation



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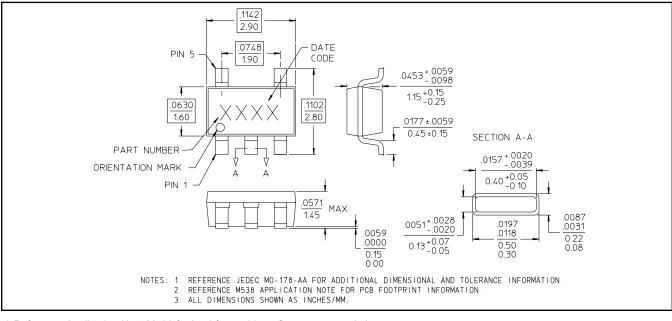
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Lead-Free SOT-25[†]

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† Reference Application Note M538 for lead-free solder reflow recommendations. Plating is 100% matte tin over copper.

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