Amplifier Transistors PNP Silicon

Features

• These are Pb-Free Devices*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	150	Vdc
Collector - Base Voltage	V _{CBO}	160	Vdc
Emitter – Base Voltage	V _{EBO}	5.0	Vdc
Collector Current – Continuous	Ι _C	600	mAdc
Total Device Dissipation @ $T_A = 25^{\circ}C$ Derate above 25°C	PD	625 5.0	mW mW/°C
Total Device Dissipation @ $T_C = 25^{\circ}C$ Derate above 25°C	P _D	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

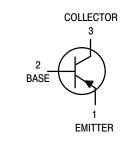
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

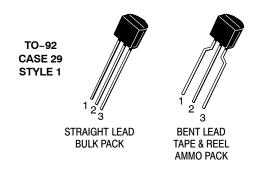
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



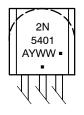
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MARKING DIAGRAM



A = Assembly Location Y = Year WW = Work Week = Pb-Free Package (Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

2N5401

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

V _(BR) CEO V _(BR) CBO V _(BR) EBO	150	_	Vdc Vdc
V _(BR) CBO V _(BR) EBO	160	_	
V _{(BR)EBO}		_	Vdc
	5.0	1	
lono	5.0	_	Vdc
ICBO	-	50 50	nAdc μAdc
I _{EBO}	-	50	nAdc
h _{FE}	50 60 50	 240 	-
V _{CE(sat)}	-	0.2 0.5	Vdc
V _{BE(sat)}	-	1.0 1.0	Vdc
fT	100	300	MHz
C _{obo}	_	6.0	pF
h _{fe}			-
	V _{CE(sat)} V _{BE(sat)} f _T C _{obo}	50 60 50 VCE(sat) - VBE(sat) - fT 100 Cobo -	$\begin{array}{c cccc} 50 & - & \\ 60 & 240 \\ 50 & - & \\ \hline & & \\ V_{CE(sat)} & - & \\ 0.5 & \\ \hline & & \\ V_{BE(sat)} & - & \\ 1.0 & \\ \hline & & \\ f_{T} & 100 & 300 \\ \hline & & \\ C_{obo} & - & 6.0 \\ \end{array}$

 $(I_{C} = 250 \ \mu \text{Adc}, V_{CE} = 5.0 \ \text{Vdc}, R_{S} = 1.0 \ \text{k}\Omega, f = 1.0 \ \text{kHz})$

1. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

ORDERING INFORMATION

Noise Figure

Device	Package	Shipping [†]
2N5401G	TO-92 (Pb-Free)	5000 Unit / Bulk
2N5401RLRAG	TO-92 (Pb-Free)	2000 / Tape & Reel

NF

dB

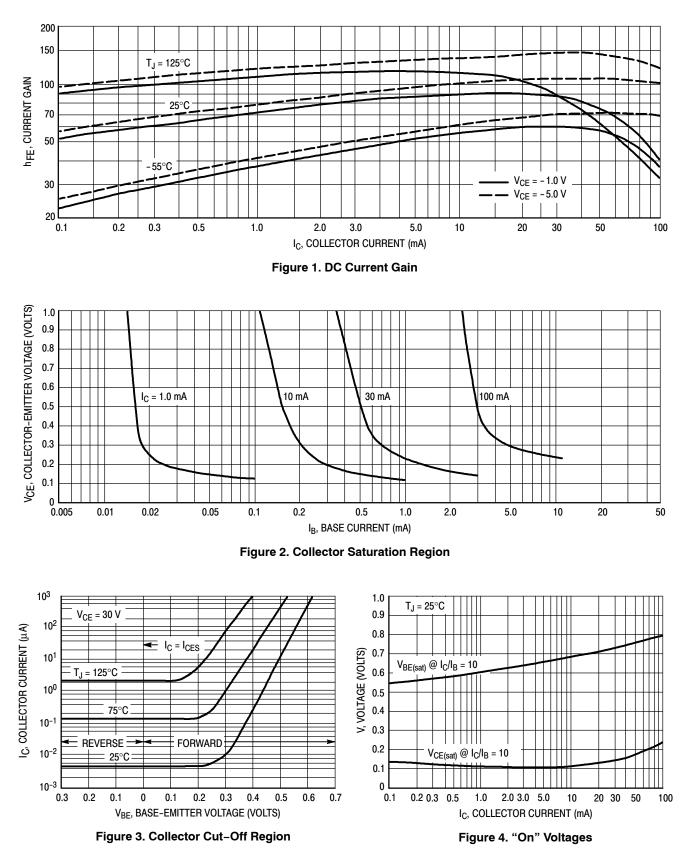
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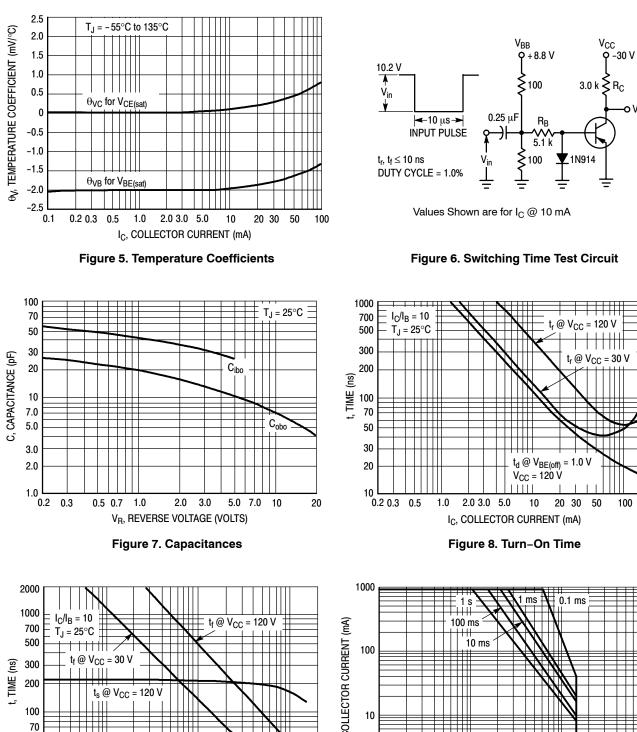
+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

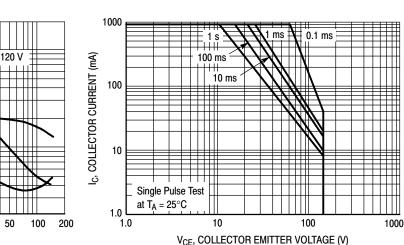
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IC, COLLECTOR CURRENT (mA) Figure 9. Turn-Off Time

10

20 30

2.0 3.0 5.0

50

30

20

0.2 0.3 0.5

1.0

Figure 10. Safe Operating Area

-o V_{out}

100

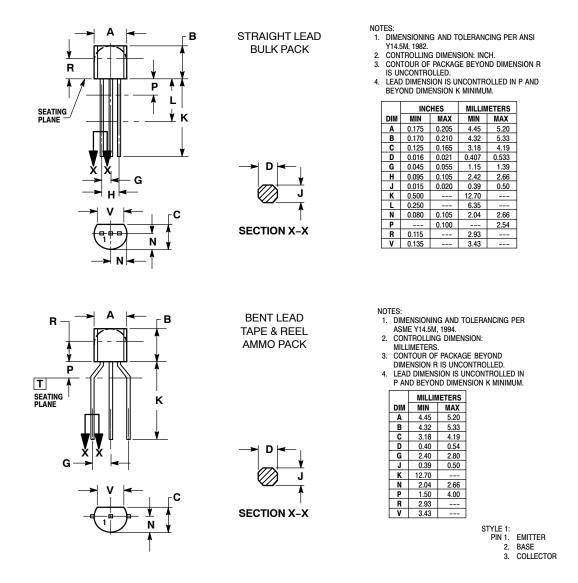
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PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AM



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