NPN - BC368; PNP - BC369

Amplifier Transistors

Voltage and Current are Negative for PNP Transistors

Features

• These are Pb-Free Devices*



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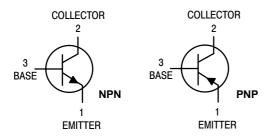
MAXIMUM RATINGS

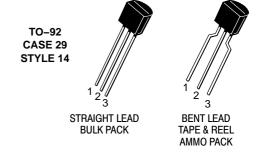
Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	20	Vdc
Collector – Emitter Voltage	V _{CES}	25	Vdc
Emitter – Base Voltage	V _{EBO}	5.0	Vdc
Collector Current – Continuous	I _C	1.0	Adc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	625 5.0	mW mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

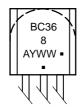
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

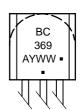
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.





MARKING DIAGRAMS





A = Assembly Location

Y = Year
WW = Work Week
Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping
BC368G	TO-92 (Pb-Free)	5000 Units / Bulk
BC368ZL1G	TO-92 (Pb-Free)	2000 / Ammo Pack
BC369ZL1G	TO-92 (Pb-Free)	2000 / Ammo Pack

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	•	•	•	1	
Collector – Emitter Breakdown Voltage $(I_C = 10 \text{ mA}, I_B = 0)$	V _{(BR)CEO}	20	_	_	Vdc
Collector – Base Breakdown Voltage ($I_C = 100 \mu A, I_E = 0$)	V _{(BR)CBO}	25	_	-	Vdc
Emitter – Base Breakdown Voltage ($I_E = 100 \mu A, I_C = 0$)	V _{(BR)EBO}	5.0	_	-	Vdc
Collector Cutoff Current $(V_{CB} = 25 \text{ V}, I_E = 0)$ $(V_{CB} = 25 \text{ V}, I_E = 0, T_J = 150^{\circ}\text{C})$	І _{СВО}	- -	- -	10 1.0	μAdc mAdc
Emitter Cutoff Current $(V_{EB} = 5.0 \text{ V}, I_{C} = 0)$	I _{EBO}	_	_	10	μAdc
ON CHARACTERISTICS		-			
DC Current Gain	h _{FE}	50 85 60	- - -	- 375 -	_
Bandwidth Product ($I_C = 10 \text{ mA}, V_{CE} = 5.0 \text{ V}, f = 20 \text{ MHz}$)	f _T	65	_	-	MHz
Collector–Emitter Saturation Voltage $(I_C = 1.0 \text{ A}, I_B = 100 \text{ mA})$	V _{CE(sat)}	_	_	0.5	V
Base–Emitter On Voltage (I _C = 1.0 A, V _{CE} = 1.0 V)	V _{BE(on)}	-	_	1.0	V

NPN - BC368; PNP - BC369

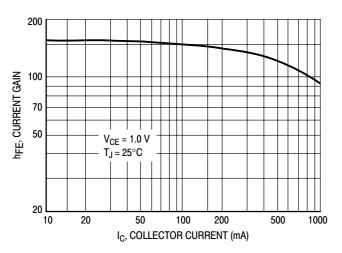


Figure 1. DC Current Gain

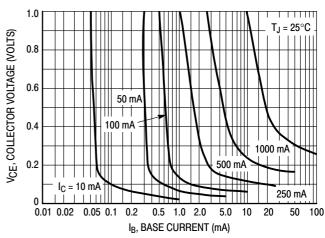


Figure 2. Collector Saturation Region

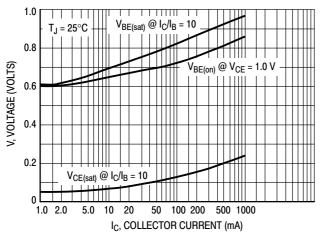


Figure 3. "On" Voltages

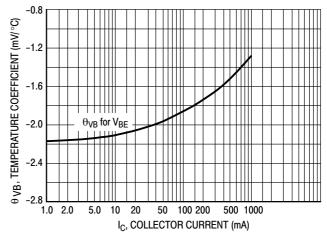


Figure 4. Temperature Coefficient

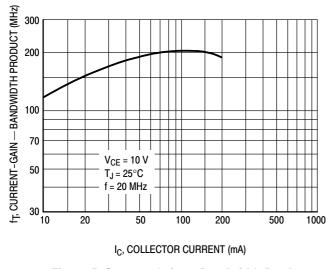


Figure 5. Current-Gain — Bandwidth Product

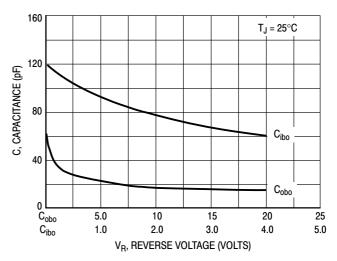
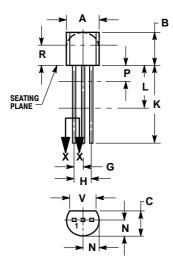


Figure 6. Capacitance

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AM

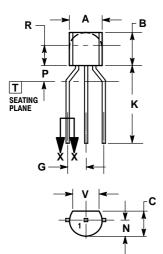


STRAIGHT LEAD **BULK PACK**



- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIM	IETERS
DIM	MIN	MAX	MIN MAX	
A	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
P		0.100		2.54
R	0.115		2.93	
V	0 135	l	3 43	



BENT LEAD TAPE & REEL AMMO PACK



NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS.
 CONTOUR OF PACKAGE BEYOND
- DIMENSION R IS UNCONTROLLED
- LEAD DIMENSION IS UNCONTROLLED IN PAND BEYOND DIMENSION K MINIMUM.

	MILLIMETERS		
DIM	MIN	MAX	
Α	4.45	5.20	
В	4.32	5.33	
С	3.18	4.19	
D	0.40	0.54	
G	2.40	2.80	
J	0.39	0.50	
K	12.70		
N	2.04	2.66	
P	1.50	4.00	
R	2.93		
٧	3.43		

STYLE 14:

PIN 1. EMITTER

COLLECTOR

3. BASE

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