Darlington Transistors

NPN Silicon

Features

• These are Pb-Free Devices*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CES}	30	Vdc
Collector – Base Voltage	V _{CB}	40	Vdc
Emitter-Base Voltage	V _{EB}	10	Vdc
Collector Current – Continuous	Ι _C	1.0	Adc
Total Power Dissipation @ $T_A = 25^{\circ}C$ Derate above $T_A = 25^{\circ}C$	PD	625 12	mW mW/°C
Total Power Dissipation @ $T_C = 25^{\circ}C$ Derate above $T_C = 25^{\circ}C$	P _D	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

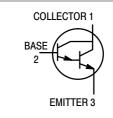
Characteristic	Symbol	Max	Unit	
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	°C/W	
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W	

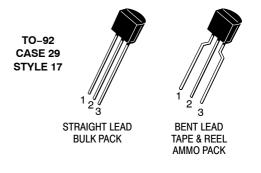
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



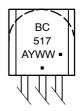
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http://onsemi.com





MARKING DIAGRAM



= Assembly Location

= Year WW

А Υ

= Work Week

= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
BC517G	TO-92 (Pb-Free)	5000 Units / Bulk
BC517RL1G	TO–92 (Pb–Free)	2000 / Tape & Reel
BC517ZL1G	TO-92 (Pb-Free)	2000 / Ammo Pack

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Publication Order Number:

BC517/D

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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September, 2011 – R

ELECTRICAL CHARACTERISTICS (T_A = $25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage $(I_C = 2.0 \text{ mAdc}, I_{BE} = 0)$	V _{(BR)CES}	30	-	_	Vdc
Collector – Base Breakdown Voltage $(I_C = 10 \ \mu Adc, I_E = 0)$	V _(BR) CBO	40	-	-	Vdc
Emitter – Base Breakdown Voltage $(I_E = 100 \ \mu Adc, I_C = 0)$	V _{(BR)EBO}	10	-	-	Vdc
Collector Cutoff Current (V _{CE} = 30 Vdc)	ICES	-	-	500	nAdc
Collector Cutoff Current (V_{CB} = 30 Vdc, I _E = 0)	I _{CBO}	-	-	100	nAdc
Emitter Cutoff Current ($V_{CB} = 10 \text{ Vdc}, I_C = 0$)	I _{EBO}	-	-	100	nAdc
ON CHARACTERISTICS (Note 1)					
DC Current Gain (I _C = 20 mAdc, V _{CE} = 2.0 Vdc)	h _{FE}	30,000	-	-	_
Collector – Emitter Saturation Voltage $(I_C = 100 \text{ mAdc}, I_B = 0.1 \text{ mAdc})$	V _{CE(sat)}	-	-	1.0	Vdc
Collector – Emitter Saturation Voltage $(I_C = 10 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc})$	V _{BE(on)}	-	-	1.4	Vdc
SMALL-SIGNAL CHARACTERISTICS		•		-	•
Current–Gain – Bandwidth Product (Note 2) $(I_C = 10 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}, f = 100 \text{ MHz})$	f _T	_	200	_	MHz
Dulas Tests Dulas Width 2000 - Duty Ousla 0.00/	· · · · · ·				

1. Pulse Test: Pulse Width = 300 μ s, Duty Cycle 2.0%. 2. f_T = |h_{fe}| • f_{test}

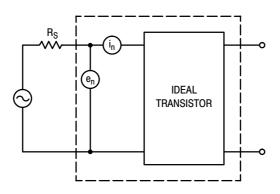
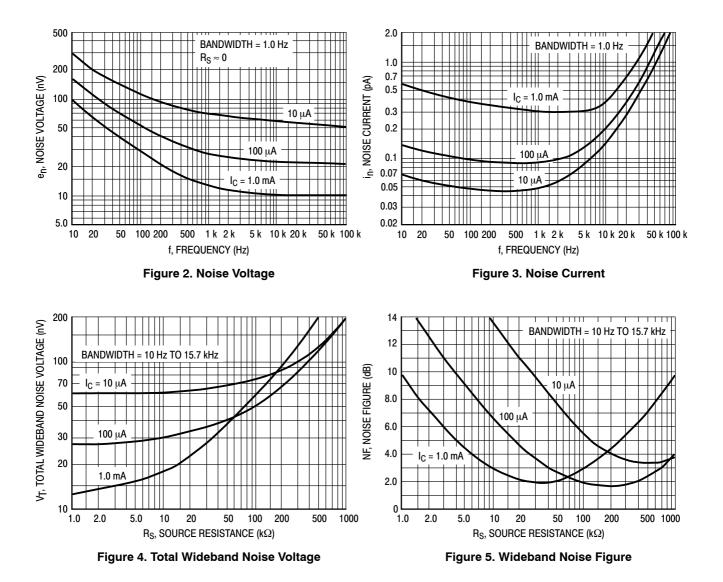


Figure 1. Transistor Noise Model

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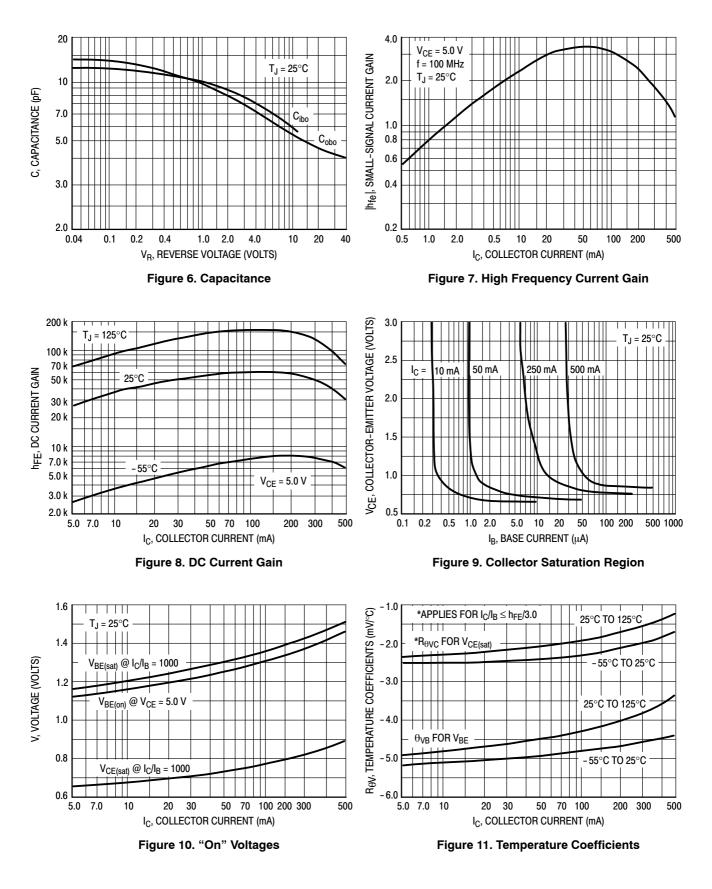
NOISE CHARACTERISTICS

(V_CE = 5.0 Vdc, T_A = 25°C)



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SMALL-SIGNAL CHARACTERISTICS



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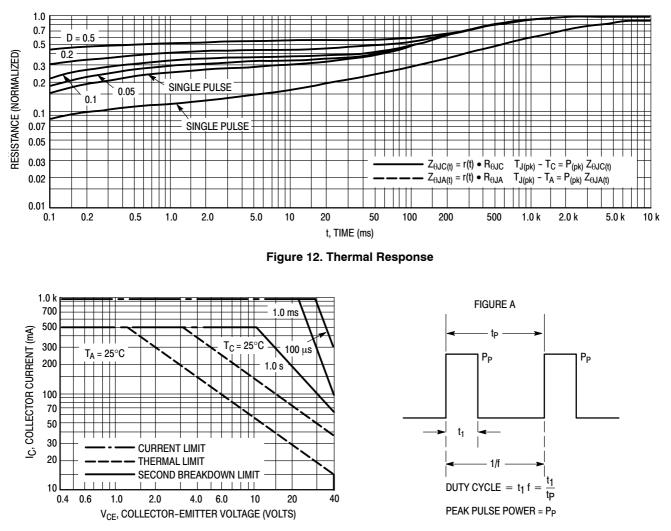
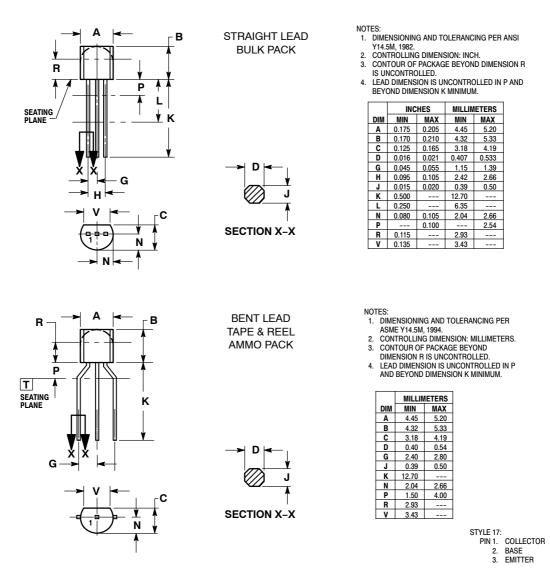


Figure 13. Active Region Safe Operating Area Design Note: Use of Transient Thermal Resistance Data

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PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AM



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