BD135, BD137, BD139

Plastic Medium Power Silicon NPN Transistors

This series of plastic, medium-power silicon NPN transistors are designed for use as audio amplifiers and drivers utilizing complementary or quasi complementary circuits.

Features

- DC Current Gain $h_{FE} = 40$ (Min) @ $I_C = 0.15$ Adc
- BD 135, 137, 139 are complementary with BD 136, 138, 140
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant*



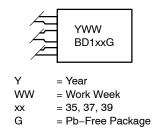
ON Semiconductor®

http://onsemi.com

1.5 A POWER TRANSISTORS NPN SILICON 45, 60, 80 V, 12.5 W



MARKING DIAGRAM



ORDERING INFORMATION

Device	Package	Shipping [†]
BD135G	TO-225AA (Pb-Free)	500 Units / Box
BD135TG	TO-225AA (Pb-Free)	50 Units / Rail
BD137G	TO-225AA (Pb-Free)	500 Units / Box
BD139G	TO-225AA (Pb-Free)	500 Units / Box

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MAXIMUM RATINGS

Rating		Symbol	Value	Unit
Collector-Emitter Voltage	BD135 BD137 BD139	V _{CEO}	45 60 80	Vdc
Collector-Base Voltage	BD135 BD137 BD139	V _{CBO}	45 60 100	Vdc
Emitter-Base Voltage		V _{EBO}	5.0	Vdc
Collector Current		Ι _C	1.5	Adc
Base Current		Ι _Β	0.5	Adc
Total Device Dissipation @ T _A = Derate above 25°C	± 25°C	P _D	1.25 10	Watts mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C		PD	12.5 100	Watts mW/°C
Operating and Storage Junction Temperature Range		T _J , T _{stg}	−55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	θJC	10	°C/W
Thermal Resistance, Junction-to-Ambient	θ_{JA}	100	°C/W

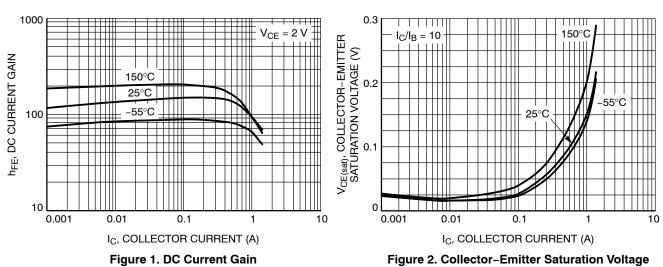
*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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BD135, BD137, BD139

Characteristic	Symbol	Туре	Min	Max	Unlt
Collector–Emitter Sustaining Voltage* $(I_C = 0.03 \text{ Adc}, I_B = 0)$	BV _{CEO} *	BD135 BD137 BD139	45 60 80		Vdc
Collector Cutoff Current ($V_{CB} = 30$ Vdc, $I_E = 0$) ($V_{CB} = 30$ Vdc, $I_E = 0$, $T_C = 125^{\circ}C$)	I _{CBO}			0.1 10	μAdc
Emitter Cutoff Current ($V_{BE} = 5.0 \text{ Vdc}, I_C = 0$)	I _{EBO}		-	10	μAdc
DC Current Gain ($I_C = 0.005 \text{ A}, V_{CE} = 2 \text{ V}$) ($I_C = 0.15 \text{ A}, V_{CE} = 2 \text{ V}$) ($I_C = 0.5 \text{ A} V_{CE} = 2 \text{ V}$)	h _{FE} *		25 40 25	_ 250 _	_
Collector–Emitter Saturation Voltage* ($I_c = 0.5$ Adc, $I_B = 0.05$ Adc)	V _{CE(sat)} *		-	0.5	Vdc
Base-Emitter On Voltage* (I _C = 0.5 Adc, V _{CE} = 2.0 Vdc)	V _{BE(on)} *		-	1	Vdc

*Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2.0%.

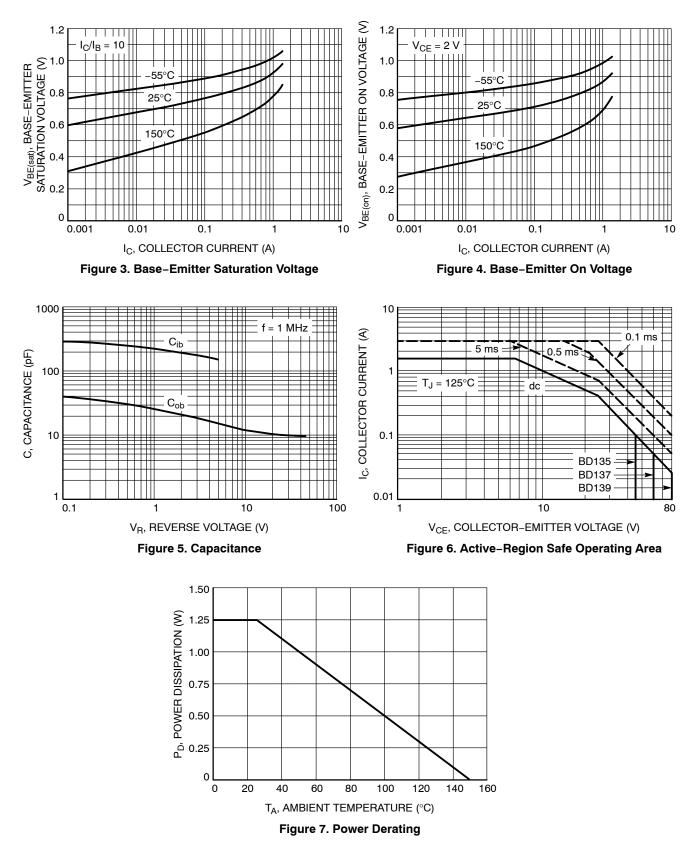


TYPICAL CHARACTERISTICS

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BD135, BD137, BD139

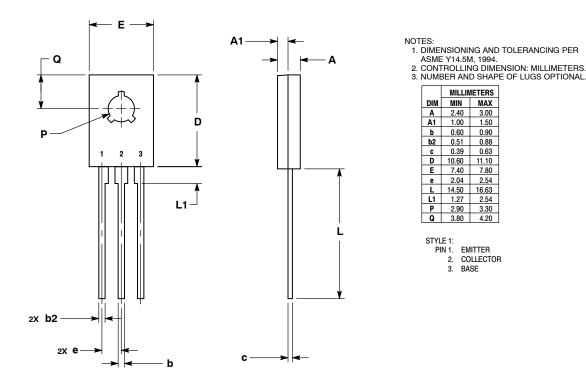
TYPICAL CHARACTERISTICS



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PACKAGE DIMENSIONS





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