# **Complementary Silicon Plastic Power Transistors**

These devices are designed for use in general purpose amplifier and switching applications.

#### **Features**

- Collector Emitter Saturation Voltage  $V_{CE(sat)} = 1.5 \text{ Vdc (Max)} @ I_C = 6.0 \text{ Adc}$
- Collector Emitter Sustaining Voltage –

V<sub>CEO(sus)</sub> = 80 Vdc (Min) – BD243B, BD244B = 100 Vdc (Min) – BD243C, BD244C

- High Current Gain Bandwidth Product  $f_T = 3.0 \text{ MHz}$  (Min) @  $I_C = 500 \text{ mAdc}$
- Pb-Free Packages are Available\*

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector–Emitter Voltage BD243B, BD244B BD243C, BD244C	V <sub>CEO</sub>	80 100	Vdc
Collector-Base Voltage BD243B, BD244B BD243C, BD244C	V <sub>CB</sub>	80 100	Vdc
Emitter-Base Voltage	V <sub>EB</sub>	5.0	Vdc
Collector Current - Continuous - Peak	IC	6 10	Adc
Base Current	Ι <sub>Β</sub>	2.0	Adc
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	65 0.52	W W/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C

# THERMAL CHARACTERISTICS

Characteristics	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.92	°C/W

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



# ON Semiconductor®

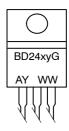
http://onsemi.com

# 6 AMPERE POWER TRANSISTORS COMPLEMENTARY SILICON 80-100 VOLTS 65 WATTS



TO-220AB CASE 221A-09 STYLE 1

## MARKING DIAGRAM



BD24xy = Device Code

x = 3 or 4y = B or C

A = Assembly Location

Y = Year
WW = Work Week
G = Pb-Free Package

# **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

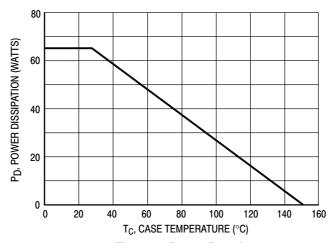


Figure 1. Power Derating

# **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

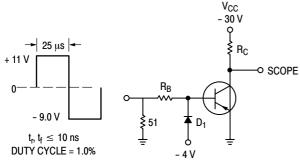
Characteristic		Symbol	Min	Max	Unit
Collector-Emitter Sustaining Voltage (Note 1) $(I_C = 30 \text{ mAdc}, I_B = 0)$	BD243B, BD244B BD243C, BD244C	V <sub>CEO(sus)</sub>	80 100	- -	Vdc
Collector Cutoff Current (V <sub>CE</sub> = 60 Vdc, I <sub>B</sub> = 0)	BD243B, BD243C, BD244B, BD244C	I <sub>CEO</sub>	-	0.7	mAdc
Collector Cutoff Current (V <sub>CE</sub> = 80 Vdc, V <sub>EB</sub> = 0) (V <sub>CE</sub> = 100 Vdc, V <sub>EB</sub> = 0)	BD243B, BD244B BD243C, BD244C	I <sub>CES</sub>	- -	400 400	μAdc
Emitter Cutoff Current (V <sub>BE</sub> = 5.0 Vdc, I <sub>C</sub> = 0)		I <sub>EBO</sub>	_	1.0	mAdc
ON CHARACTERISTICS (Note 1)					
DC Current Gain $ \begin{aligned} \text{(I}_{\text{C}} &= 0.3 \text{ Adc, V}_{\text{CE}} = 4.0 \text{ Vdc)} \\ \text{(I}_{\text{C}} &= 3.0 \text{ Adc, V}_{\text{CE}} = 4.0 \text{ Vdc)} \end{aligned} $		h <sub>FE</sub>	30 15	- -	-
Collector-Emitter Saturation Voltage (I <sub>C</sub> = 6.0 Adc, I <sub>B</sub> = 1.0 Adc)		V <sub>CE(sat)</sub>	_	1.5	Vdc
Base-Emitter On Voltage (I <sub>C</sub> = 6.0 Adc, V <sub>CE</sub> = 4.0 Vdc)		V <sub>BE(on)</sub>	_	2.0	Vdc

# **DYNAMIC CHARACTERISTICS**

Current-Gain - Bandwidth Product (Note 2) (I <sub>C</sub> = 500 mAdc, V <sub>CE</sub> = 10 Vdc, f <sub>test</sub> = 1.0 MHz)	f <sub>T</sub>	3.0	-	MHz
Small-Signal Current Gain (I <sub>C</sub> = 0.5 Adc, V <sub>CE</sub> = 10 Vdc, f = 1.0 kHz)	h <sub>fe</sub>	20	-	-

<sup>1.</sup> Pulse Test: Pulsewidth  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%.

<sup>2.</sup>  $f_T = h_{fe} \bullet f_{test}$ 



2.0  $T_{.I} = 25^{\circ}C$ 1.0  $V_{CC}$  = 30 V0.7  $I_C/I_B = 10$ 0.5 t, TIME (µs) 0.3 0.2 0.1  $t_d @ V_{BE(off)} = 5.0 V$ 0.07 0.05 0.03 0.1 0.4 0.6 4.0 6.0 IC, COLLECTOR CURRENT (AMP)

Figure 3. Turn-On Time



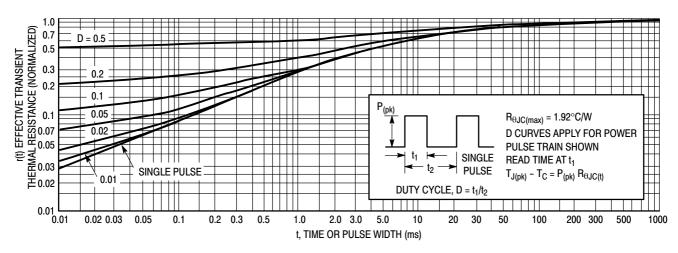


Figure 4. Thermal Response

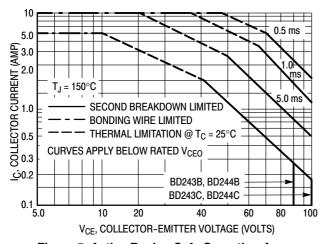


Figure 5. Active Region Safe Operating Area

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate  $I_C$  –  $V_{CE}$  limits of the transistor that must be observed for reliable operation, i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 5 is based on  $T_{J(pk)} = 150$ °C:  $T_C$  is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided  $T_{J(pk)} \le 150$ °C,  $T_{J(pk)}$  may be calculated from the data in Figure 4. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

300

200

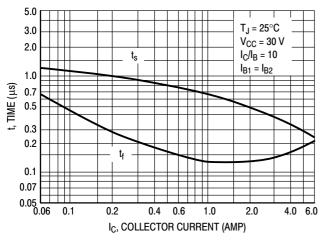


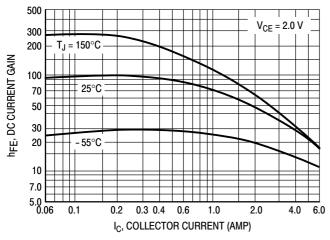
Figure 6. Turn-Off Time

V<sub>R</sub>, REVERSE VOLTAGE (VOLTS)

Figure 7. Capacitance

 $T_J = 25^{\circ}C$ 

50



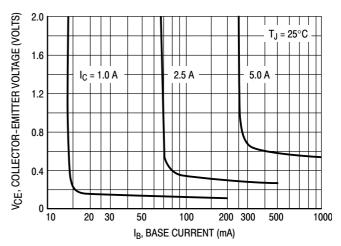
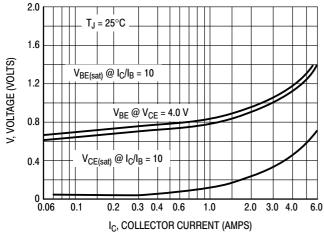


Figure 8. DC Current Gain

Figure 9. Collector Saturation Region



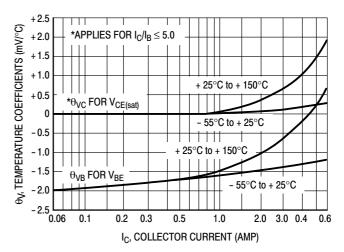
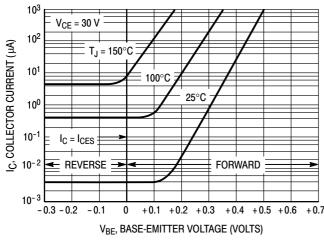


Figure 10. "On" Voltages

Figure 11. Temperature Coefficients





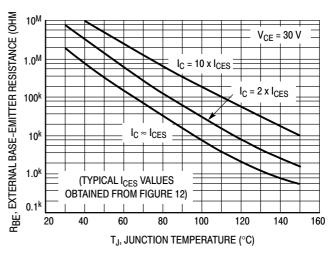


Figure 13. Effects of Base-Emitter Resistance

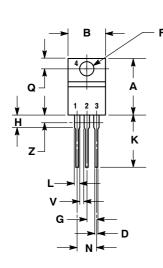
#### **ORDERING INFORMATION**

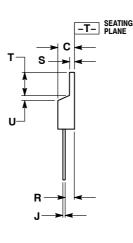
Device	Package	Shipping <sup>†</sup>
BD243B	TO-220	
BD243BG	TO-220 (Pb-Free)	50 Units / Rail
BD243C	TO-220	
BD243CG	TO-220 (Pb-Free)	50 Units / Rail
BD244B	TO-220	
BD244BG	TO-220 (Pb-Free)	50 Units / Rail
BD244C	TO-220	
BD244CG	TO-220 (Pb-Free)	50 Units / Rail

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

## PACKAGE DIMENSIONS

TO-220 CASE 221A-09 ISSUE AG





#### NOTES

- DIMENSIONING AND TOLERANCING PER ANSI
   Y14 5M 1982
- Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.
- DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.036	0.64	0.91
F	0.142	0.161	3.61	4.09
G	0.095	0.105	2.42	2.66
Н	0.110	0.161	2.80	4.10
J	0.014	0.025	0.36	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

#### STYLE 1:

- PIN 1. BASE
  - 2. COLLECTOR
  - EMITTER
     COLLECTOR

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