

SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

CPH6121 — PNP Epitaxial Planar Silicon Transistor DC / DC Converter Applications

Applications

· Relay drivers, lamp drivers, motor drivers, strobes

Features

- · Adoption of MBIT process
- · High current capacitance
- · Low collector-to-emitter saturation voltage
- · High speed switching
- Ultrasmall-sized package permitting applied sets to be made small and slim (0.9mm)
- · High allowable power dissipation

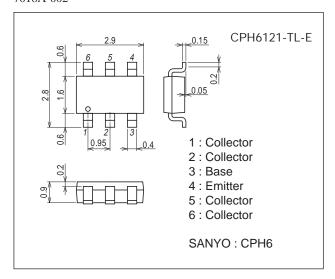
Specifications

Absolute Maximum Ratings at Ta=25°C

	•			
Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		-15	V
Collector-to-Emitter Voltage	VCEO		-12	V
Emitter-to-Base Voltage	VEBO		-5	V
Collector Current	IC		-3	А
Collector Current (Pulse)	ICP		-5	А
Base Current	IB		-600	mA
Collector Dissipation	PC	Mounted on a ceramic board (600mm ² ×0.8mm)	1.3	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Package Dimensions

unit : mm (typ) 7018A-002



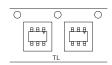
Product & Package Information

• Package : CPH6

• JEITA, JEDEC : SC-74, SOT-26, SOT-457

• Minimum Packing Quantity : 3,000 pcs./reel

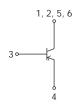
Packing Type: TL



Marking



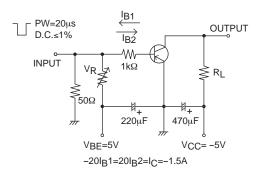
Electrical Connection



Electrical Characteristics at Ta=25°C

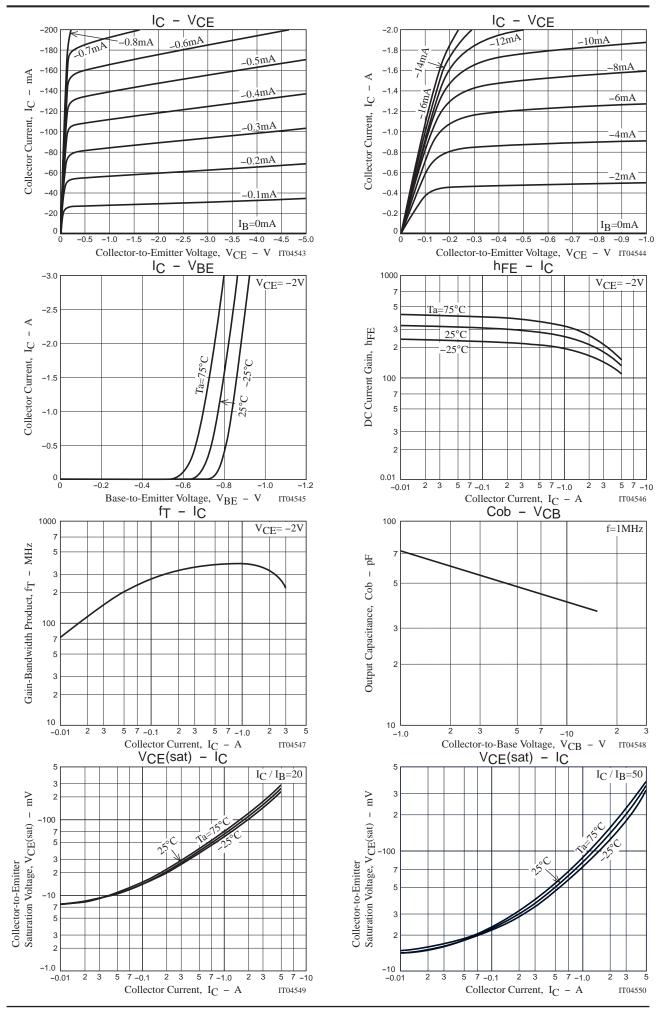
Parameter	Symbol	Conditions	Ratings			Unit	
Parameter	Symbol Conditions		min	typ	max	Offit	
Collector Cutoff Current	ICBO	V _{CB} = -12V, I _E =0A			-0.1	μΑ	
Emitter Cutoff Current	IEBO	V _{EB} = -4V, I _C =0A			-0.1	μΑ	
DC Current Gain	hFE	V _{CE} = -2V, I _C = -500mA	200		560		
Gain-Bandwidth Product	fŢ	V _{CE} = -2V, I _C = -500mA		380		MHz	
Output Capacitance	Cob	V _{CB} = -10V, f=1MHz		40		pF	
Collector-to-Emitter Saturation Voltage	V _{CE} (sat)	I _C = -1.5A, I _B = -30mA		-110	-165	mV	
Base-to-Emitter Saturation Voltage	V _{BE} (sat)	I _C = -1.5A, I _B = -30mA		-0.85	-1.2	V	
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C = -10μA, I _E =0A	-15			V	
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	IC= -1mA, RBE=∞	-12			V	
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E = -10μA, I _C =0A	-5			V	
Turn-On Time	ton			30		ns	
Storage Time	t _{stg}	See specified Test Circuit.		90		ns	
Fall Time	tf			14		ns	

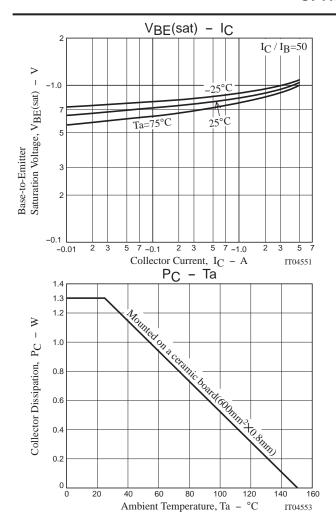
Switching Time Test Circuit

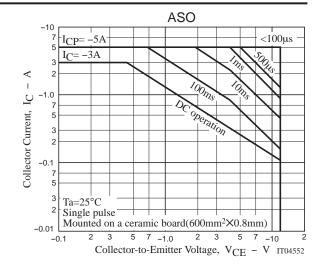


Ordering Information

Device	Device Package		memo	
CPH6121-TL-E			Pb Free	





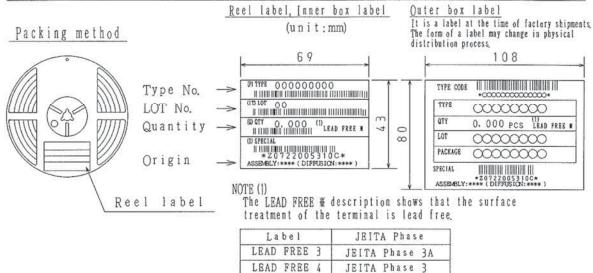


Embossed Taping Specification

CPH6121-TL-E

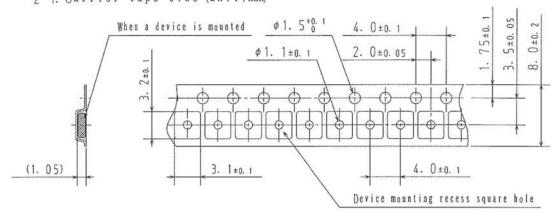
1. Packing Format

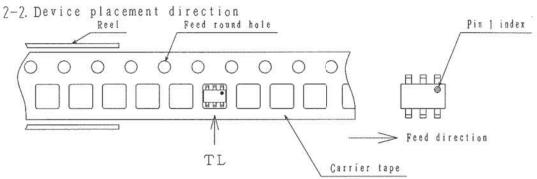
8 38 39	Carrier Tape	Maximum Number of devices contained (pcs)			Packing format		
	Туре	Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)	
СРН6	CPH6	3, 000	15, 000	90, 000		6 inner boxes contained Dimensions:mm (external) $440 \times 195 \times 210$	



2. Taping configuration

2-1. Carrier tape size (unit:mm)





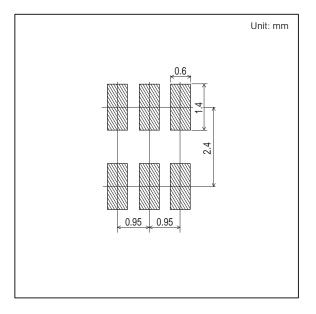
Those with pin 1 index on the feed hole side ·····TL

Outline Drawing

CPH6121-TL-E

Mass (g) Unit 0.015 For reference mm 0. 15^{+0. 1}_{-0. 05} 2. 9±0. 1 0.6±0.1 A 0. 2±0.1 [*1][*1] 0. 05±0.05 2. 8±0. 15 1.6±0.1 [*1] - \$ 0.95 0. 4±0. 1 M A PIN#1 0.05 \$ *1:Lot indication

Land Pattern Example



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