# High Current Surface Mount PNP Silicon Switching Transistor for Load Management in Portable Applications

#### **Features**

- AEC-Q101 Qualified and PPAP Capable
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- Pb-Free Packages are Available\*

# MAXIMUM RATINGS (T<sub>A</sub> = 25°C)

Rating	Symbol	Max	Unit	
Collector-Emitter Voltage	V <sub>CEO</sub>	-35	Vdc	
Collector-Base Voltage	V <sub>CBO</sub>	-55	Vdc	
Emitter-Base Voltage	V <sub>EBO</sub>	-5.0	Vdc	
Collector Current – Continuous	I <sub>C</sub>	-2.0	Adc	
Collector Current - Peak	I <sub>CM</sub>	-5.0	Α	
Electrostatic Discharge	ESD	HBM Class 3 MM Class C		

# THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub> (Note 1)	625 5.0	mW mW/°C
Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub> (Note 1)	200	°C/W
Total Device Dissipation T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub> (Note 2)	1.0 8.0	W mW/°C
Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub> (Note 2)	120	°C/W
Thermal Resistance, Junction-to-Lead #1	$R_{ hetaJL}$	80	°C/W
Total Device Dissipation (Single Pulse < 10 sec.)	P <sub>Dsingle</sub> (Notes 2 & 3)	1.75	W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	–55 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

- 1. FR-4 @ Minimum Pad
- 2. FR-4 @ 1.0 X 1.0 inch Pad
- 3. ref: Figure 9



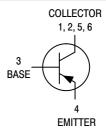
# ON Semiconductor®

http://onsemi.com

# 35 VOLTS 2.0 AMPS PNP TRANSISTOR



CASE 318G TSOP-6 STYLE 6



#### **MARKING DIAGRAM**



G4 = Specific Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

# **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
MBT35200MT1	TSOP-6	3,000 / Tape & Reel
MBT35200MT1G	TSOP-6 (Pb-Free)	3,000 / Tape & Reel
SMBT35200MT1G	TSOP-6 (Pb-Free)	3,000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Typical	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage (I <sub>C</sub> = -10 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	-35	-45	_	Vdc
Collector – Base Breakdown Voltage (I <sub>C</sub> = -0.1 mAdc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	-55	-65	-	Vdc
Emitter – Base Breakdown Voltage (I <sub>E</sub> = -0.1 mAdc, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	-5.0	-7.0	_	Vdc
Collector Cutoff Current (V <sub>CB</sub> = -35 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	-	-0.03	-0.1	μAdc
Collector-Emitter Cutoff Current (V <sub>CES</sub> = -35 Vdc)	I <sub>CES</sub>	-	-0.03	-0.1	μAdc
Emitter Cutoff Current (V <sub>EB</sub> = -4.0 Vdc)	I <sub>EBO</sub>	-	-0.01	-0.1	μAdc
ON CHARACTERISTICS	•		-1	•	
DC Current Gain (Note 1) ( $I_C = -1.0 \text{ A}, V_{CE} = -1.5 \text{ V}$ ) ( $I_C = -1.5 \text{ A}, V_{CE} = -1.5 \text{ V}$ ) ( $I_C = -2.0 \text{ A}, V_{CE} = -3.0 \text{ V}$ )	h <sub>FE</sub>	100 100 100	200 200 200	- 400 -	
	V <sub>CE(sat)</sub>	- - -	-0.125 -0.175 -0.260	-0.15 -0.20 -0.31	V
Base – Emitter Saturation Voltage (Note 1) (I <sub>C</sub> = -1.2 A, I <sub>B</sub> = -0.012 A)	V <sub>BE(sat)</sub>	_	-0.68	-0.85	V
Base – Emitter Turn–on Voltage (Note 1) (I <sub>C</sub> = -2.0 A, V <sub>CE</sub> = -3.0 V)	V <sub>BE(on)</sub>	_	-0.81	-0.875	V
Cutoff Frequency (I <sub>C</sub> = -100 mA, V <sub>CE</sub> = -5.0 V, f = 100 MHz)	f <sub>T</sub>	100	-	-	MHz
Input Capacitance (V <sub>EB</sub> = -0.5 V, f = 1.0 MHz)	Cibo	-	600	650	pF
Output Capacitance (V <sub>CB</sub> = -3.0 V, f = 1.0 MHz)	Cobo	=	85	100	pF
Turn-on Time (V <sub>CC</sub> = -10 V, I <sub>B1</sub> = -100 mA, I <sub>C</sub> = -1 A, R <sub>L</sub> = 3 $\Omega$ )	t <sub>on</sub>	-	35	-	nS
Turn-off Time (V <sub>CC</sub> = -10 V, I <sub>B1</sub> = I <sub>B2</sub> = -100 mA, I <sub>C</sub> = 1 A, R <sub>L</sub> = 3 $\Omega$ )	t <sub>off</sub>	-	225	_	nS

<sup>1.</sup> Pulsed Condition: Pulse Width = 300  $\mu$ sec, Duty Cycle  $\leq$  2%

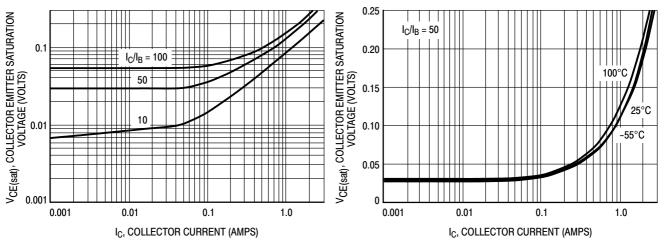


Figure 1. Collector Emitter Saturation Voltage versus Collector Current

Figure 2. Collector Emitter Saturation Voltage versus Collector Current

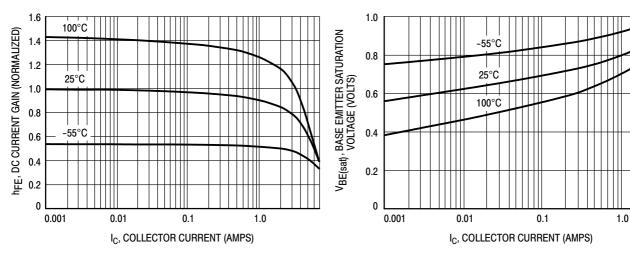


Figure 3. DC Current Gain versus Collector Current

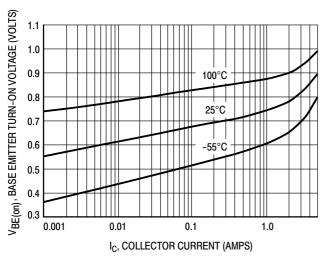


Figure 5. Base Emitter Turn-On Voltage versus Collector Current

Figure 4. Base Emitter Saturation Voltage versus Collector Current

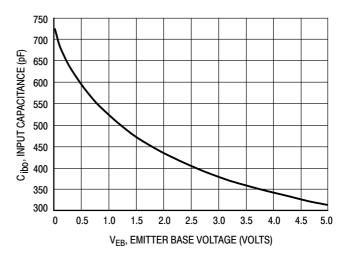
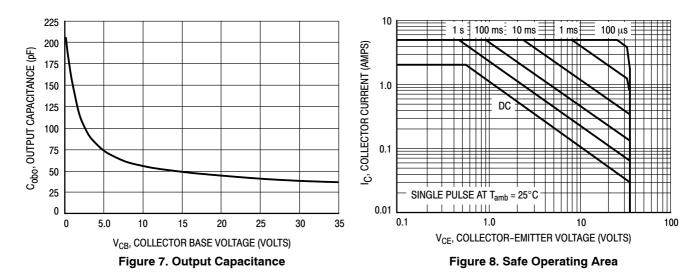


Figure 6. Input Capacitance



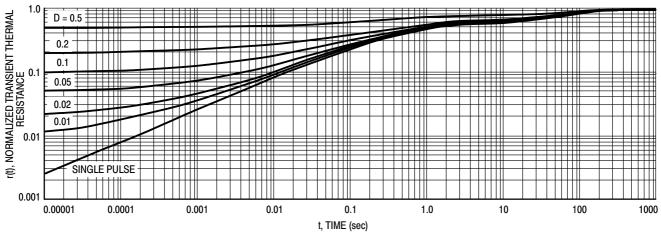
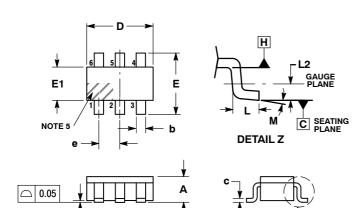


Figure 9. Normalized Thermal Response

# PACKAGE DIMENSIONS

## TSOP-6 CASE 318G-02 **ISSUE U**



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS.
  MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM
- LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
  DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH,
  PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.15 PER SIDE. DIMENSIONS D AND E1 ARE DETERMINED AT DATUM H. 5. PIN ONE INDICATOR MUST BE LOCATED IN THE INDICATED ZONE.

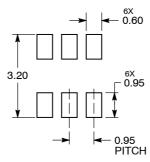
	MILLIMETERS			
DIM	MIN	NOM	MAX	
Α	0.90	1.00	1.10	
A1	0.01	0.06	0.10	
b	0.25	0.38	0.50	
С	0.10	0.18	0.26	
D	2.90	3.00	3.10	
E	2.50	2.75	3.00	
E1	1.30	1.50	1.70	
е	0.85	0.95	1.05	
L	0.20	0.40	0.60	
L2	0.25 BSC			
M	0°	_	10°	

#### STYLE 6:

- PIN 1. COLLECTOR 2. COLLECTOR
  - 3 BASE

  - 4. EMITTER
  - 5. COLLECTOR 6. COLLECTOR

## RECOMMENDED **SOLDERING FOOTPRINT\***



**DIMENSIONS: MILLIMETERS** 

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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