Amplifier Transistors

NPN Silicon

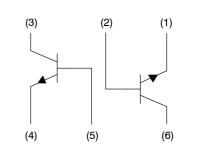
Features

• These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant



ON Semiconductor®

http://onsemi.com



Unit

Vdc

Vdc

Vdc

mAdc

6.0

200

V_{EBO}

 I_{C}



SC-88 (SOT-363) CASE 419B

MARKING DIAGRAM



= Specific Device Code

= Date Code

1T

Μ

= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]	
MBT6429DW1T1G	SC-88 (Pb-Free)	3000 / Tape & Reel	

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Publication Order Number:

MBT6429DW1T1/D

MAXIMUM RATINGS				
Rating	Symbol	Value		
Collector – Emitter Voltage	V _{CEO}	45		
Collector - Base Voltage	V _{CBO}	55		

THERMAL CHARACTERISTICS

Collector Current - Continuous

Emitter-Base Voltage

Characteristic	Symbol	Max	Unit
Total Device Dissipation (Note 1) T _A = 25°C	P _D	150	mW
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	833	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Device mounted on FR4 glass epoxy printed circuit board using the minimum recommended foot print.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit		
OFF CHARACTERISTICS						
Collector – Emitter Breakdown Voltage $(I_{C} = 1.0 \text{ mAdc}, I_{B} = 0)$	V _{(BR)CEO}	45	-	Vdc		
Collector – Base Breakdown Voltage $(I_{C} = 0.1 \text{ mAdc}, I_{E} = 0)$	V _{(BR)CBO}	55	_	Vdc		
Collector Cutoff Current (V _{CE} = 30 Vdc)	I _{CES}	_	0.1	μAdc		
Collector Cutoff Current $(V_{CB} = 30 \text{ Vdc}, I_E = 0)$	I _{CBO}	_	0.01	μAdc		
Emitter Cutoff Current ($V_{EB} = 5.0 \text{ Vdc}, I_C = 0$)	I _{EBO}	_	0.01	μAdc		
ON CHARACTERISTICS						
$ \begin{array}{l} \text{DC Current Gain} \\ (I_{C} = 0.01 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}) \\ (I_{C} = 0.1 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}) \\ (I_{C} = 1.0 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}) \\ (I_{C} = 10 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}) \end{array} $	h _{FE}	500 500 500 500	_ 1250 _ _	_		
Collector – Emitter Saturation Voltage ($I_C = 10 \text{ mAdc}, I_B = 0.5 \text{ mAdc}$) ($I_C = 100 \text{ mAdc}, I_B = 5.0 \text{ mAdc}$)	V _{CE(sat)}		0.2 0.6	Vdc		
Base – Emitter On Voltage (I _C = 1.0 mAdc, V _{CE} = 5.0 Vdc)	V _{BE(on)}	0.56	0.66	Vdc		
SMALL-SIGNAL CHARACTERISTICS						
Current – Gain – Bandwidth Product ($I_C = 1.0 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}, f = 100 \text{ MHz}$)	fT	100	700	MHz		
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 1.0 MHz)	C _{obo}	_	3.0	pF		
Input Capacitance (V _{EB} = 0.5 Vdc, I _C = 0, f = 1.0 MHz)	C _{ibo}	-	8.0	pF		

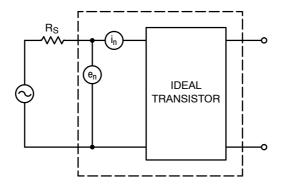


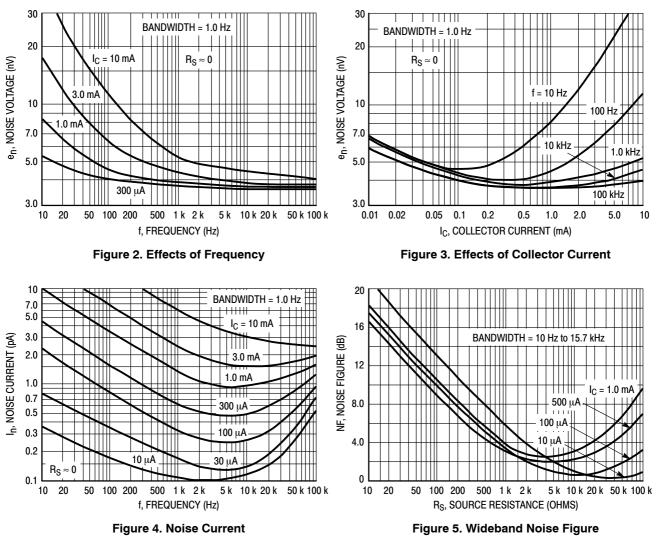
Figure 1. Transistor Noise Model

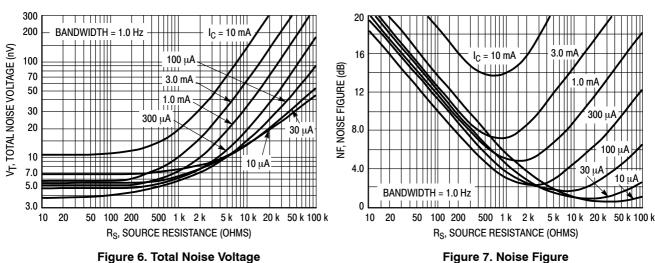
www.BDhtp://html.comcom/ON/

NOISE CHARACTERISTICS

 $(V_{CE} = 5.0 \text{ Vdc}, \text{ T}_{A} = 25^{\circ}\text{C})$

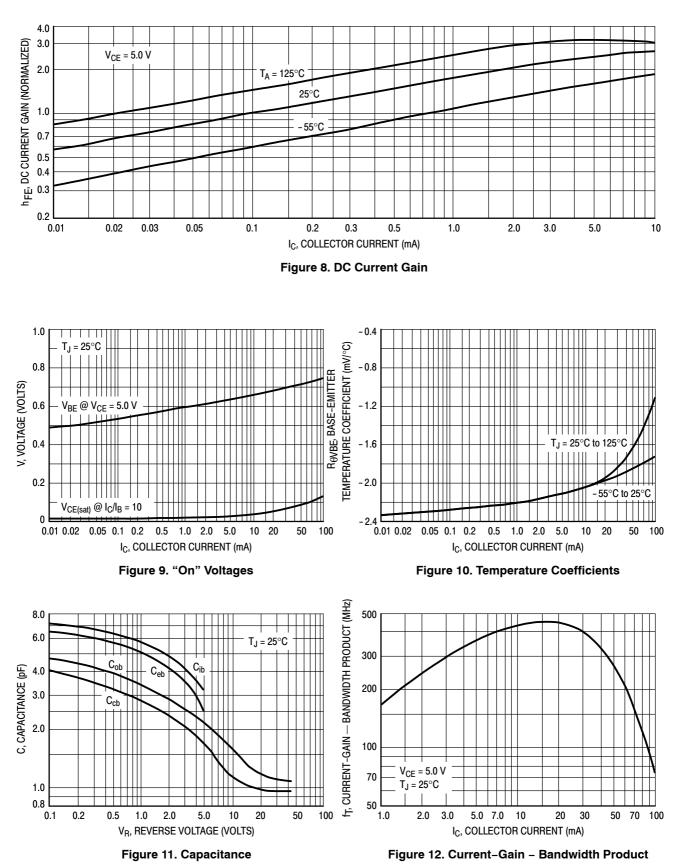
NOISE VOLTAGE





100 Hz NOISE DATA

www.BDhtp://disemi.comcom/ON/



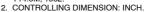
www.BDhtp://hsmil.com.com/ON/

PACKAGE DIMENSIONS

SC-88/SC70-6/SOT-363

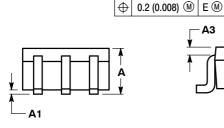
CASE 419B-02 ISSUE W

NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M. 1982.



3. 419B-01 OBSOLETE, NEW STANDARD 419B-02.

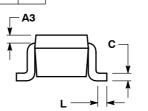
	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.80	0.95	1.10	0.031	0.037	0.043
A1	0.00	0.05	0.10	0.000	0.002	0.004
A3	0.20 REF			0.008 REF		
b	0.10	0.21	0.30	0.004	0.008	0.012
С	0.10	0.14	0.25	0.004	0.005	0.010
D	1.80	2.00	2.20	0.070	0.078	0.086
Е	1.15	1.25	1.35	0.045	0.049	0.053
е	0.65 BSC			0.026 BSC		
L	0.10	0.20	0.30	0.004	0.008	0.012
HE	2.00	2.10	2.20	0.078	0.082	0.086



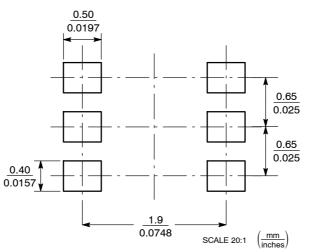
-E-

b 6 PL

HE



SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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