High Voltage Transistor

PNP Silicon

Features

• These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	-150	Vdc
Collector – Base Voltage	V _{CBO}	-160	Vdc
Emitter-Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current – Continuous	Ι _C	-500	mAdc

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 2) $T_A = 25^{\circ}C$ Derate Above 25^{\circ}C	P _D	400 3.2	mW mW/°C
Thermal Resistance, Junction-to-Ambient	R _{θJA}	312	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

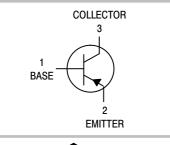
1. FR-5 @ 100 mm², 0.5 oz. copper traces, still air.

2. FR-5 = $1.0 \times 0.75 \times 0.062$ in.



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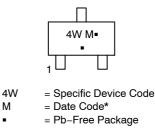
http://onsemi.com





SC-70 (SOT-323) **CASE 419** STYLE 3

MARKING DIAGRAM



(Note: Microdot may be in either location)

Μ

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBT5401WT1G	SC–70 (Pb–Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS	· · ·			
Collector – Emitter Breakdown Voltage $(I_{C} = -1.0 \text{ mAdc}, I_{B} = 0)$	V _{(BR)CEO}	-150	_	Vdc
Collector – Base Breakdown Voltage $(I_{C} = -100 \ \mu Adc, I_{E} = 0)$	V _{(BR)CBO}	-160	_	Vdc
Emitter – Base Breakdown Voltage $(I_E = -10 \ \mu Adc, I_C = 0)$	V _{(BR)EBO}	-5.0	_	Vdc
Collector–Base Cutoff Current $(V_{CB} = -120 \text{ Vdc}, I_E = 0)$ $(V_{CB} = -120 \text{ Vdc}, I_E = 0, T_A = 100^{\circ}\text{C})$	Ісво	-	-50 -50	nAdc μAdc
ON CHARACTERISTICS				
DC Current Gain $(I_{C} = -1.0 \text{ mAdc}, V_{CE} = -5.0 \text{ Vdc})$ $(I_{C} = -10 \text{ mAdc}, V_{CE} = -5.0 \text{ Vdc})$ $(I_{C} = -50 \text{ mAdc}, V_{CE} = -5.0 \text{ Vdc})$	h _{FE}	50 60 50	_ 240 _	-
Collector – Emitter Saturation Voltage ($I_C = -10$ mAdc, $I_B = -1.0$ mAdc) ($I_C = -50$ mAdc, $I_B = -5.0$ mAdc)	V _{CE(sat)}	-	-0.2 -0.5	Vdc
Base – Emitter Saturation Voltage $(I_C = -10 \text{ mAdc}, I_B = -1.0 \text{ mAdc})$ $(I_C = -50 \text{ mAdc}, I_B = -5.0 \text{ mAdc})$	V _{BE(sat)}	-	-1.0 -1.0	Vdc
SMALL-SIGNAL CHARACTERISTICS				
Current – Gain — Bandwidth Product ($I_C = -10$ mAdc, $V_{CE} = -10$ Vdc, f = 100 MHz)	fT	100	300	MHz
Output Capacitance $(V_{CB} = -10 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz})$	C _{obo}	-	6.0	pF
Small Signal Current Gain (I _C = –1.0 mAdc, V _{CE} = –10 Vdc, f = 1.0 kHz)	h _{fe}	40	200	-
				-

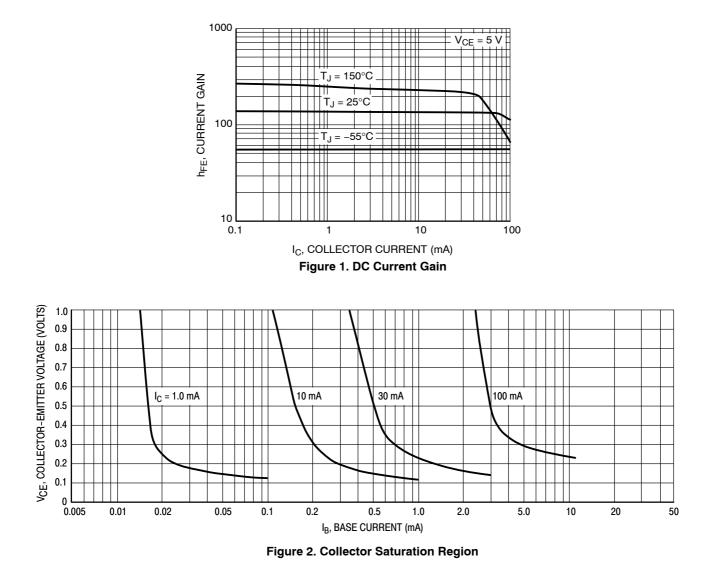
dB

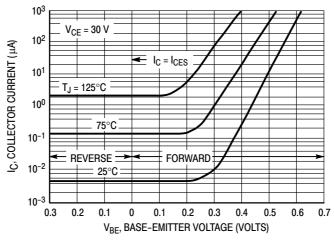
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NF

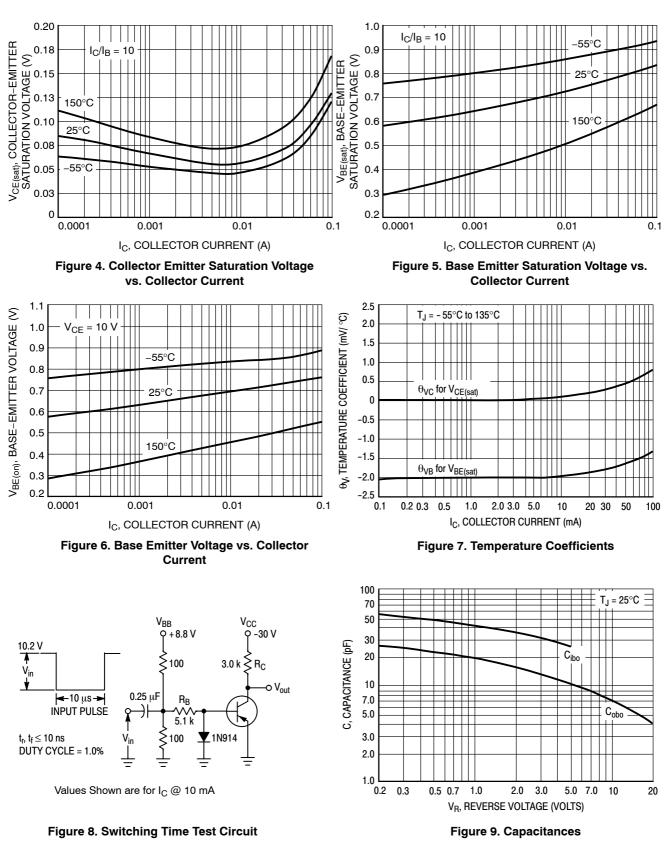
8.0

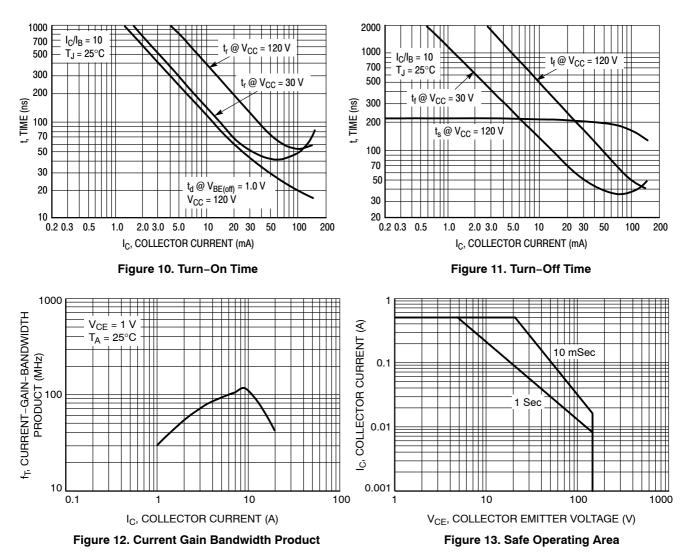
Noise Figure	
(I_C = -200 $\mu Adc, V_{CE}$ = -5.0 Vdc, R_S = 10 Ω, f = 1.0 kHz)





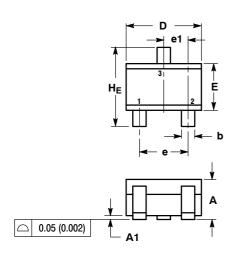






PACKAGE DIMENSIONS

SC-70 (SOT-323) CASE 419-04 ISSUE N



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH

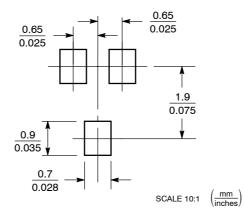
	MILLIMETERS				INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.80	0.90	1.00	0.032	0.035	0.040	
A1	0.00	0.05	0.10	0.000	0.002	0.004	
A2	0.70 REF			0.028 REF			
b	0.30	0.35	0.40	0.012	0.014	0.016	
c	0.10	0.18	0.25	0.004	0.007	0.010	
D	1.80	2.10	2.20	0.071	0.083	0.087	
Е	1.15	1.24	1.35	0.045	0.049	0.053	
е	1.20	1.30	1.40	0.047	0.051	0.055	
e1	0.65 BSC			0.026 BSC			
L	0.20	0.38	0.56	0.008	0.015	0.022	
HE	2.00	2.10	2.40	0.079	0.083	0.095	

STYLE 3: PIN 1. BASE

2. EMITTER 3. COLLECTOR

SOLDERING FOOTPRINT*

Α2



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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