

SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

TF252 — Electret Condenser Microphone Applications

Features

- High gain : GV=1.0dB typ (V_{CC}=2V, R_L=2.2k Ω , Cin=5pF, V_{IN}=10mV, f=1kHz)
- Ultrasmall package facilitates miniaturization in end products [1.0mm×0.6mm×0.27mm (max 0.3mm)]
- Best suited for use in Electret Condenser Microphone for audio equipments and telephones
- · Excellent voltage characteristics
- · Excellent transient characteristics
- · Adoption of FBET process
- · Halogen free compliance
- · Protection diode in

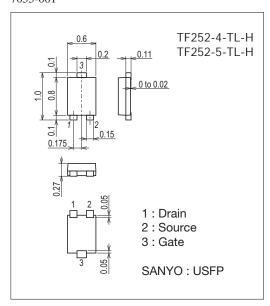
Specifications

Absolute Maximum Ratings at Ta=25°C

| Parameter | Symbol | Conditions | Ratings | Unit |
|-----------------------------|------------------|------------|-------------|------|
| Gate-to-Drain Voltage | V _{GDO} | | -20 | V |
| Gate Current | IG | | 10 | mA |
| Drain Current | ID | | 1 | mA |
| Allowable Power Dissipation | PD | | 30 | mW |
| Junction Temperature | Tj | | 150 | °C |
| Storage Temperature | Tstg | | -55 to +150 | °C |

Package Dimensions

unit : mm (typ) 7055-001



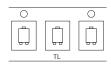
Product & Package Information

• Package : USFP

• JEITA, JEDEC : -

• Minimum Packing Quantity : 10,000 pcs./reel

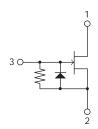
Packing Type: TL



Marking



Electrical Connection



Electrical Characteristics at Ta=25°C

| Parameter | Cumbal | Conditions | Ratings | | | Unit | |
|---|-----------------------------|---|---------|------|------|------|--|
| Parameter | Parameter Symbol Conditions | | min | typ | max | Uill | |
| Gate-to-Drain Breakdown Voltage | V(BR)GDO | IG=-100μA | -20 | | | V | |
| Cutoff Voltage | V _{GS} (off) | V _{DS} =2V, I _D =1μA | -0.1 | -0.4 | -1.0 | V | |
| Drain Current | IDSS | V _{DS} =2V, V _{GS} =0V | 140* | | 350* | μΑ | |
| Forward Transfer Admittance | yfs | V _{DS} =2V, V _{GS} =0V, f=1kHz | 0.8 | 1.4 | | mS | |
| Input Capacitance | Ciss | VDQ 2V VQQ QV f 1MHz | | 3.1 | | pF | |
| Reverse Transfer Capacitance | Crss | VDS=2V, VGS=0V, f=1MHz | | 0.95 | | pF | |
| [Ta=25°C, V _{CC} =2V, R _L =2.2kΩ, Cin=5pF, See specified Test Circuit.] | | | | | | | |
| Voltage Gain | Gy | V _{IN} =10mV, f=1kHz | | 1.0 | | dB | |
| Reduced Voltage Characteristic | ΔGVV | V _{IN} =10mV, f=1kHz, V _{CC} =2.0V → 1.5V | | -0.6 | -2.0 | dB | |
| Frequency Characteristic | ΔGvf | f=1kHz to 110Hz | | | -1.0 | dB | |
| Total Harmonic Distortion | THD | V _{IN} =30mV, f=1kHz | | 0.65 | | % | |
| Output Noise Voltage | V _{NO} | V _{IN} =0V, A curve | | -106 | -102 | dB | |

* : The TF252 is classified by IDSS as follows : (unit : $\mu A)$

| Rank | 4 | 5 | | |
|------|------------|------------|--|--|
| IDSS | 140 to 240 | 210 to 350 | | |

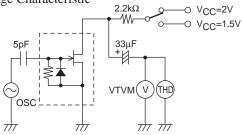
Test Circuit

Voltage gain

Frequency Characteristic

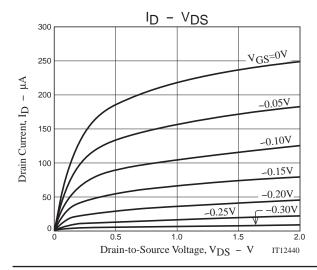
Distortion

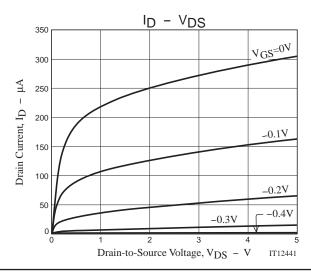
Reduced Voltage Characteristic

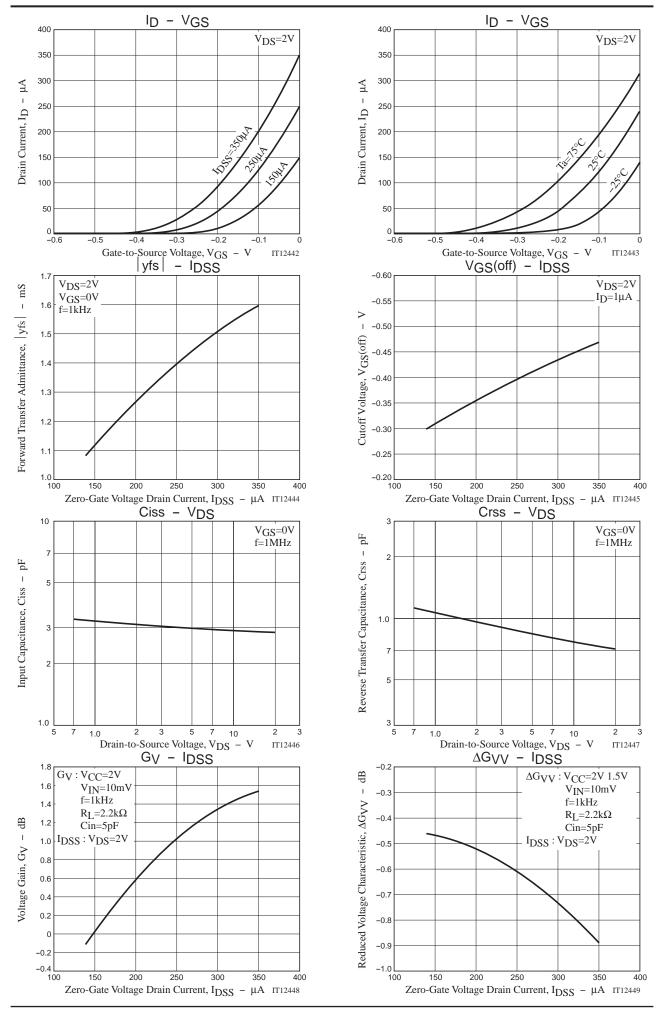


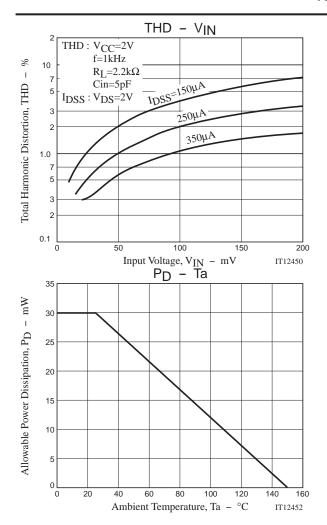
Ordering Information

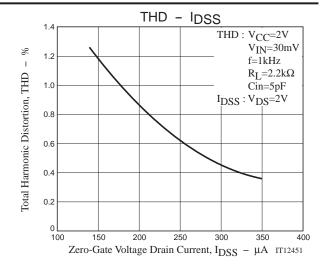
| Device | Package | Shipping | memo | |
|--------------|---------|-----------------|--------------------------|--|
| TF252-4-TL-H | USFP | 10,000pcs./reel | Db Free and Helegen Free | |
| TF252-5-TL-H | USFP | 10,000pcs./reel | Pb Free and Halogen Free | |









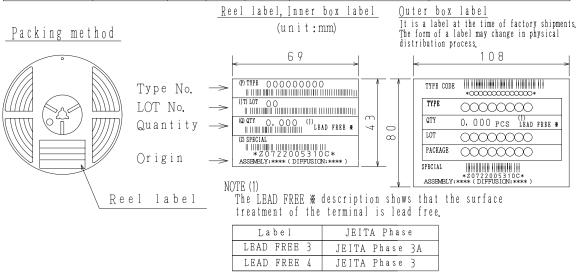


Taping Specification

TF252-4-TL-H, TF252-5-TL-H

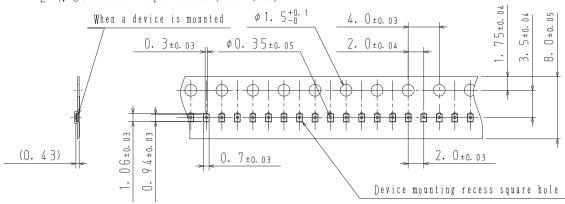
1. Packing Format

| Package Name | Carrier Tape | Maximum Number of devices contained (pcs) | | | Packing format | | |
|--------------|--------------|--|-----------|-----------|--------------------------|--------------------------|--|
| | Туре | Reel | Inner box | Outer box | Inner $BOX(C-1)$ | Outer BOX (A-7) | |
| USFP | USFP | 10,000 | 50,000 | 300,000 | 5 reels contained | 6 inner boxes contained | |
| | | | | | Dimensions:mm (external) | Dimensions:mm (external) | |
| | | | | | 183×72×185 | 440×195×210 | |

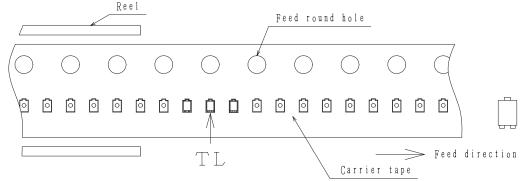


2. Taping configuration

2-1. Carrier tape size (unit:mm)



2-7. Device placement direction



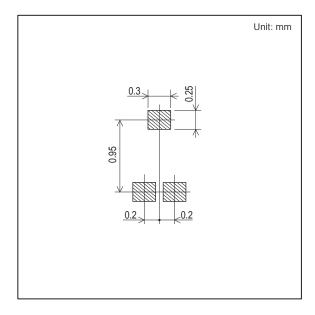
Those with one electrode terminal on the feed hole side·····TL

Outline Drawing

TF252-4-TL-H, TF252-5-TL-H

Mass (g) Unit (0.0005) mm 0.6+0.02 0.11**0.05 0.11**0.05 0.10**0.02 0.10**0.05 0.10*

Land Pattern Example



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