

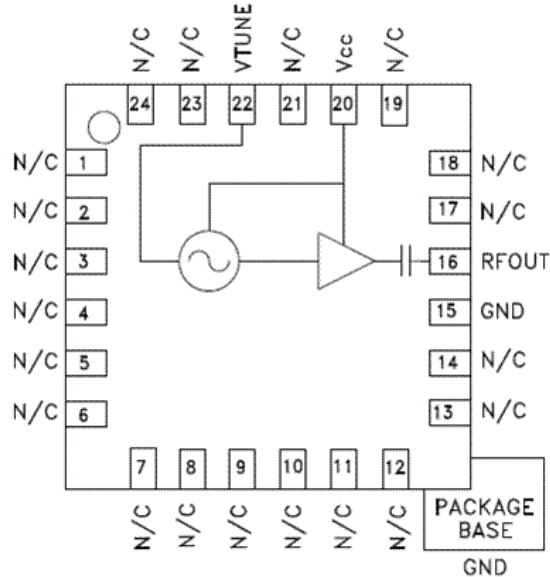


Features

- 4.45 GHz to 5.2 GHz Operation
- -108 dBc/Hz Phase Noise at 100 KHz Offset
- +8.0 dBm P_{OUT}
- No External Resonator or Elements Needed
- 4 mm x 4 mm QFN Package
- 3V V_{CC} Operation

Applications

- Instrumentation
- Military
- Aerospace
- Point-to-Point Radio
- Test Equipment
- VSAT
- CATV



TOP VIEW
Functional Block Diagram

Product Description

RFMD's RFVC1821 is a 3V InGaP MMIC VCO with an integrated buffer amplifier operating over a frequency range of 4.45GHz to 5.2GHz. Its monolithic tuning structure provides excellent temperature, shock, and vibration performance while its integrated buffer amplifier provides an output power of +8dBm from a 3V supply. Phase noise is -108dBc/Hz at 100kHz offset. The RFVC1821 is available in a low cost leadless ceramic 4mmx4mm surface mount QFN outline.

Ordering Information

RFVC1821S2	2 piece sample bag
RFVC1821PCK-410	PCBA with 2 piece sample bag
RFVC1821SB	5 piece bag
RFVC1821SQ	25 piece bag
RFVC1821SR	100 pieces on 7" reel
RFVC1821TR7	750 pieces on 7" reel

Optimum Technology Matching® Applied

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|---|--------------------------------------|-------------------------------------|------------------------------------|
| <input type="checkbox"/> GaAs HBT | <input type="checkbox"/> SiGe BiCMOS | <input type="checkbox"/> GaAs pHEMT | <input type="checkbox"/> GaN HEMT |
| <input type="checkbox"/> GaAs MESFET | <input type="checkbox"/> Si BiCMOS | <input type="checkbox"/> Si CMOS | <input type="checkbox"/> BiFET HBT |
| <input checked="" type="checkbox"/> InGaP HBT | <input type="checkbox"/> SiGe HBT | <input type="checkbox"/> Si BJT | <input type="checkbox"/> LD MOS |

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Absolute Maximum Ratings

Parameter	Rating	Unit
Bias Voltage (V_{DD})	+3.25	V_{DC}
V_{TUNE}	15	V_{DC}
Operating Junction Temperature (T_J)	99	$^{\circ}C$
Continuous Power Dissipation ($T = +85^{\circ}C$)	185	mW
Thermal Resistance (Pad to Die Bottom)	10	$^{\circ}C/W$
Storage Temperature	-40 to +150	$^{\circ}C$
Operating Temperature	-40 to +85 $^{\circ}C$	$^{\circ}C$
ESD JESD22-A114 Human Body Model (HBM)	Class 0, 150V	



Caution! ESD sensitive device.

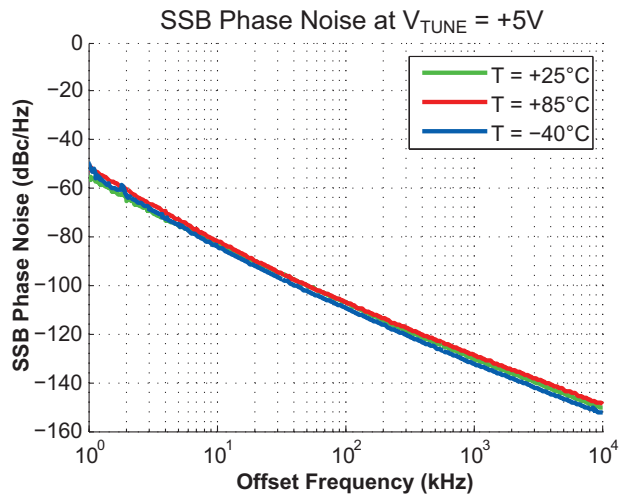
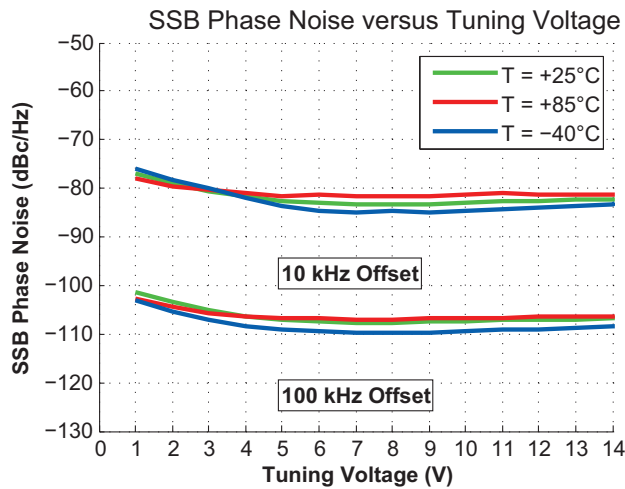
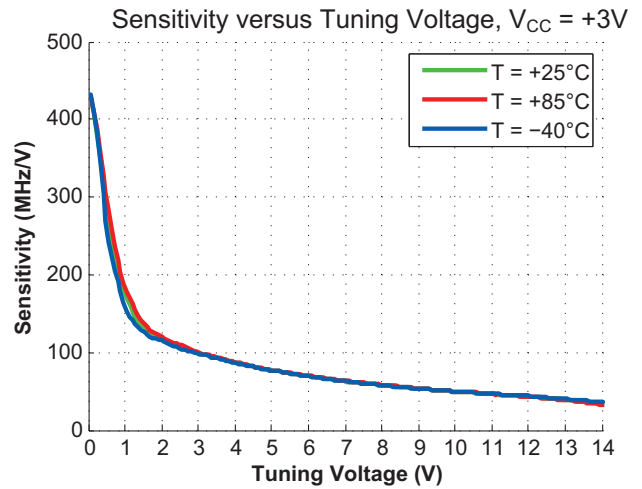
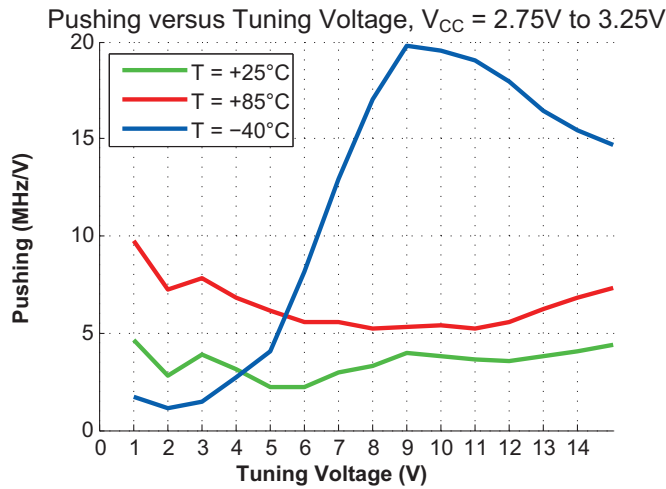
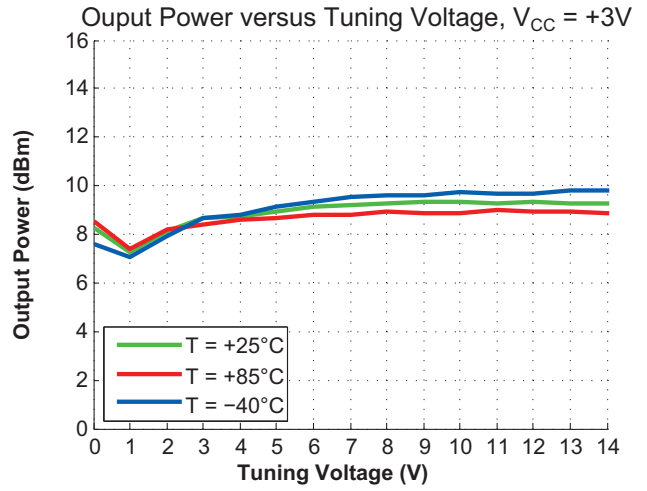
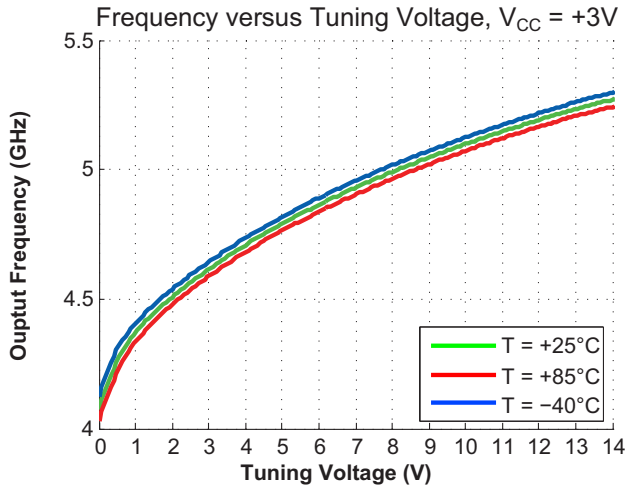
Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

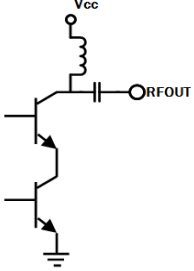
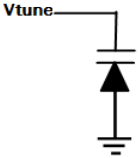
RoHS status based on EUDirective2002/95/EC (at time of this document revision).

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Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Electrical Specifications					$T_A = +25^{\circ}C$, $V_{CC} = +3.0V_{DC}$
Operating Frequency	4.45		5.2	GHz	
V_{TUNE}	1.5		14.5	V	
V_{TUNE} Leakage Current		0.3	1.0	μA	At $V_{TUNE} = 10V$
Output Power	5	8		dBm	At $V_{TUNE} = 5V$
Phase Noise at 10kHz Offset		-84		dBc/Hz	At $V_{TUNE} = 5V$
Phase Noise at 100kHz Offset		-108		dBc/Hz	At $V_{TUNE} = 5V$
Harmonics					
2nd		-14		dBc	At $V_{TUNE} = 5V$
3rd		-37		dBc	
Output Spurious			-70	dBc	
Output Return Loss		10		dB	
Supply Current		43	50	mA	At $V_{TUNE} = 5V$
Pulling		5.5		MHz	VSWR 2.5:1 all phases
Pushing		2.2		MHz/V	At $V_{TUNE} = 5V$
Frequency Drift		-0.36		MHz/C	At $V_{TUNE} = 5V$

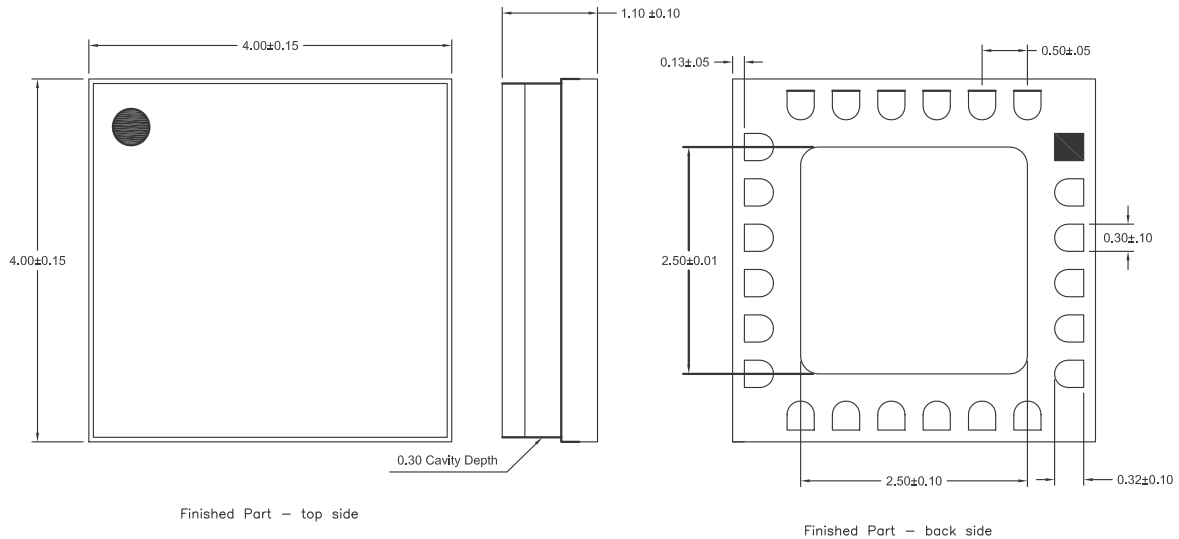
Typical Electrical Performance



Pin	Function	Description	Interface Schematic
15	GND	Connect directly to PCB ground for best performance.	
16	RFOUT	RF out. This pad is AC coupled and matched for optimum P_{OUT} . A 50Ω termination is recommended for this pin.	
20	VCC	Connect 3V to power both the oscillator core and the buffer amplifier.	
22	VTUNE	Direct connection to the varactor diodes used to vary the frequency of oscillation.	
Pkg Base	GND	Ground connection. Solder package bottom directly to ground plane for best performance.	

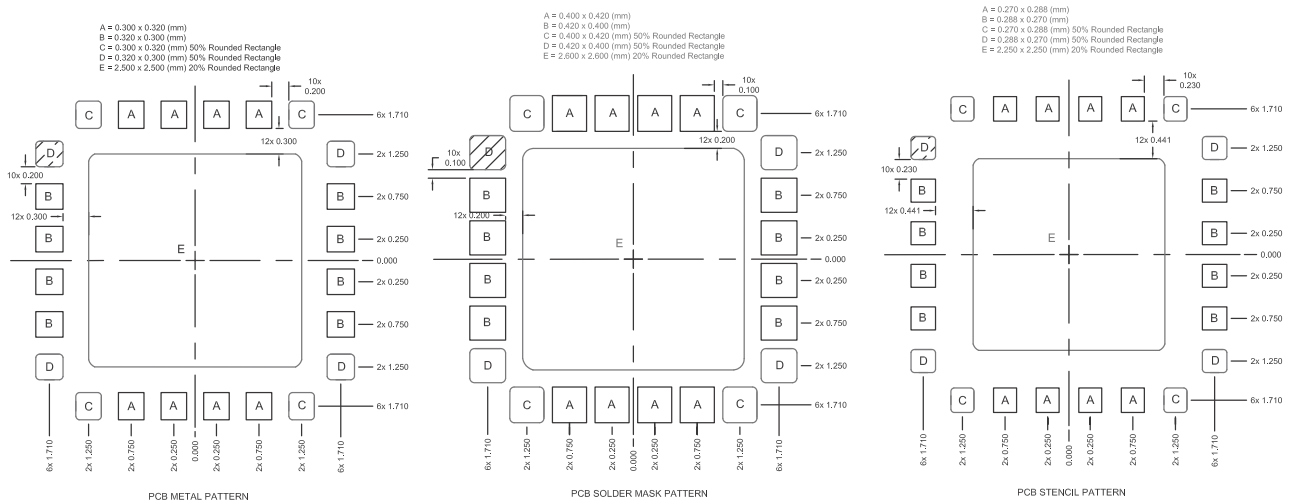
Note: All dimensions in millimeters.

Package Drawing



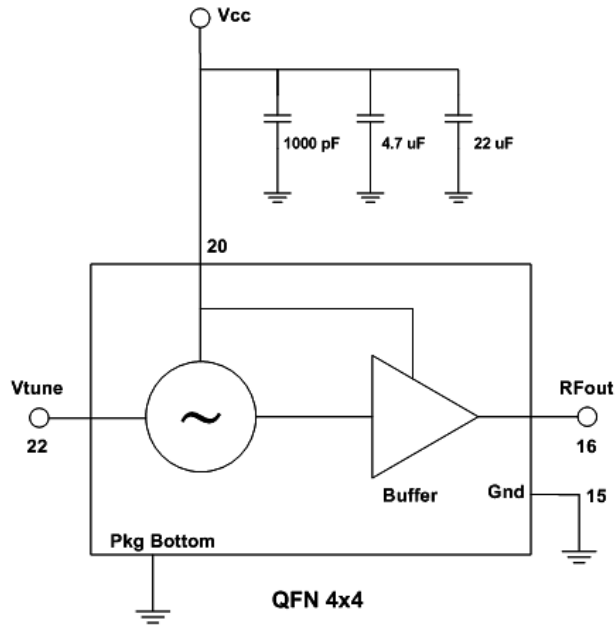
1. Dimensions in mm.
2. Dimensions are for reference only.
3. Package body material: Alumina.
4. Lead and paddle plating: Au, 30u" minimum.

Recommended PCB Layout



- Notes:**
1. Shaded area represents Pin 1 location.

Application Circuit Block Diagram



Evaluation Board Layout

