SGA5586Z





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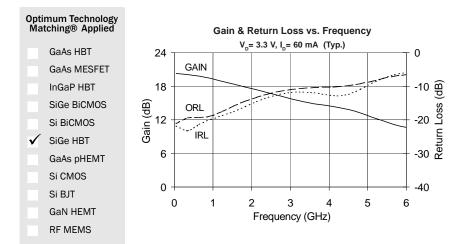
Package: SOT-86



Product Description

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The SGA5586Z is a high performance SiGe HBT MMIC Amplifier. A Darlington configuration featuring one-micron emitters provides high F_T and excellent thermal performance. The heterojunction increases breakdown voltage and minimizes leakage current between junctions. Cancellation of emitter junction non-linearities results in higher suppression of intermodulation products. Only two DC-blocking capacitors, a bias resistor, and an optional RF choke are required for operation.



Features

- High Gain: 18.7 dB at 1950 MHz
- Cascadable 50Ω
- Operates from Single Supply
- Low Thermal Resistance Package

Applications

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- IF Amplifier

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Wireless Data, Satellite

Parameter	Specification			Unit	Condition	
	Min.	Тур.	Max.	Unit	Condition	
Small Signal Gain		23.1		dB	850MHz	
		18.7		dB	1950MHz	
		17.3		dB	2400MHz	
Output Power at 1dB Compression		18.1		dBm	850MHz	
		15.8		dBm	1950MHz	
Output Third Intercept Point		31.6		dBm	850MHz	
		28.8		dBm	1950MHz	
Bandwidth Determined by Return Loss		4000		MHz	>10dB	
Input Return Loss		12.2		dB	1950MHz	
Output Return Loss		20.7		dB	1950MHz	
Noise Figure		2.6		dB	1950MHz	
Device Operating Voltage	3.5	3.9	4.2	V		
Device Operating Current	54	60	66	mA		
Thermal Resistance (Junction - Lead)		97		°C/W		

Test Conditions: $V_S = 8V$, $I_D = 60$ mA Typ., OIP₃ Tone Spacing = 1 MHz, P_{OUT} per tone = 0 dBm, $R_{BIAS} = 68\Omega$, $T_L = 25$ °C, $Z_S = Z_L = 50\Omega$

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SGA5586Z

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Absolute Maximum Ratings

Parameter	Rating	Unit
Max Device Current (I _D)	120	mA
Max Device Voltage (V _D)	6	V
Max RF Input Power	+16	dBm
Max Junction Temp (T _J)	+150	°C
Operating Temp Range (T _L)	-40 to +85	°C
Max Storage Temp	+150	°C

Operation of this device beyond any one of these limits may cause permanent dam-age. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one. Bias conditions should also satisfy the following expression:

 $I_D V_D < (T_J - T_L) / R_{TH}$, j-l

Typical Performance at Key Operating Frequencies



Caution! ESD sensitive device.

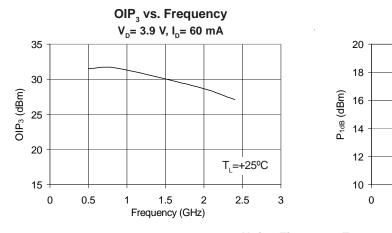
Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical perfor-mance or functional operation of the device under Absolute Maximum Rating condi-tions is not implied.

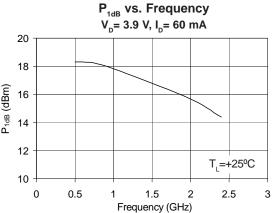
RoHS status based on EUDirective2002/95/EC (at time of this document revision).

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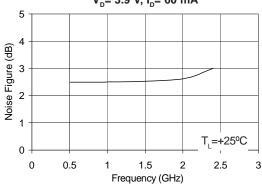
Parameter	Unit	100	500	850	1950	2400	3500
		MHz	MHz	MHz	MHz	MHz	MHz
Small Signal Gain	dB	26.0	24.7	23.1	18.7	17.3	14.8
Output Third Order Intercept Point	dBm		31.5	31.6	28.8	27.1	
Output Power at 1dB Compression	dBm		18.3	18.1	15.8	14.4	
Input Return Loss	dB	22.3	13.9	11.8	12.2	12.3	10.6
Output Return Loss	dB	19.8	18.6	18.8	20.7	17.5	14.2
Reverse Isolation	dB	27.7	27.4	27.0	23.4	21.5	17.5
Noise Figure	dB		2.5	2.5	2.6	3.0	

Test Conditions: $V_S = 8V$, $I_D = 60$ mA Typ., OIP₃ Tone Spacing = 1MHz, P_{OUT} per tone = 0dBm, $R_{BIAS} = 75\Omega$, $T_L = 25^{\circ}$ C, $Z_S = Z_L = 50\Omega$





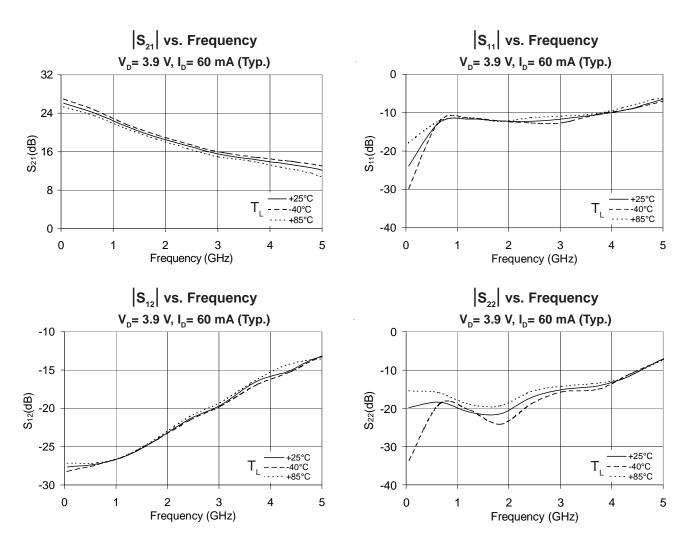
Noise Figure vs. Frequency $V_{\rm D}$ = 3.9 V, $I_{\rm D}$ = 60 mA



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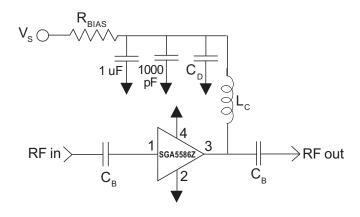
Pin
Function
Description

1
RF IN
RF input pin. This pin requires the use of an external DC-blocking capacitor chosen for the frequency of operation.

2, 4
GND
Connection to ground. For optimum RF performance, use via holes as close to ground leads as possible to reduce lead inductance.

3
RF OUT/BIAS
RF output and bias pin. DC voltage is present on this pin, therefore a DC blocking capacitor is necessary for proper oper

Application Schematic

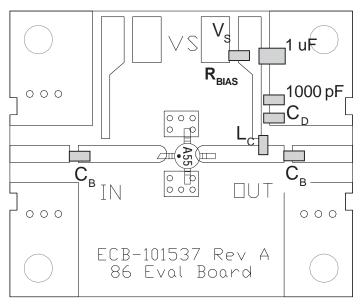


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Reference	Frequency (Mhz)					
Designator	500	850	1950	2400	3500	
C _B	220 pF	100 pF	68 pF	56 pF	39 pF	
C _D	100 pF	68 pF	22 pF	22 pF	15 pF	
L _c	68 nH	33 nH	22 nH	18 nH	15 nH	

Recommended Bias Resistor Values for I_p =60mA R_{BIAS} =(V_s - V_p) / I_p				
Supply Voltage(V _S)	6 V	8 V	10 V	12 V
R _{BIAS} 36 Ω 68 Ω 100 Ω 130 Ω				
Note: $\mathbf{R}_{_{\text{BIAS}}}$ provides DC bias stability over temperature.				

Evaluation Board Layout

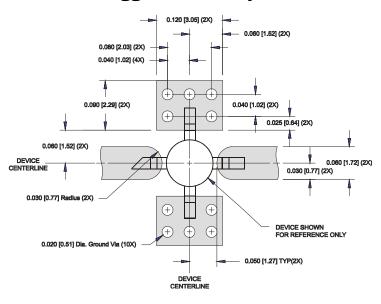


Mounting Instructions

- 1. Use a large ground pad area under device pins 2 and 4 with many plated through-holes as shown.
- 2. We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31 mil thick FR-4 board with 1 ounce copper on both sides.



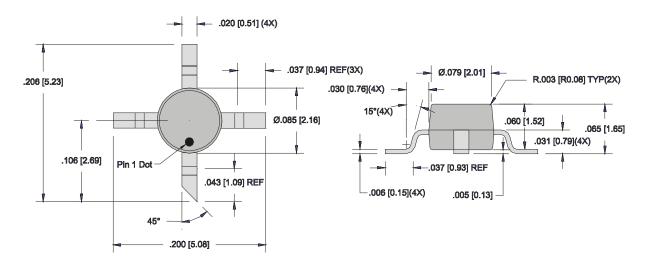




Suggested Pad Layout

Package Drawing

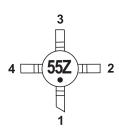
Dimensions in inches (millimeters) Refer to drawing posted at www.rfmd.com for tolerances.







Part Identification



Ordering Information

Ordering Code	Description
SGA5586Z	13" Reel with 3000 pieces
SGA5586ZSQ	Sample bag with 25 pieces
SGA5586ZSR	7" Reel with 100 pieces
SGA5586ZPCK1	850MHz, 8V Operation PCBA with 5-piece sample bag

