

## LM119-LM219-LM319

## High speed dual comparators

### Features

- Two independent comparators
- Supply voltage: +5V to ±15V
- Typically 80ns response time at ±15V
- Minimum fan-out of 2 each side
- Maximum input current of 1µa over operating temperature range
- Inputs and outputs can be isolated from system ground
- High common-mode slew rate

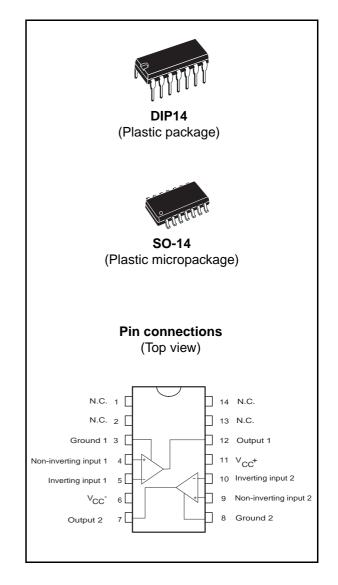
### Description

These products are precision high speed dual comparators designed to operate over a wide range of supply voltages down to a single 5V logic supply and ground. They feature low input currents and high gains.

The open collector of the output stage makes it compatible with TTL as well as capable of driving lamps and relays at currents up to 25mA.

Although designed primarily for applications requiring operation from digital logic supplies, the comparators are fully specified for power supplies up to  $\pm 15$ V.

They feature faster response than LM111 at the expense of higher current consumption. However, the high speed, wide operating voltage range and low package count make the LM119/219/319 much more versatile.



Rev 2

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#### Schematic diagram 1

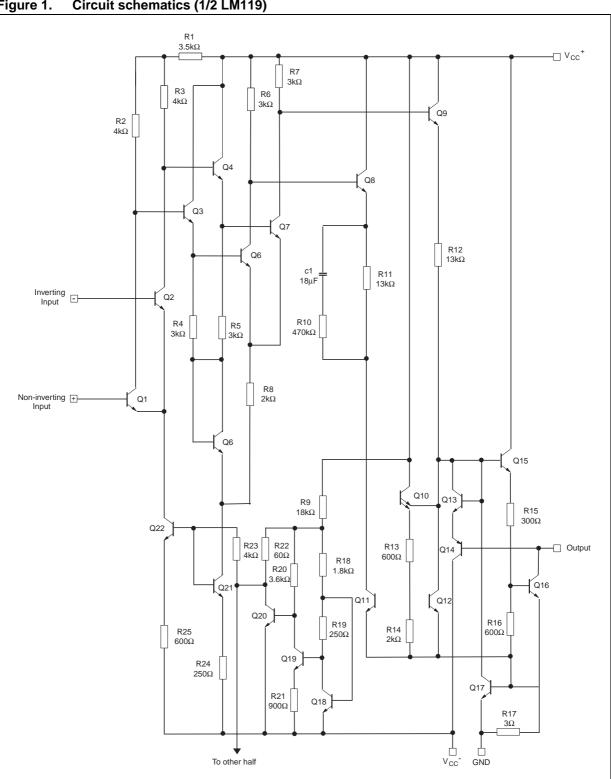


Figure 1. Circuit schematics (1/2 LM119)

## 2 Absolute maximum ratings and operating conditions

Symbol	Parameter	Value	Unit
V <sub>o</sub> - V <sub>CC</sub> <sup>-</sup>	Output to negative supply voltage	36	V
V <sub>CC</sub> <sup>-</sup>	Negative supply voltage	-25	V
V <sub>CC</sub> <sup>+</sup>	Positive supply voltage	18	V
V <sub>id</sub>	Differential input voltage	±5	V
Vi	Input voltage <sup>(1)</sup>	±15	V
	Output short-circuit to ground	Infinite	
Тj	Maximum junction temperature	150	°C
T <sub>stg</sub>	storage temperature range	-65 to +150	°C
R <sub>thja</sub>	Thermal resistance junction to ambient <sup>(2) (3)</sup> DIP14 SO-14	80 105	°C/W
R <sub>thjc</sub>	Thermal resistance junction to case (2) (3)DIP14SO-1431		°C/W
ESD	HBM: human body model <sup>(4)</sup> MM: machine model <sup>(5)</sup> CDM: charged device model <sup>(6)</sup>	400 100 1500	V

Table 1. Absolute maximum ratings (AMR)
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1. For supply voltages lower than ±15V the absolute maximum input voltage is equal to the supply voltage.

2. Short-circuits can cause excessive heating. Destructive dissipation can result from simultaneous shortcircuits on all amplifiers.

3. R<sub>th</sub> are typical values.

<sup>6.</sup> Charged device model: all pins and the package are charged together to the specified voltage and then discharged directly to the ground through only one pin. This is done for all pins.

Table 2.	Operating conditions
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Symbol	Parameter	Value	Unit
V <sub>CC</sub>	Supply voltage	5 to ±15	V
T <sub>oper</sub>	Operating free-air temperature range LM119 LM219 LM319	-55 to + 125 -40 to + 105 0 to + 70	°C



<sup>4.</sup> Human body model: 100pF discharged through a  $1.5k\Omega$  resistor between two pins of the device, done for all couples of pin combinations with other pins floating.

<sup>5.</sup> Machine model: a 200pF cap is charged to the specified voltage, then discharged directly between two pins of the device with no external series resistor (internal resistor <  $5\Omega$ ), done for all couples of pin combinations with other pins floating.

## 3 Electrical characteristics

Quarter at	Parameter		LM119 - LM219			LM319		
Symbol			Тур.	Max.	Min	Тур.	Max.	- Unit
V <sub>io</sub>	Input offset voltage ( $R_s \le 5k\Omega$ ) <sup>(1) (2)</sup> $T_{min} \le T_{amb} \le T_{max}$		0.7	4 7		2	8 10	mV
I <sub>io</sub>	Input offset current <sup>(1)</sup> $T_{min} \le T_{amb} \le T_{max}$		30	75 100		80	200 300	nA
I <sub>ib</sub>	Input bias current <sup>(1)</sup> T <sub>min</sub> ≤ T <sub>amb</sub> ≤ T <sub>max</sub>		150	500 1000		250	1000 1200	nA
A <sub>vd</sub>	Large signal voltage gain	10	40		8	40		V/mV
I <sub>CC</sub> ⁺	Positive supply current $V_{CC} = \pm 15V$ $V_{CC}^{+} = +5V$ , $V_{CC}^{-} = 0V$		8 4.3	11.5		8 4.3	12.5	mA
I <sub>CC</sub> <sup>-</sup>	Negative supply current		3	4.5		3	5	mA
V <sub>icm</sub>	Input common mode voltage range $V_{CC} = \pm 15V$ $V_{CC}^{+} = \pm 5V$ , $V_{CC}^{-} = 0V$	±12 1	±13	3	±12 1	±13	3	V
V <sub>OL</sub>	Low level output voltage $I_o = 25mA$ $V_i \le -5mV$ $V_i \le -10mV$ $T_{min} \le T_{amb} \le T_{max}$ $V_{CC}^+ \ge +4.5V, V_{CC}^- = 0V, I_{o(sink)} < 3.2mA$ $V_i \le -6mV$ $V_i \le -10mV$		0.75	1.5		0.75	1.5	V
						0.3	0.4	
I <sub>OH</sub>	$      High \ level \ output \ current \ (V_o = +35V) \\ V_i \ge \ 5mV \\ V_i \ge \ 10mV \\ T_{min} \le T_{amb} \ \le T_{max}, \ V_i \ge \ 5mV $		0.2	2 10		0.2	10	μA
t <sub>res</sub>	Response time <sup>(3)</sup>		80			80		ns

Table 3. VCC = ±15V, Tamb = +25°C (unless otherwise specified)

 These specifications apply for V<sub>CC</sub> = ±15V, unless otherwise stated. The offset voltage, offset current and bias current specifications apply for any supply voltage from a single +5V up to ±15V supplies. The offset voltages and offset current given are the maximum values required to drive the output down to 1V or up to +14V with a 1mA load current. Thus, these parameters define an error band and take into account the worst case effects of voltage gain and input impedance.

2. At output switch point, Vo  $\approx$  1.4V, no load, with V<sub>CC</sub> from 5V to ±15V and over the full input common-mode range.

3. The response time specified is for a 100mV input step with 5mV overdrive.



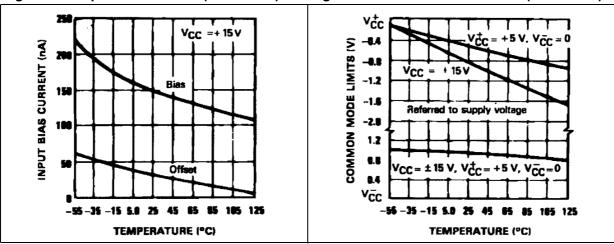
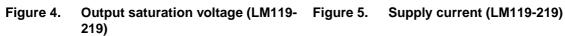
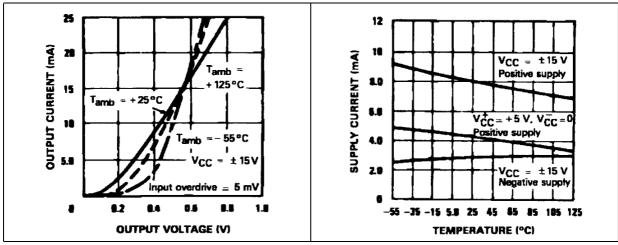
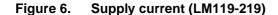
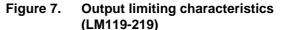


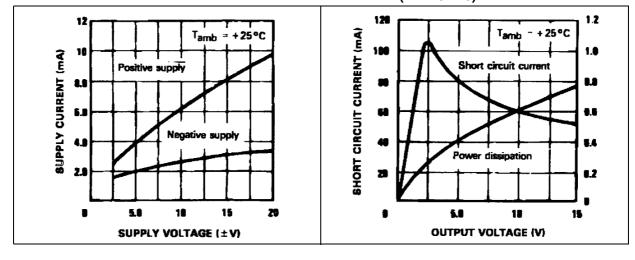
Figure 2. Input bias currents (LM119-219) Figure 3. Common mode limits (LM119-219)





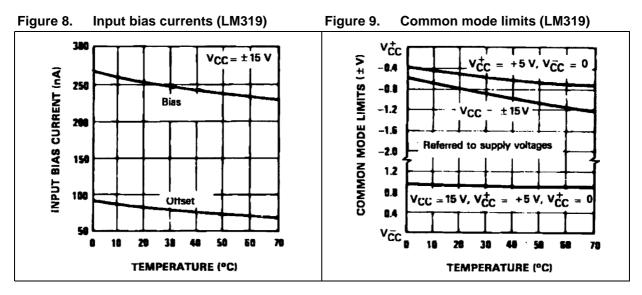






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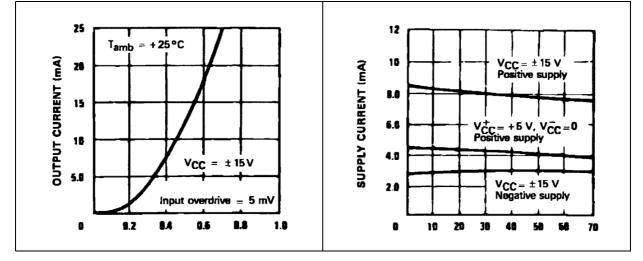
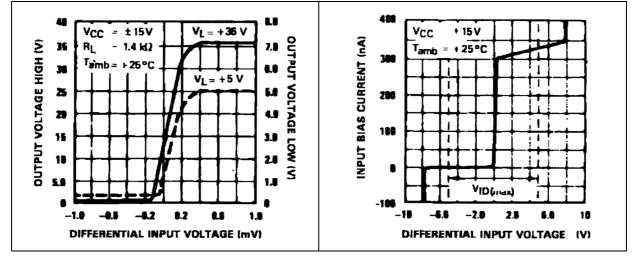




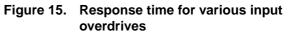
Figure 13. Input characteristics



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Figure 14. Response time for various input overdrives



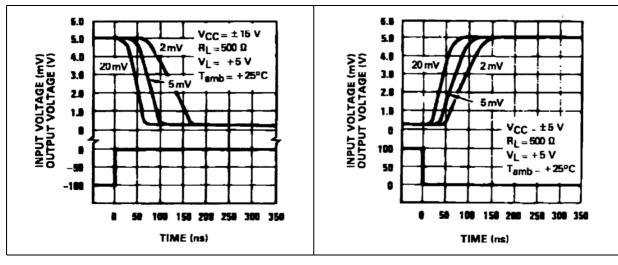
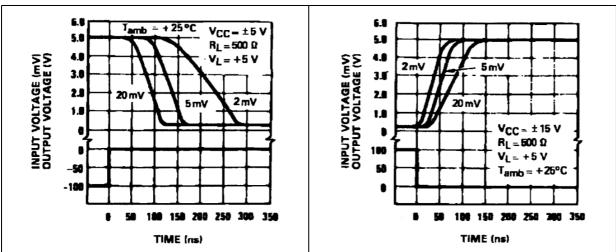
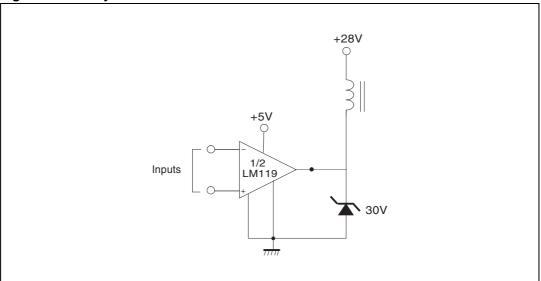


Figure 16. Response time for various input overdrives

Figure 17. Response time for various input overdrives

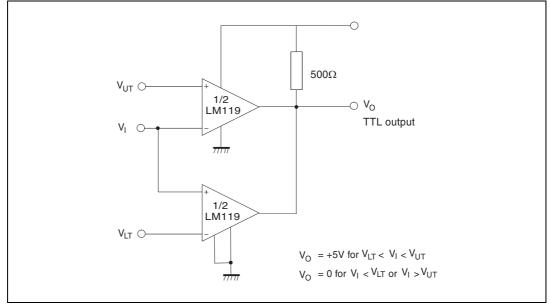


## 4 Typical application diagrams





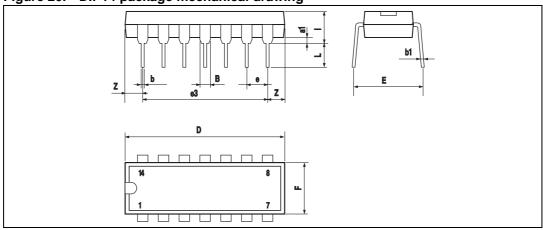




## 5 Package information

In order to meet environmental requirements, ST offers these devices in ECOPACK<sup>®</sup> packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: <u>www.st.com</u>.

### 5.1 DIP14 package information



#### Figure 20. DIP14 package mechanical drawing

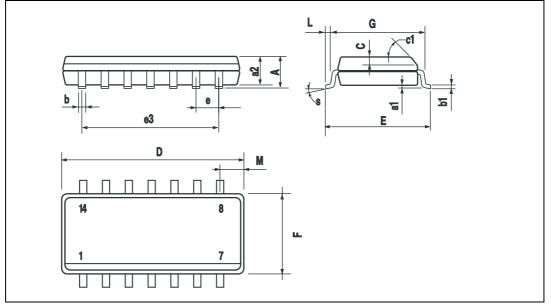
	Dimensions					
Ref.		Millimeters				
	Min.	Тур.	Max.	Min.	Тур.	Max.
a1	0.51			0.020		
В	1.39		1.65	0.055		0.065
b		0.5			0.020	
b1		0.25			0.010	
D			20			0.787
E		8.5			0.335	
е		2.54			0.100	
e3		15.24			0.600	
F			7.1			0.280
I			5.1			0.201
L		3.3			0.130	
Z	1.27		2.54	0.050		0.100



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### 5.2 SO-14 package information





#### Table 5. SO-14 package mechanical data

	Dimensions					
Ref.	Millimeters Inches				lillimeters Inches	
	Min.	Тур.	Max.	Min.	Тур.	Max.
А			1.75			0.068
a1	0.1		0.2	0.003		0.007
a2			1.65			0.064
b	0.35		0.46	0.013		0.018
b1	0.19		0.25	0.007		0.010
С		0.5			0.019	
c1			45°	(typ.)		
D	8.55		8.75	0.336		0.344
Е	5.8		6.2	0.228		0.244
е		1.27			0.050	
e3		7.62			0.300	
F	3.8		4.0	0.149		0.157
G	4.6		5.3	0.181		0.208
L	0.5		1.27	0.019		0.050
М			0.68			0.026
S	8° (max.)					

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## 6 Ordering information

Order code	Temperature range	Package	Packaging	Marking
LM119N		DIP14	Tube	LM119N
LM119D LM119DT	-55°C to +125°C	SO-14	Tube or Tape & reel	119
LM219N		DIP14	Tube	LM219N
LM219D LM219DT	-40°C to +105°C	SO-14	Tube or Tape & reel	219
LM319N		DIP14	Tube	LM319N
LM319D LM319DT	0°C to +70°C	SO-14	Tube or Tape & reel	319

## 7 Revision history

### Table 7. Document revision history

Date	Revision	Changes
5-Jul-2002	1	Initial release.
28-Jan-2007	2	Added ESD, R <sub>thja</sub> parameters in <i>Table 1: Absolute maximum ratings</i> ( <i>AMR</i> ). Expanded orderable parts table, see <i>Table 6: Order codes</i> . Updated document format.



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