

# Snubberless™, logic level and standard 12 A Triacs

#### **Features**

- Medium current Triac
- High static and dynamic commutation
- Low thermal resistance with clip bonding
- Packages is RoHS (2002/95/EC) compliant
- 600 V V<sub>RM</sub>

#### **Applications**

- Value sensitive application
- General purpose ac line load switching
- Motor control circuits in power tools
- Small home appliances, lighting
- Inrush current limiting circuits
- Overvoltage crowbar protection

#### **Description**

Available in through-hole, the T12T series of Triacs can be used as on/off or phase angle control function in general purpose ac switching where high commutation capability is required.

This series can be designed-in in many value sensitive appliances thanks to the parameters guidance provided in the following pages.

Provides insulation rated at 2500 V rms (TO-220AB insulated package).

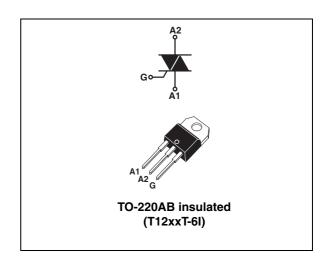


Table 1. Device summary

Order code	Symbol	Value
T1220T-6I T1235T-6I	I <sub>GT</sub> 3Q Snubberless	20 / 35 mA
T1225T-6I	I <sub>GT</sub> 4Q standard	25 mA
T1210T-6I	I <sub>GT</sub> 3Q logic level	10 mA

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## 1 Characteristics

Table 2. Absolute maximum ratings (limiting values;  $T_i = 25$  °C, unless otherwise specified)

Symbol	l Parameter				Unit	
I <sub>T(RMS)</sub>	On-state rms current (full sine wave) T <sub>c</sub> = 88 °C			12	Α	
1 .	Non repetitive surge peak on-state current (full	F = 50 Hz	t <sub>p</sub> = 20 ms	90	Α	
I <sub>TSM</sub>	cycle, T <sub>j</sub> initial = 25 °C)	F = 60 Hz	$t_p = 16.7 \text{ ms}$	95	A	
l <sup>2</sup> t	$I^2t$ Value for fusing $t_p = 10 \text{ ms}$			54	A <sup>2</sup> s	
dl/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$ $F = 60 \text{ Hz}$ $T_j = 125 ^{\circ}\text{C}$		50	A/µs		
V <sub>DSM</sub> / V <sub>RSM</sub>	Non repetitive surge peak off-state voltage $t_p = 10 \text{ ms}$ $T_j = 25 ^\circ$		T <sub>j</sub> = 25 °C	V <sub>DRM</sub> /V <sub>RRM</sub> + 100	V	
I <sub>GM</sub>	Peak gate current $t_p = 20 \ \mu s$ $T_j = 125 \ ^{\circ}C$		4	Α		
P <sub>G(AV)</sub>	Average gate power dissipation $T_j = 125 ^{\circ}\text{C}$				W	
T <sub>stg</sub>	Storage junction temperature range			- 40 to + 150	°C	
Tj	Operating junction temperature range			- 40 to + 125	°C	

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T12T Characteristics

Table 3. Electrical characteristics ( $T_j = 25$  °C, unless otherwise specified)

Compleal	Toot conditions	Quadrant		T12xxT			11		
Symbol	Test conditions	Quadrant		T1210T	T1220T	T1225T	T1235T	Unit	
I <sub>GT</sub> <sup>(1)</sup>	$V_D = 12 \text{ V}  R_L = 30 \Omega$	1 - 11 - 111	MAX.	10	20	25	35	mA	
'GT \"		IV				40			
V <sub>GT</sub>	$V_D = V_{DRM}$ , $R_L = 3.3 \text{ k}\Omega$ , $T_j = 25 ^{\circ}\text{C}$	ALL	MAX.	1.3				V	
V <sub>GD</sub>	$V_D = V_{DRM}$ , $R_L = 3.3 \text{ k}\Omega$ , $T_j = 125 ^{\circ}\text{C}$	ALL	MIN.	0.2		V			
I <sub>H</sub> <sup>(2)</sup>	I <sub>T</sub> = 500 mA		MAX.	10	15	20	30	mA	
	I <sub>G</sub> = 1.2 I <sub>GT</sub>	I - III		20	35	40	50		
IL		IV	MAX.			40		mA	
		II		30	40	60	80		
dV/dt (2)	V <sub>D</sub> = 67% V <sub>DRM,</sub> gate open	T <sub>j</sub> = 125 °C	MIN.	100	1000	100	2000	V/µs	
u v/ui · /		$T_j = 150  {}^{\circ}C^{(3)}$	IVIIIN.	50	500	50	1000	V/μS	
	$(dV/dt)c = 0.1 V/\mu s$			7		7			
	(dV/dt)c = 10 V/μs	T <sub>j</sub> = 125 °C		3		3			
(di/dt)c (2)	Without snubber	MINI	MIN.		6		12	A/ms	
(ui/ut)c ( /	$(dV/dt)c = 0.1 V/\mu s$		IVIIIN.	3		3		AVIIIS	
	(dV/dt)c = 10 V/µs	$T_j = 150  {}^{\circ}C^{(3)}$		1		1			
	Without snubber				3		10		

- 1. minimum  $I_{\mbox{\footnotesize{GT}}}$  is guaranted at 5% of  $I_{\mbox{\footnotesize{GT}}}$  max.
- 2. for both polarities of A2 referenced to A1.
- 3. derating information for excess temperature above  $T_j$  max.

Table 4. Static characteristics

Symbol	Test conditions				Unit
V <sub>T</sub> <sup>(1)</sup>	$I_{TM} = 17 \text{ A, } t_p = 380  \mu\text{s}$	T <sub>j</sub> = 25 °C	MAX.	1.55	V
V <sub>TO</sub> (1)	Threshold voltage	T <sub>j</sub> = 125 °C	MAX.	0.85	V
R <sub>D</sub> <sup>(1)</sup>	Dynamic resistance	T <sub>j</sub> = 125 °C	MAX.	35	mΩ
	$V_{DRM} = V_{RRM}$	T <sub>j</sub> = 25 °C	MAX.	5	μΑ
I <sub>DRM</sub>		T <sub>j</sub> = 125 °C		1	^
IRRM	$V_D = 0.9 \times V_{DRM}$	$T_j = 150  ^{\circ}C^{(2)}$	TYP.	1.9	mA

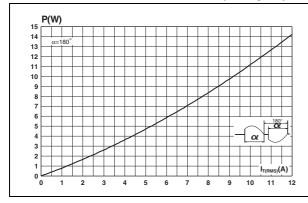
- 1. for both polarities of A2 referenced to A1.
- 2. derating information for excess temperature above  $T_{i}$  max.

Characteristics T12T

Table 5. Thermal resistance

Symbol	Parameter	Value	Unit
R <sub>th(j-c)</sub>	Junction to case (AC)	2.6	°C/W
R <sub>th(j-a)</sub>	Junction to ambient (DC)	60	°C/W

Figure 1. Maximum power dissipation versus Figure 2. On-state rms current versus case rms on-state current (full cycle) temperature (full cycle)



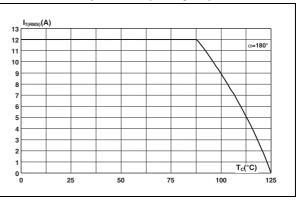
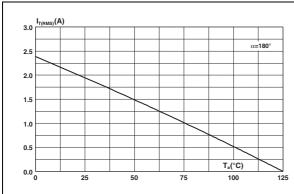


Figure 3. On-state rms current versus ambient temperature

Figure 4. Relative variation of thermal impedance versus pulse duration



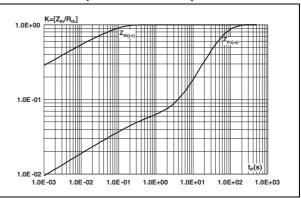
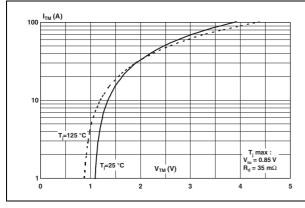
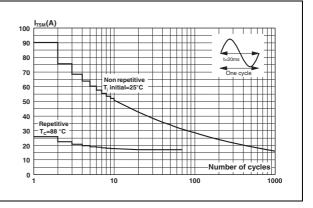


Figure 5. On state characteristics (maximum values)

Figure 6. Surge peak on state current versus number of cycles





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T12T **Characteristics** 

Non repetitive surge peak on state Figure 8. Relative variation of gate trigger Figure 7. current for a sinusoidal current and gate trigger voltage versus junction temperature

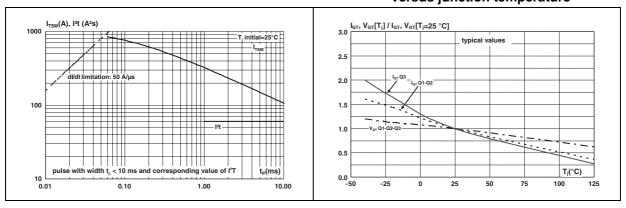


Figure 9. Relative variation of holding current and latching current versus junction temperature

Figure 10. Relative variation of critical rate of decrease of main current versus (dV/dt)c

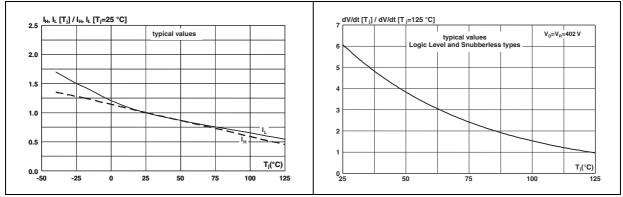
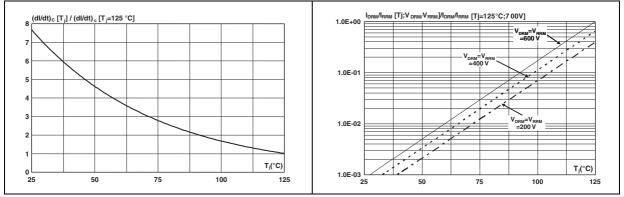


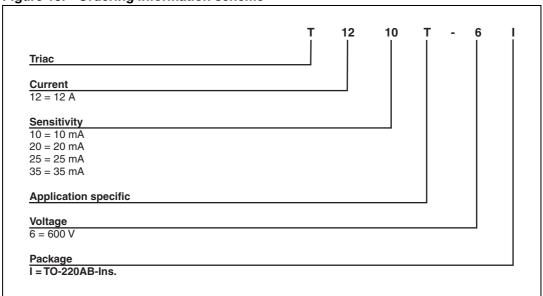
Figure 11. Relative variation of critical rate of Figure 12. Leakage current versus junction decrease of main current versus junction temperature

temperature for different values of blocking voltage (typical values)



# 2 Ordering information scheme

Figure 13. Ordering information scheme

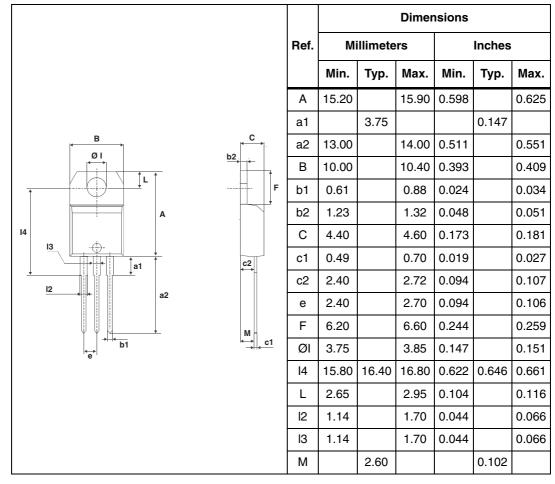


## 3 Package mechanical data

- Epoxy meets UL94, V0
- Lead-free packages

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.

Table 6. TO-220AB insulated dimensions





# 4 Ordering information

 Table 7.
 Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
T1210T-6I	T1210T-6I				
T1220T-6I	T1220T-6I	TO-220AB-ins.	2.3 g	50	Tube
T1225T-6I	T1225T-6I	10-220AD-IIIs.	2.5 g	50	Tube
T1235T-6I	T1235T-6I				

## 5 Revision history

Table 8. Document revision history

Date	Revision	Changes
03-Dec-2009	1	Initial release.
18-Jan-2010	2	Updated pag.1.

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