

T3035H, T3050H

Snubberless™ high temperature 30 A Triacs

Features

- High current Triac
- High immunity level
- Low thermal resistance with clip bounding
- RoHS (2002/95/EC) compliant package
- Very high commutation (3Q) at 150 °C capability
- UL certified (ref. file E81734)

Applications

Thanks to its high electrical noise immunity level and its strong current robustness, the T30xxH series is designed for the control of AC actuators in appliances and industrial systems.

Description

Specifically designed to operate at 150 °C, the new 30 A T30xxH Triacs provide very high dynamic performance and enhanced performance in terms of power loss and thermal dissipation. This allows optimizing the heatsink size, leading to space and cost effectiveness when compared to electro-mechanical solutions.

Based on ST SnubberlessTM technology, they offer a specified minimal commutation and high noise immunity levels valid up to the T_i max.

The T30xxH series optimize safely the control of universal motors and of inductive loads found in power tools and major appliances.

By using an internal ceramic pad, the T30xxH-6l provides voltage insulation (rated at 2500 V rms).

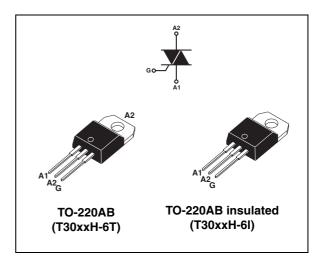


Table 1. Device summary

Symbol	Value
I _{T(rms)}	30 A
V _{DRM} /V _{RRM}	600 V
I _{GT}	35 or 50 mA

TM: Snubberless is a trademark of STMicroelectronics

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Table 2. Absolute maximum rating

Symbol	Paramete	Value	Unit			
		TO-220AB	T _c = 121 °C			
I _{T(RMS)}	On-state rms current (full sine wave)	TO-220AB insul.	T _c = 92 °C	30	Α	
	Non repetitive surge peak on-state	F = 50 Hz	t = 20 ms	270		
ITSM	current (full cycle, T _j initial = 25 °C)	F = 60 Hz	t = 16.7 ms	284	Α	
l ² t	I ² t Value for fusing	t _p = 10 ms		487	A ² s	
dl/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$, $t_r \le 100 \text{ ns}$ $F = 120 \text{ I}$		T _j = 150 °C	50	A/μs	
V _{DSM} / V _{RSM}	Non repetitive surge peak off-state volt-age $t_p = 10 \ \mu s$		T _j = 25 °C	V _{DSM} /V _{RSM} +100	V	
I _{GM}	Peak gate current	t _p = 20 μs	T _j = 150 °C	4	Α	
P _{G(AV)}	Average gate power dissipation $T_j = 150 ^{\circ}\text{C}$			1	W	
T _{stg} T _j	Storage junction temperature range Operating junction temperature range			-40 to +150 -40 to +150	°C	

Table 3. Electrical characteristics ($T_j = 25$ °C, unless otherwise specified)

Symbol	Test conditions	Quadrant		Value		Unit
	rest conditions	Quadrant		T3035H	T3050H	Onit
I _{GT} ⁽¹⁾	$V_{D} = 12 \text{ V} R_{L} = 33 \Omega$	1 - 11 - 111	MAX.	35	50	mA
V _{GT}	AD = 15 A UF = 22.75	- -		1.0		V
V_{GD}	$V_D = V_{DRM} R_L = 3.3 \text{ k}\Omega$ I - II - III MIN. 0.15		15	V		
I _H ⁽²⁾	I _T = 500 mA		MAX.	60	75	mA
	1. 101	1 - 111	MAX.	75	90	mA
IL I	$I_{G} = 1.2 I_{GT}$	II		90	110	
dV/dt (2)	V _D = 67 %V _{DRM} gate open	T _j = 150 °C	MIN.	1000	1500	V/µs
(dl/dt)c (2)	Without snubber	T _j = 150 °C	MIN.	33	44	A/ms

^{1.} Minimum $I_{\mbox{\scriptsize GT}}$ is guaranted at 20 % of $I_{\mbox{\scriptsize GT}}$ max.

^{2.} For both polarities of A2 referenced to A1.

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Table 4. Static characteristics

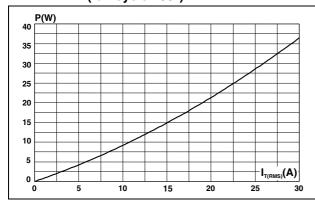
Symbol	Test conditions				Unit
V _{TM} ⁽¹⁾	$I_{TM} = 42 \text{ A}$ $t_p = 380 \mu\text{s}$	T _j = 25 °C	MAX.	1.55	V
V _{to} ⁽¹⁾	Threshold voltage $T_j = 150 ^{\circ}\text{C}$ MAX.		0.85	V	
R _d ⁽¹⁾	Dynamic resistance $T_j = 150 ^{\circ}\text{C}$ MAX.		15	mΩ	
	$V_{DRM} = V_{RRM}$	T _j = 25 °C	MAX.	10	μΑ
I _{DRM}	VDRM = VRRM	T _j = 150 °C		8.5	
I _{RRM}	V _D /V _R = 400V (at peak mains voltage)	T _j = 150 °C	MAX.	7	mA
	V _D /V _R = 200V (at peak mains voltage)	T _j = 150 °C	IVIAA.	5.5	

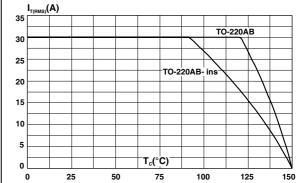
^{1.} for both polarities of A2 referenced to A1.

Table 5. Thermal resistance

Symbol	Symbol Parameter		Value	Unit
В	Junction to case (AC)	TO-220AB		°C/W
R _{th(j-c)}	Surretion to case (AC)	TO-220AB Insul	1.6	C/VV
R _{th(j-a)}	Junction to ambient	TO-220AB / TO-220AB Insul	60	°C/W

Figure 1. Maximum power dissipation versus Figure 2. On-state rms current vs case rms on-state current (full cycle 180°)

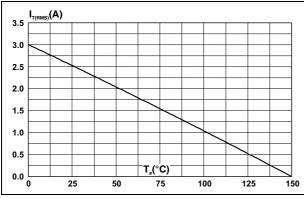




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Figure 3. On-state rms current versus ambient temperature (free air convection)

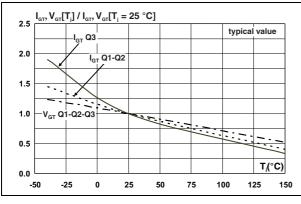
Figure 4. Relative variation of thermal impedance versus pulse duration



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Figure 5. Relative variation of gate trigger current and gate trigger voltage versus junction temperature

Figure 6. Relative variation of holding current and latching current vs junction temperature (typical value)



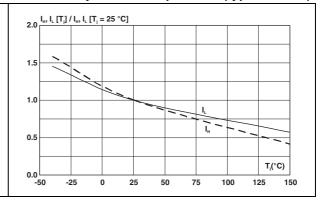
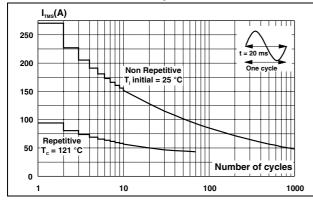
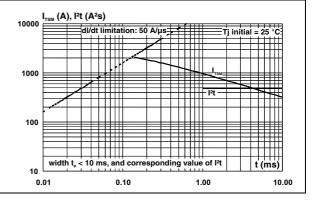


Figure 7. Surge peak on-state current vs number of cycles

Figure 8. Non repetitive surge peak on-state current for a sinusoidal pulse



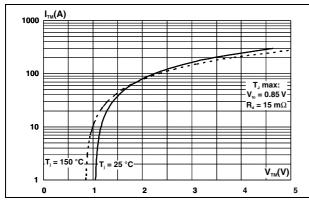
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Figure 9. On state characteristics (maximum values)

Figure 10. Relative variation of critical rate of decrease of main current vs junction temeprature



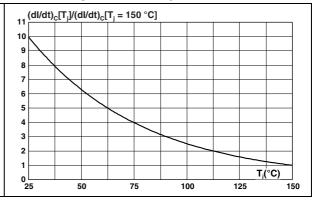
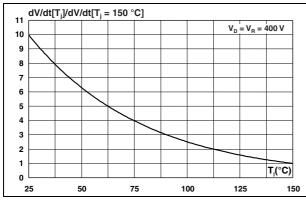


Figure 11. Relative variation of static dV/dt immunity vs junction temperature

Figure 12. Relative variation of leakage current vs junction temperature for different values of blocking voltage



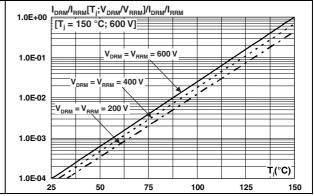
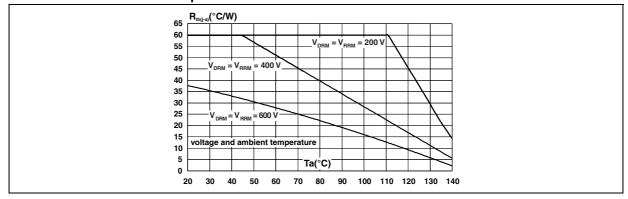


Figure 13. Acceptable junction to ambient thermal resistance vs repetitive peak off-state voltage and ambient temperature

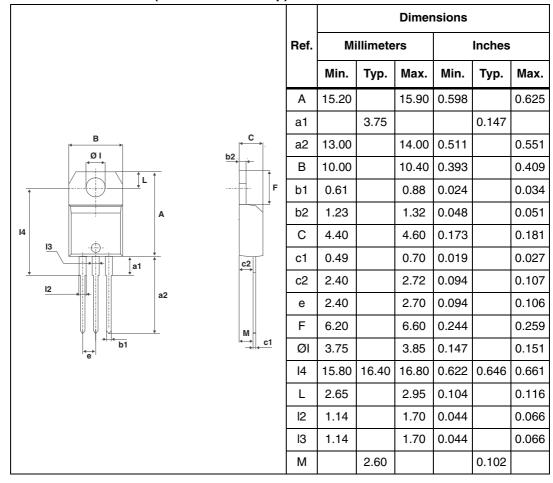


2 Package information

- Epoxy meets UL94, V0
- Recommended torque value: 0.4 to 0.6 N·m

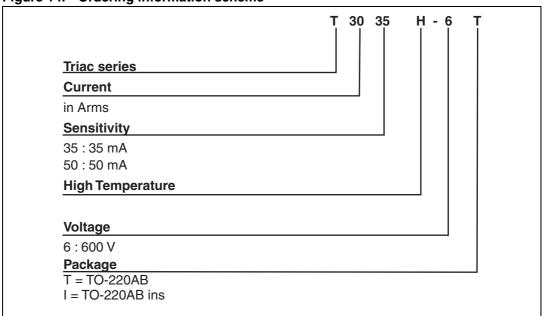
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Table 6. TO-220AB (NIns. and Ins. 20-up) dimensions



3 Ordering information scheme

Figure 14. Ordering information scheme



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4 Ordering information

 Table 7.
 Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
T3035H-6T	T3035H 6T	TO-220AB			
T3050H-6T	T3050H 6T	10-220AB	2.3 g	50	Tube
T3035H-6I	T3035H 6I	TO-220AB	2.5 g	50	lube
T3050H-6I	T3050H 6I	Insulated			

5 Revision history

Table 8. Document revision history

Date	Revision	Changes
28-Jan-2010	1	Initial release.
17-May-2010	2	Updated maximum T _j in <i>Table 2</i> .
14-Dec-2010	3	Updated I _{GT} in <i>Table 1</i> .
20-Sep-2011	4	Updated: Features.

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