

## Snubberless™, logic level and standard 8 A Triacs

#### **Features**

- Medium current Triac
- High static and dynamic commutation
- Low thermal resistance with clip bonding
- Packages is RoHS (2002/95/EC) compliant
- 600 V V<sub>RM</sub>
- UL certified (ref. file E81734)

#### **Applications**

- Value sensitive application
- General purpose ac line load switching
- Motor control circuits in power tools
- Small home appliances, lighting
- Inrush current limiting circuits
- Overvoltage crowbar protection

### **Description**

Available in through-hole, the T8T series of Triacs can be used as on/off or phase angle control function in general purpose ac switching where high commutation capability is required.

This series can be designed-in in many value sensitive appliances thanks to the parameters guidance provided in the following pages.

Provides insulation rated at 2500 V rms (TO-220AB insulated package).

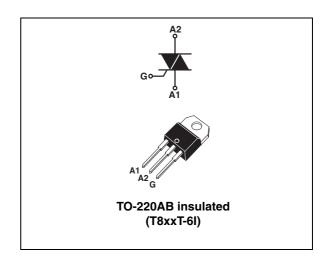


Table 1. Device summary

Table 1. Device Buillinary					
Order code	e Symbol Value				
T810T-6I	I <sub>GT</sub> 3Q logic level	10 mA			
T820T-6I T835T-6I	I <sub>GT</sub> 3Q Snubberless	20 / 35 mA			
T825T-6I	I <sub>GT</sub> 4Q standard	25 mA			

TM: Snubberless is a trademark of STMicroelectronics

September 2011 Doc ID 16192 Rev 3 1/9

Characteristics T8T

# 1 Characteristics

Table 2. Absolute ratings (limiting values;  $T_j = 25$  °C, unless otherwise specified)

Symbol	Parameter				Unit
I <sub>T(RMS)</sub>	On-state rms current (full sine wave)	On-state rms current (full sine wave) $T_c = 97  ^{\circ}\text{C}$		8	Α
I	Non repetitive surge peak on-state current	F = 50 Hz	$t_p = 20 \text{ ms}$	60	Α
ITSM	(full cycle, T <sub>j</sub> initial = 25 °C)	F = 60 Hz	$t_p = 16.7 \text{ ms}$	63	A
l <sup>2</sup> t	I <sup>2</sup> t Value for fusing		t <sub>p</sub> = 10 ms	26	A <sup>2</sup> s
dl/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$ $F = 60 \text{ Hz}$ $T_j = 12 \text{ T}$ $T_j = 12 \text{ T}$		T <sub>j</sub> = 125 °C	50	A/µs
V <sub>DSM</sub> / V <sub>RSM</sub>	Non repetitive surge peak off-state voltage $t_p = 10 \text{ ms}$ $T_j$		T <sub>j</sub> = 25 °C	V <sub>DRM</sub> /V <sub>RRM</sub> + 100	V
I <sub>GM</sub>	Peak gate current $t_p = 20 \mu s$ $T_j = 125 °C$		4	Α	
P <sub>G(AV)</sub>	Average gate power dissipation $T_j = 125  ^{\circ}\text{C}$			1	W
T <sub>stg</sub>	Storage junction temperature range			- 40 to + 150	°C
T <sub>j</sub>	Operating junction temperature range			- 40 to + 125	°C

T8T Characteristics

Table 3. Electrical characteristics ( $T_j = 25$  °C, unless otherwise specified)

Cumbal	Test conditions	Quadrant		T8xxT			Unit	
Symbol	Test conditions	Quadrant		T810T	T820T	T825T	T835T	Unit
I <sub>GT</sub> <sup>(1)</sup>	$V_D = 12 \text{ V}, R_L = 30 \Omega$	1 - 11 - 111	MAX.	10	20	25	35	mA
'GT`'		IV				40		
V <sub>GT</sub>	$V_D = V_{DRM}, R_L = 30 \Omega,$ $T_j = 25  ^{\circ}C$	ALL	MAX.		1	.3		V
V <sub>GD</sub>	$V_D = V_{DRM}$ , $R_L = 3.3 \text{ k}\Omega$ , $T_j = 125 \text{ °C}$	ALL	MIN.	0.2		V		
I <sub>H</sub> <sup>(2)</sup>	I <sub>T</sub> = 500 mA	1	MAX.	15	25	30	40	mA
		I - III		20	35	40	50	
IL	$I_{G} = 1.2 I_{GT}$	IV	MAX.			40		mA
		II		25	40	70	70	
dV/dt (2)	V <sub>D</sub> = 67% V <sub>DRM,</sub> gate open	T <sub>j</sub> = 125 °C	MIN.	100	750	500	2000	V/µs
uv/ut · /		$T_j = 150  {}^{\circ}C^{(3)}$		50	500	300	1000	
	$(dV/dt)c = 0.1 V/\mu s$			5.4				
	$(dV/dt)c = 10 V/\mu s$	T <sub>j</sub> = 125 °C		2		4.5		
(di/dt)c (2)	Without snubber	NAINI		3.4		8	A/ms	
(ul/ut)c (=/	$(dV/dt)c = 0.1 V/\mu s$		MIN.	2.5				AVIIIS
	(dV/dt)c = 10 V/μs	$T_j = 150  {}^{\circ}C^{(3)}$		1		2		
	Without snubber				2		6.5	

- 1. Minimum  $I_{\mbox{\scriptsize GT}}$  is guaranted at 5% of  $I_{\mbox{\scriptsize GT}}$  max.
- 2. For both polarities of A2 referenced to A1.
- 3. Derating information for excess temperature above  $T_j$  max.

Table 4. Static characteristics

Symbol	Test conditions				Unit
V <sub>T</sub> <sup>(1)</sup>	$I_{TM} = 11.3 \text{ A}, t_p = 380 \ \mu\text{s}$	T <sub>j</sub> = 25 °C	MAX.	1.60	V
V <sub>TO</sub> (1)	Threshold voltage	T <sub>j</sub> = 125 °C	MAX.	0.87	V
R <sub>D</sub> <sup>(1)</sup>	Dynamic resistance	T <sub>j</sub> = 125 °C	MAX.	60	mΩ
I <sub>DRM</sub> I <sub>RRM</sub>	$V_{DRM} = V_{RRM}$	T <sub>j</sub> = 25 °C	MAX.	5	μΑ
		T <sub>j</sub> = 125 °C		1	
	$V_D = 0.9 \times V_{DRM}$	$T_j = 150  {}^{\circ}C^{(2)}$	TYP.	1.9	mA

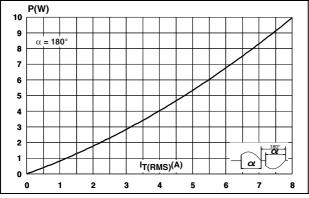
- 1. For both polarities of A2 referenced to A1.
- 2. Derating information for excess temperature above  $T_{i}$  max.

Characteristics T8T

Table 5. Thermal resistance

Symbol	Parameter	Value	Unit
R <sub>th(j-c)</sub>	Junction to case (AC)	2.8	°C/W
R <sub>th(j-a)</sub>	Junction to ambient (DC)	60	°C/W

Figure 1. Maximum power dissipation versus Figure 2. On-state rms current versus case rms on-state current temperature



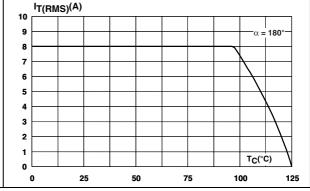
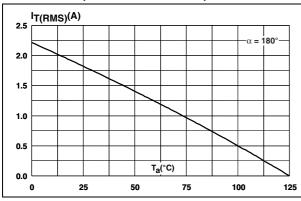


Figure 3. On-state rms current versus ambient temperature (free air convection)

Figure 4. Relative variation of thermal impedance versus pulse duration



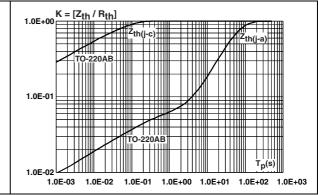
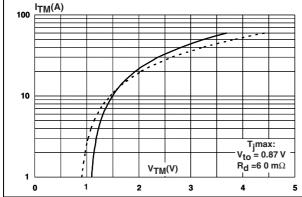


Figure 5. On-state characteristics (maximum values)

4/9

Surge peak on state current versus



Doc ID 16192 Rev 3

20

10

0

Figure 6.

 $T_C = 97 \,^{\circ}C$ 

**T8T Characteristics** 

Non repetitive surge peak on-state Figure 8. Relative variation of gate trigger Figure 7. current and gate trigger voltage current for a sinusoidal versus junction temperature

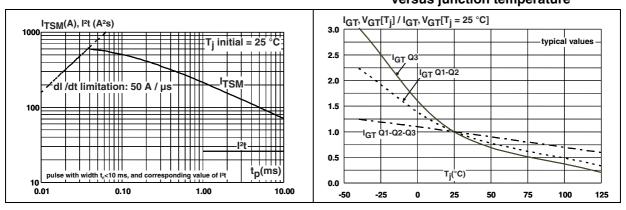


Figure 9. Relative variation of holding current and latching current versus junction temperature

Figure 10. Relative variation of static dV/dt immunity versus junction temperature

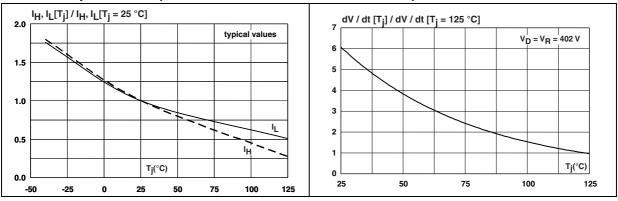
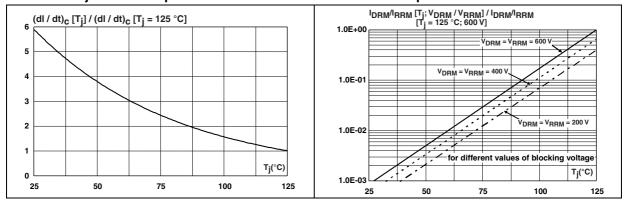


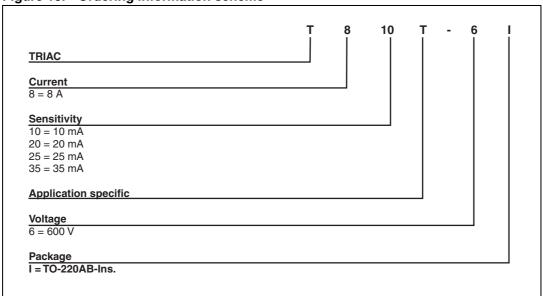
Figure 11. decrease of main current versus junction temperature

Relative variation of critical rate of Figure 12. Relative variation of leakage current versus junction temperature



# 2 Ordering information scheme

Figure 13. Ordering information scheme

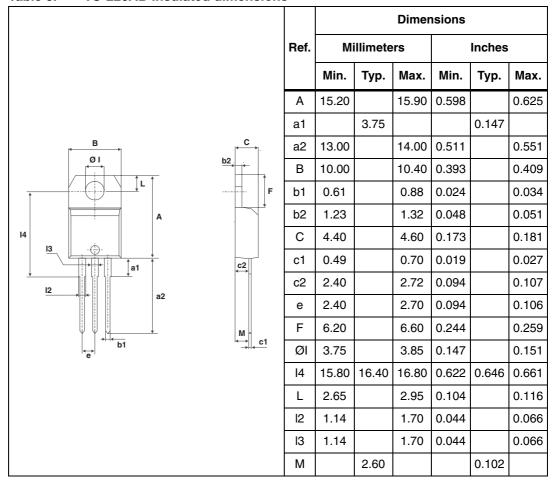


# 3 Package mechanical data

- Epoxy meets UL94, V0
- Lead-free packages

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.

Table 6. TO-220AB Insulated dimensions





# 4 Ordering information

 Table 7.
 Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
T810T-6I	T810T-6I				
T820T-6I	T820T-6I	TO-220AB-Ins.	2.3 g	50	Tube
T825T-6I	T825T-6I	10-220AB-IIIs.	2.5 g	50	Tube
T835T-6I	T835T-6I				

# 5 Revision history

Table 8. Document revision history

Date	Revision	Changes
10-Sep-2009	1	First issue.
18-Jan-2010	2	Updated pag.1.
20-Sep-2011	3	Updated: Features.

#### Please Read Carefully:

Information in this document is provided solely in connection with ST products. STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, modifications or improvements, to this document, and the products and services described herein at any time, without notice.

All ST products are sold pursuant to ST's terms and conditions of sale.

Purchasers are solely responsible for the choice, selection and use of the ST products and services described herein, and ST assumes no liability whatsoever relating to the choice, selection or use of the ST products and services described herein.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by ST for the use of such third party products or services, or any intellectual property contained therein or considered as a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

UNLESS OTHERWISE SET FORTH IN ST'S TERMS AND CONDITIONS OF SALE ST DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF ST PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

UNLESS EXPRESSLY APPROVED IN WRITING BY TWO AUTHORIZED ST REPRESENTATIVES, ST PRODUCTS ARE NOT RECOMMENDED, AUTHORIZED OR WARRANTED FOR USE IN MILITARY, AIR CRAFT, SPACE, LIFE SAVING, OR LIFE SUSTAINING APPLICATIONS, NOR IN PRODUCTS OR SYSTEMS WHERE FAILURE OR MALFUNCTION MAY RESULT IN PERSONAL INJURY, DEATH, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE. ST PRODUCTS WHICH ARE NOT SPECIFIED AS "AUTOMOTIVE GRADE" MAY ONLY BE USED IN AUTOMOTIVE APPLICATIONS AT USER'S OWN RISK.

Resale of ST products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by ST for the ST product or service described herein and shall not create or extend in any manner whatsoever, any liability of ST.

ST and the ST logo are trademarks or registered trademarks of ST in various countries.

Information in this document supersedes and replaces all information previously supplied.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners.

© 2011 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Philippines - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

www.st.com



Doc ID 16192 Rev 3

9/9