TS391H

High temperature low power single voltage comparator

Features

- Wide single supply voltage range or dual supplies +2 V to +36 V or ±1 V to ±18 V
- Very low supply current (0.2 mA) independent of supply voltage (1 mW/comparator at +5 V)
- Low input bias current: 25 nA typ.
- Low input offset current: ±5 nA typ.
- Low input offset voltage: ±1 mV typ.
- Input common-mode voltage range includes ground
- Low output saturation voltage: 250 mV typ. (I_o = 4 mA)
- Differential input voltage range equal to the supply voltage
- TTL, DTL, ECL, CMOS compatible outputs

Wafer form

Description

The TS391H is a low power voltage comparator designed specifically to operate from a single supply over a wide range of voltages. Operation from split power supplies is also possible.

This comparator also has a unique characteristic in that the input common-mode voltage range includes ground even though operated from a single power supply voltage.

1 Schematic diagram

Figure 1. Schematic diagram

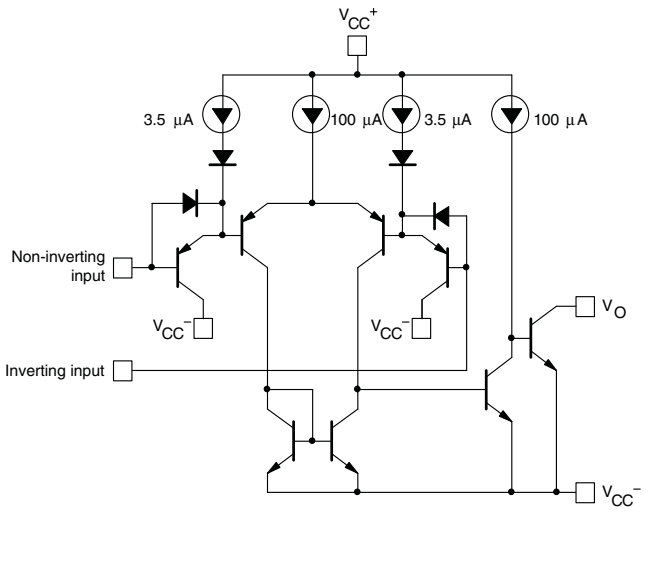
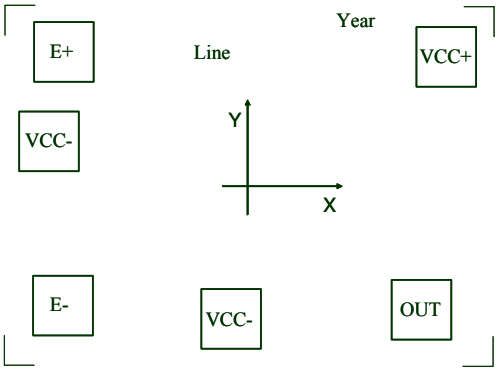


Figure 2. Pad locations and coordinates



Name	Pad placement		Pad dimensions (opening)	
	X	Y	X	Y
OUT	+365	-250	100	100
VCC-	-45	-270	100	100
E-	-400	-250	100	100
VCC-	-425	+90	100	100
E+	-395	+275	100	100
VCC+	+415	+260	100	100

The coordinate of origin is at the centre of the die.

All dimensions are in micrometers (µm).

2 Absolute maximum ratings and operating conditions

Table 1. Absolute maximum ratings (AMR)

Symbol	Parameter	Value	Unit
V_{CC}	Supply voltage	±18 or 36	V
V_{id}	Differential input voltage	±36	V
V_i	Input voltage	-0.3 to +36	V
	Output short-circuit to ground(1)	Infinite	
T_j	Maximum junction temperature	+160	°C
R_{thja}	Thermal resistance junction to ambient(2)	250	°C/W
R_{thjc}	Thermal resistance junction to case (2)	81	°C/W
T_{stg}	Storage temperature range	-65 to +150	°C
ESD	Human body model (HBM)(3)	1500	V
	Machine model (MM)(4)	100	
	Charged device model (CDM)(5)	1000	

1. Short-circuits from the output to V_{CC}^+ can cause excessive heating and potential destruction. The maximum output current is approximately 20 mA independent of the magnitude of V_{CC}^+.
2. Short-circuits can cause excessive heating. These values are typical.
3. Human body model: a 100 pF capacitor is charged to the specified voltage, then discharged through a 1.5 kΩ resistor between two pins of the device. This is done for all couples of connected pin combinations while the other pins are floating.
4. Machine model: a 200 pF capacitor is charged to the specified voltage, then discharged directly between two pins of the device with no external series resistor (internal resistor < 5 Ω). This is done for all couples of connected pin combinations while the other pins are floating.
5. Charged device model: all pins and package are charged together to the specified voltage and then discharged directly to ground through only one pin. This is done for all pins.

Table 2. Operating conditions

Symbol	Parameter	Value	Unit
V_{CC}	Supply voltage	2 to 36 ±1 to ±18	V
V_{icm}	Input common mode voltage range (1) T_{amb} = 25°C $T_{min} \le T_{amb} \le T_{max}$	 0 to V_{CC}^+ -1.5 0 to V_{CC}^+ -2	V
T_{oper}	Operating free-air temperature range	-40 to +150	°C

1. The input common-mode voltage of either input signal voltage should not be allowed to go negative by more than 0.3 V. The upper end of the common-mode voltage range is V_{CC}^+ -1.5 V, but either or both inputs can go to +30 V without damage.

3 Electrical characteristics

Table 3. V_{CC}^+ = +5 V, V_{CC}^- = 0 V, T_{amb} = 25°C (unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_{io}	Input offset voltage (1)	$T_{min} \le T_{amb} \le T_{max}$		1	5 9	mV
I_{io}	Input offset current	$T_{min} \le T_{amb} \le T_{max}$		5	50 150	nA
I_{ib}	Input bias current (2)	$T_{min} \le T_{amb} \le T_{max}$		25	250 400	nA
A_{vd}	Large signal voltage gain	V_{CC}^+ = 15 V, R_L = 15 kΩ, V_o = 1 to 11 V	50	200		V/mV
I_{CC}	Supply current	V_{CC}^+ = 5 V, no load V_{CC}^+ = 30 V, no load		0.2 0.5	0.5 1.25	mA
V_{id}	Differential input voltage (3)				V_{CC}^+	V
I_{sink}	Output sink current	V_{id} = -1 V, V_O = 1.5 V $T_{min} \le T_{amb} \le T_{max}$	6 2	16		mA
V_{OL}	Low level output voltage	V_{id} = 1 V, $V_{CC}^+ = V_O$ = 30 V $T_{min} \le T_{amb} \le T_{max}$		250	400 700	mV
I_{OH}	High level output current	V_{id} = 1 V, $V_{CC}^+ = V_O$ = 30 V $T_{min} \le T_{amb} \le T_{max}$		0.1	 1	nA µA
tre	Small signal response time	R_L = 5.1 kΩ to V_{CC}^+ (4)		1.3		µs
t_{rel}	Large signal response time	V_i = TTL, V_{ref} = +1.4 V, R_L = 5.1 kΩ to V_{CC}^+		300		ns

1. At output switch point, $V_O \approx$ 1.4 V, R_S = 0 Ω with V_{CC}^+ from 5 V to 30 V and over the full input common-mode range (0 V to V_{CC}^+ -1.5 V).
2. The direction of the input current is out of the IC due to the PNP input stage. This current is essentially constant, independent of the state of the output, so there is no loading charge on the reference or input lines.
3. Positive excursions of input voltage may exceed the power supply level. As long as the other voltage remains within the common-mode range, the comparator will provide a proper output state. The low input voltage state must not be less than -0.3 V (or 0.3 V below the negative power supply, if used).
4. The response time specified is for a 100 mV input step with 5 mV overdrive. For larger overdrive signals, 300 ns can be obtained.

Figure 3. Supply current vs. supply voltage

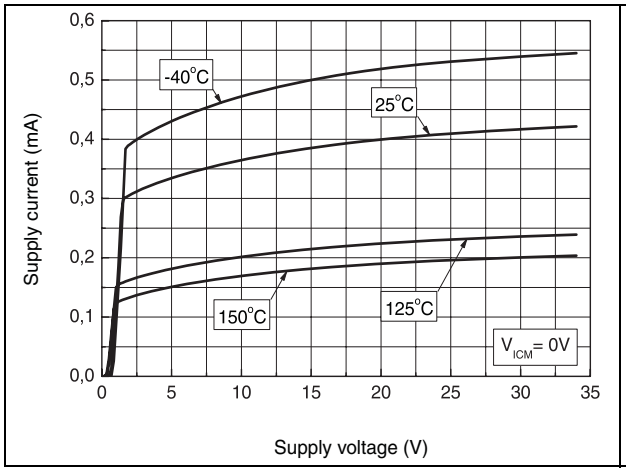


Figure 4. Response time for various input overdrives - negative transition

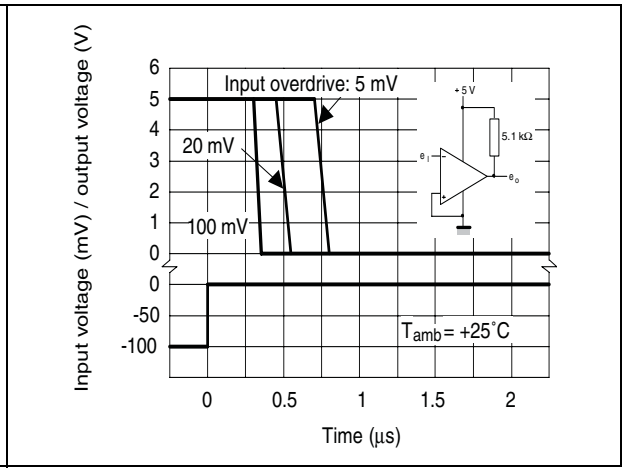


Figure 5. Input current vs. supply voltage

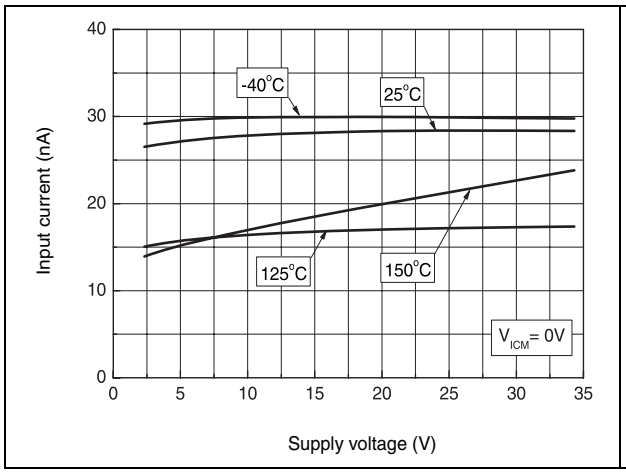


Figure 6. Response time for various input overdrives - positive transition

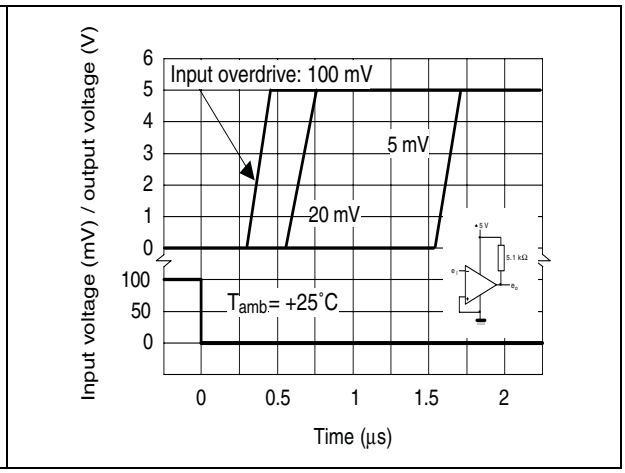
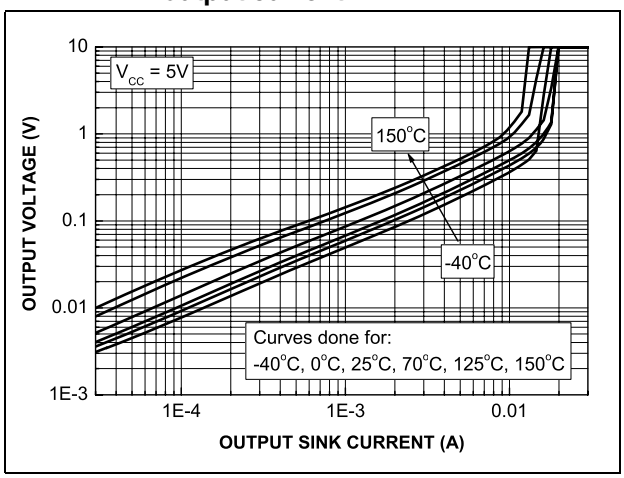


Figure 7. Output saturation voltage vs. output current



4 Ordering information

Table 4. Order codes

Part number	Temperature range	Package	Packaging	Marking
JTS391HY_I6D1[1]	-40°C, +150°C		Wafer form	

1. Qualification and characterization according to AEC Q100 and Q003 or equivalent, advanced screening according to AEC Q001 & Q 002 or equivalent.

5 Revision history

Table 5. Document revision history

Date	Revision	Changes
28-Mar-2011	1	Initial release.

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