

#### **FEATURES**

- Controlled Baseline
  - One Assembly
  - Test Site
  - One Fabrication Site
- Extended Temperature Performance of -55°C to 125°C
- Enhanced Diminishing Manufacturing Sources (DMS) Support
- Enhanced Product Change Notification
- Qualification Pedigree (1)
- Single Supply or Dual Supplies
- Wide Range of Supply Voltage
  - Max Rating . . . 2 V to 36 V
  - Tested to 30 V
- (1) Component qualification in accordance with JEDEC and industry standards to ensure reliable operation over an extended temperature range. This includes, but is not limited to, Highly Accelerated Stress Test (HAST) or biased 85/85, temperature cycle, autoclave or unbiased HAST, electromigration, bond intermetallic life, and mold-compound life. Such qualification testing should not be viewed as justifying use of this component beyond specified performance and environmental limits.

#### **DESCRIPTION/ORDERING INFORMATION**

 Low Supply-Current Drain Independent of Supply Voltage . . . 0.4 mA Typical Per Comparator

- Low Input Bias Current . . . 25 nA Typical
- Low Input Offset Voltage ... 2 mV Typical
- Common-Mode Input Voltage Range Includes Ground
- Differential Input Voltage Range Equal to Maximum-Rated Supply Voltage . . . ±36 V
- Low Output Saturation Voltage
- Output Compatible With TTL, MOS, and CMOS

LM293 D PACKAGE (TOP VIEW)							
10UT [	1	U	8	] V <sub>cc</sub>			
1IN- [	2		7	] 20UT			
1IN+ [	3		6	] 2IN–			
GND [	4		5	] 2IN+			

This device consist of two independent voltage comparators that are designed to operate from a single power supply over a wide range of voltages. Operation from dual supplies also is possible as long as the difference between the two supplies is 2 V to 36 V, and  $V_{CC}$  is at least 1.5 V more positive than the input common-mode voltage. Current drain is independent of the supply voltage. The outputs can be connected to other open-collector outputs to achieve wired-AND relationships.

The LM293-EP is characterized for operation from –55°C to 125°C.

#### **ORDERING INFORMATION**<sup>(1)</sup>

T <sub>A</sub>	V <sub>IOmax</sub> AT 25°C	MAX V <sub>CC</sub>	PACKAGE <sup>(2)</sup>		ORDERABLE PART NUMBER	TOPSIDE MARKING	
–55°C to 125°C	5 mV	30 V	SOIC – D	Reel of 2500	LM293MDREP	LM293E	

(1) For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI website at www.ti.com.

(2) Package drawings, thermal data, and symbolization are available at www.ti.com/packaging.



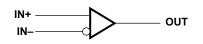
Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

## LM293-EP DUAL DIFFERENTIAL COMPARATOR

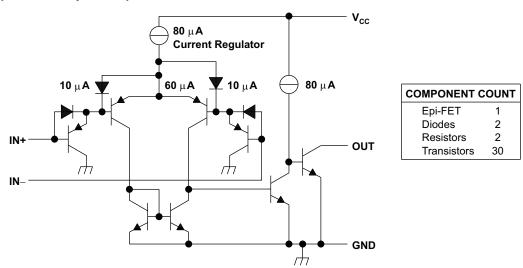
SLCS155-OCTOBER 2007



#### Symbol (Each Comparator)



#### Schematic (Each Comparator)



Current values shown are nominal.

#### Absolute Maximum Ratings<sup>(1)</sup>

over operating free-air temperature range (unless otherwise noted)

			MIN	MAX	UNIT		
V <sub>CC</sub>	Supply voltage <sup>(2)</sup>	Supply voltage <sup>(2)</sup>					
V <sub>ID</sub>	Differential input voltage <sup>(3)</sup>		±36	V			
VI	Input voltage range (either input)	-0.3	36	V			
Vo	Output voltage		36	V			
I <sub>O</sub>	Output current		20	mA			
	Duration of output short-circuit to ground <sup>(4)</sup>			Unlimited			
$\theta_{JA}$	Package thermal impedance <sup>(5)(6)</sup>	D package		97	°C/W		
$T_{J}$	Operating virtual junction temperature		150	°C			
T <sub>stg</sub>	Storage temperature range	-65	150	°C			

(1) Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

All voltage values, except differential voltages, are with respect to GND. (2)

(3)

- Differential voltages are at IN+, with respect to IN-. Short circuits from outputs to  $V_{CC}$  can cause excessive heating and eventual destruction. (4)
- Maximum power dissipation is a function of  $T_J(max)$ ,  $\theta_{JA}$ , and  $T_A$ . The maximum allowable power dissipation at any allowable ambient temperature is  $P_D = (T_J(max) T_A)/\theta_{JA}$ . Operating at the absolute maximum  $T_J$  of 150°C can affect reliability. (5)

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(6) The package thermal impedance is calculated in accordance with JESD 51-7.

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#### **Electrical Characteristics**

at specified free-air temperature,  $V_{CC}$  = 5 V (unless otherwise noted)

PARAMETER		TEST CONDITIONS		<b>T</b> (1)	L	LM293			
	PARAMETER	TEST CONDITIONS		T <sub>A</sub> <sup>(1)</sup>	MIN	TYP	MAX	UNIT	
		$V_{CC} = 5 V \text{ to } 30 V,$		25°C		2	5		
V <sub>IO</sub>	Input offset voltage	$V_{O} = 1.4 V,$ $V_{IC} = V_{IC(min)}$		Full range			9	mV	
	Input offect ourrest	V 1 4 V		25°C		5	50		
IIO	Input offset current	V <sub>O</sub> = 1.4 V		Full range			250	nA	
	Input high ourrent			25°C		-25	-250	nA	
IB	I <sub>IB</sub> Input bias current	V <sub>O</sub> = 1.4 V		Full range			-400		
	Common-mode input			25°C	0 to VCC - 1.5				
V <sub>ICR</sub> voltage range <sup>(2)</sup>			Full range	0 to VCC – 2			V		
A <sub>VD</sub>	Large-signal differential-voltage amplification	$V_{CC} = 15 V,$ $V_{O} = 1.4 V \text{ to } 11$ $R_{L} \ge 15 k\Omega \text{ to } V$		25°C	50	200		V/mV	
	Lligh lovel output ourrent	V <sub>OH</sub> = 5 V,	$V_{ID} = 1 V$	25°C		0.1	50	nA	
I <sub>OH</sub>	High-level output current	$V_{OH} = 30 \text{ V}, \qquad V_{ID} = 1 \text{ V}$		Full range			1	μA	
				25°C		150	400		
V <sub>OL</sub> Low-level output voltage	$I_{OL} = 4 \text{ mA},$	$v_{ID} = -1 v$	V <sub>ID</sub> = -1 V Full range			700	mV		
I <sub>OL</sub>	Low-level output current	V <sub>OL</sub> = 1.5 V,	$V_{ID} = -1 V$	25°C	6			mA	
	Supply ourront	P _ ~	$V_{CC} = 5 V$	25°C		0.8	1	mA	
I <sub>CC</sub>	Supply current	R <sub>L</sub> = ∞	V <sub>CC</sub> = 30 V	Full range	nge		2.5	ШA	

(1) Full range (MIN or MAX) for LM293 is -55°C to 125°C. All characteristics are measured with zero common-mode input voltage, unless otherwise specified.

(2) The voltage at either input or common-mode should not be allowed to go negative by more than 0.3 V. The upper end of the common-mode voltage range is V<sub>CC+</sub> – 1.5 V for the inverting input (–), and the non-inverting input (+) can exceed the V<sub>CC</sub> level; the comparator provides a proper output state. Either or both inputs can go to 30 V without damage.

#### **Switching Characteristics**

 $V_{CC}=5~V,~T_{A}=25^{\circ}C$ 

PARAMETER	TEST CO	LM293 TYP	UNIT	
Response time	$R_L$ connected to 5 V through 5.2 k $\Omega$ ,	100 mV input step with 5 mV overdrive	1.3	
	$R_L$ connected to 5 V through 5.2 kΩ, $C_L$ = 15 pF $^{(1)},$ See $^{(2)}$	TTL-level input step	0.3	μs

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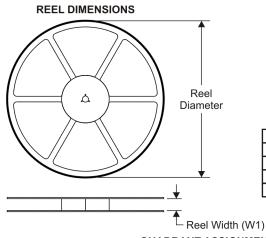
(1)  $C_L$  includes probe and jig capacitance.

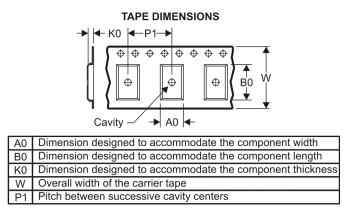
(2) The response time specified is the interval between the input step function and the instant when the output crosses 1.4 V.

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#### TAPE AND REEL INFORMATION





#### QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All di	mensions	are	nominal
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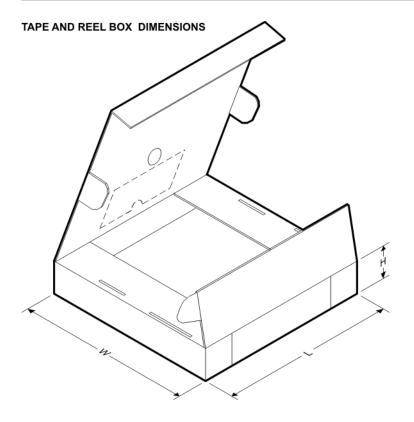
Device	Package Type	Package Drawing			Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LM293MDREP	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1

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### PACKAGE MATERIALS INFORMATION

19-Mar-2008



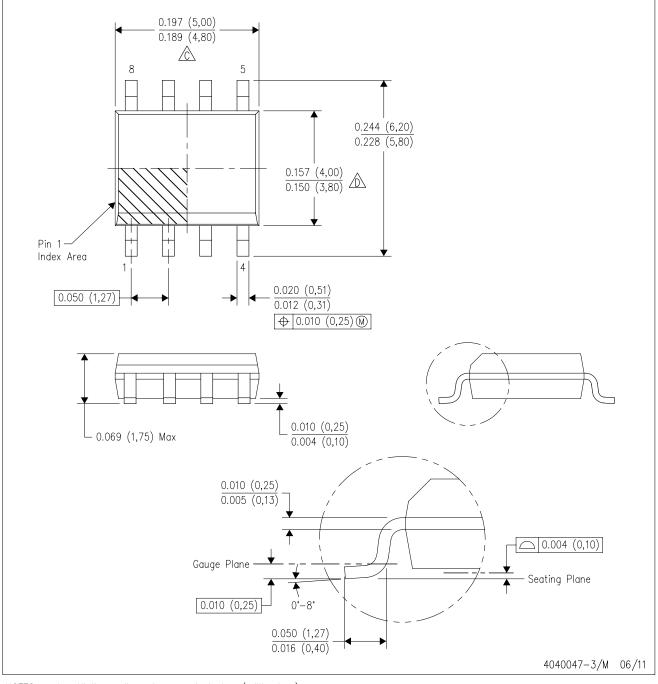
\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LM293MDREP	SOIC	D	8	2500	340.5	338.1	20.6

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D (R-PDSO-G8)

PLASTIC SMALL OUTLINE

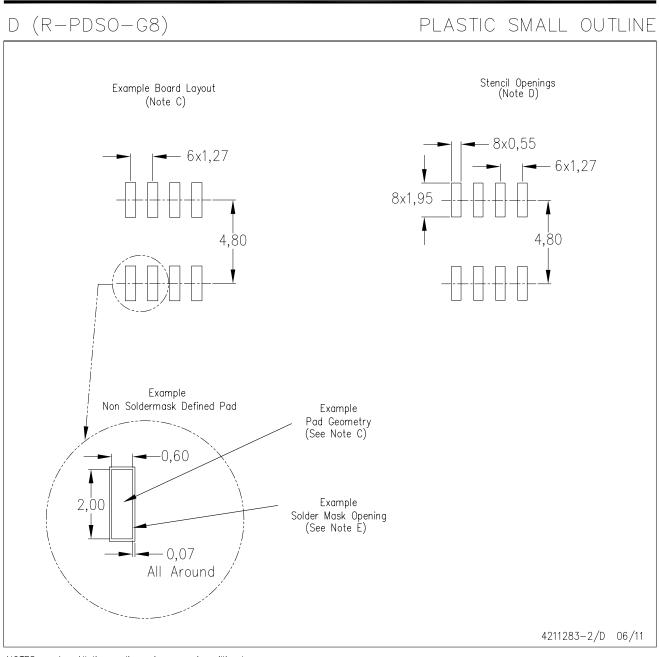


NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
- Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
- E. Reference JEDEC MS-012 variation AA.



### LAND PATTERN DATA



NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Publication IPC-7351 is recommended for alternate designs.
- D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
  E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.



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