SDLS148 - OCTOBER 1976 - REVISED MARCH 1988

- Three-State Outputs Interface Directly with System Bus
- 'LS257B and 'LS258B Offer Three Times the Sink-Current Capability of the Original 'LS257 and 'LS258
- Same Pin Assignments as SN54LS157, SN74LS157, SN54S157, SN74S157, and SN54LS158, SN74LS158, SN54S158, SN74S158
- Provides Bus Interface from Multiple Sources in High-Performance Systems

	AVERAGE PROPAGATION	TYPICAL
	DELAY FROM	POWER .
	DATA INPUT	DISSIPATIONT
'LS257B	9 ns	55 mW
'LS258B	9 ns	55 mW
'S257	4.8 ns	320 mW
'S258	4 ns	280 mW

<sup>†</sup>Off state (worst case)

#### description

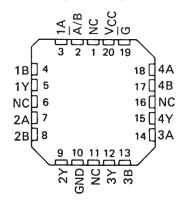
These devices are designed to multiplex signals from four-bit data sources to four-output data lines in busorganized systems. The 3-state outputs will not load the data lines when the output control pin  $(\overline{G})$  is at a high-logic level.

Series 54LS and 54S are characterized for operation over the full military temperature range of  $-55^{\circ}$ C to 125°C; Series 74LS and 74S are characterized for operation from 0°C to 70°C.

SN54LS257B, SN54S257, SN54LS258B, SN54S258 . . . J OR W PACKAGE SN74LS257B, SN74S257, SN74LS258B, SN74S258 . . . D OR N PACKAGE (TOP VIEW)

1	U <sub>16</sub>	D⊻cc
2	15	G
3	14	] 4A
4	13	] 4B
5	12	] 4Y
6	11	]3A
7	10	]3B
8	9	] 3Y
	3 4 5 6 7	2 15 3 14 4 13 5 12 6 11 7 10

SN54LS257B, SN54S257, SN54LS258B, SN54S258 . . . FK PACKAGE (TOP VIEW)



NC-No internal connection.

#### **FUNCTION TABLE**

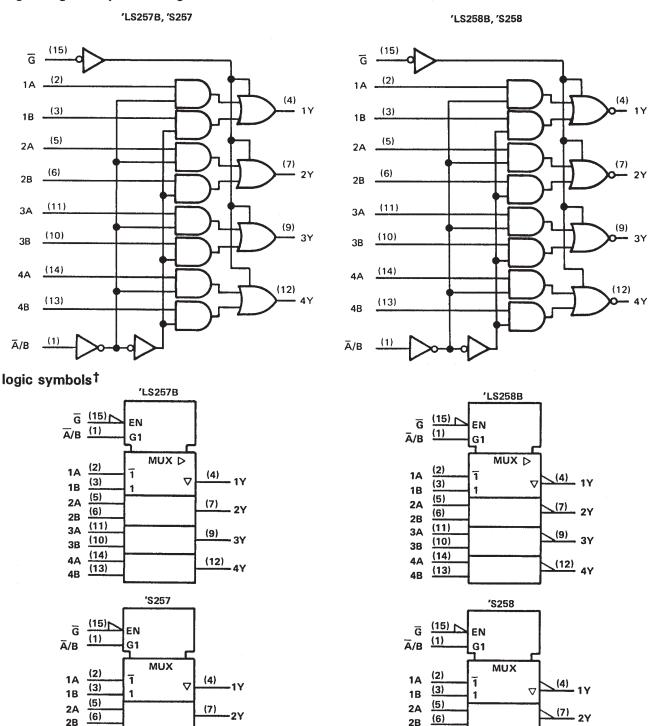
	INPUTS			OUTP	UT Y
OUTPUT CONTROL	SELECT	А	В	'LS257B 'S257	'LS258B 'S258
Н	Х	Х	Х	Z	Z
L	L,	L	Х	L	Н
L.	L	Н	Х	Н	L
L	Н	Х	L	L	Н
L	Н	Х	Н	Н	L

H = high level, L = low level, X = irrelevant,

Z = high impedance (off)

SDLS148 - OCTOBER 1976 - REVISED MARCH 1988

#### logic diagrams (positive logic)



(9)

(12)



(11)

(10)

(14)

(13)

(9) 3Y

(12) 4Y

**3A** 

3В

(11)

(10)

(14)

(13)

**3A** 

**3**B

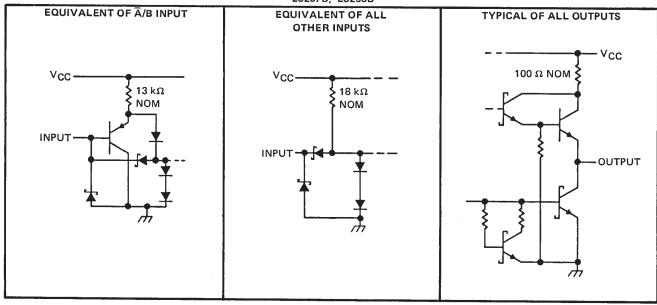
4A

<sup>&</sup>lt;sup>†</sup>These symbols are in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12. Pin numbers shown are for D, J, N, and W packages.

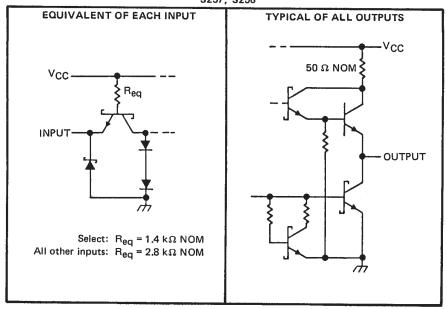
SDLS148 – OCTOBER 1976 – REVISED MARCH 1988

### schematics of inputs and outputs





'S257, 'S258



# absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (see Note 1)	7 V
Input voltage: 'LS257B, 'LS258B Circuits	
'S257, 'S258 Circuits	5.5 V
Off-state output voltage	5.5 V
Operating free-air temperature range: SN54LS', SN54S' Circuits	
	0°C to 70°C
Storage temperature range	

NOTE 1: Voltage values are with respect to network ground terminal.



## SN54LS257B, SN54LS258B, SN54S257, SN54S258 SN74LS257B, SN74LS258B, SN74S257, SN74S258 QUADRUPLE 2-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS

SDLS148 - OCTOBER 1976 - REVISED MARCH 1988

#### recommended operating conditions

			SN54LS	3'		SN74LS	S'	
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	V
VIH	High-level input voltage	2			2		<del></del>	V
VIL	Low-level input voltage			0.7			0.8	V
loh	High-level output current		·	<b>– 1</b>			- 2.6	mA
loL	Low-level output current			12			24	mA
TA	Operating free-air temperature	- 55		125	0		70	°C

### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	PARAMETER	TE	ST CONDITION	ust		SN54LS	3'		SN74LS	3'	
		•	ST CONDITION	40.	MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT
VIK		V <sub>CC</sub> = MIN,	1 <sub>1</sub> = 18 mA				- 1.5			- 1.5	V
V <sub>OH</sub>		V <sub>CC</sub> = MIN, I <sub>OH</sub> = MAX	V <sub>IH</sub> = 2 V,	VIL = MAX,	2.4	3.4		2.4	3.1	, ,	٧
VOL		VCC = MIN,	V <sub>IH</sub> = 2 V,	I <sub>OL</sub> = 12 mA		0.25	0.4		0.25	0.4	<u> </u>
-02		VIL = MAX,		I <sub>OL</sub> = 24 mA		***************************************			0.35	0.5	٧
lozh		V <sub>CC</sub> = MAX,	V <sub>IH</sub> = 2 V,	V <sub>O</sub> = 2.7 V			20			20	μΑ
lozL		V <sub>CC</sub> - MAX,	$V_{1H} = 2 V$	V <sub>O</sub> = 0.4 V			20			- 20	μΑ
11		V <sub>CC</sub> = MAX,	V1 = 7 V				0.1			0.1	mA
ΊΗ		V <sub>CC</sub> = MAX,	V1 = 2.7 V				20			20	μΑ
l <sub>I</sub> L_		V <sub>CC</sub> = MAX,	V <sub>I</sub> = 0.4 V				- 0.4			- 0.4	mA
los §		V <sub>CC</sub> = MAX,			- 30		- 130	- 30		- 130	mA
	All outputs high					8	12		8	12	
	All outputs low			'LS257B		12	18		12	18	1
loo	All outputs off	VMAY	Can Nata O			13	19		13	19	1
lcc	All outputs high	$V_{CC} = MAX,$	See NOTE 2			6	9		6	9	mA
	All outputs low			'LS258B		10	15		10	15	1
	All outputs off					11	16		11	16	1

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. ‡ All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25^{\circ}C$ .

#### switching characteristics, $V_{CC} = 5 \text{ V}$ , $T_{A} = 25^{\circ}\text{C}$ . $R_{I} = 667 \Omega$

PARAMETER	FROM	то	TEST CON	IDITIONS		'LS257	В		'LS258	В	
TATION ETER	(INPUT)	(OUTPUT)	1231 001	IDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNIT
<sup>t</sup> PLH	Data	Any				8	13		7	12	
<sup>t</sup> PHL	Data	Ally				10	15		11	17	ns
<sup>t</sup> PLH	Select	Any	C <sub>L</sub> = 45 pF,	See Note 3		16	21		14	21	
tpHL			C[ - 45 pr,	See Note S		17	24		19	24	ns
<sup>t</sup> PZH	Output	Any			-	15	30		15	30	
<sup>t</sup> PZL	Control	City				19	30		20	30	ns
<sup>t</sup> PHZ	Output	Any	C <sub>L</sub> = 5 pF,	See Note 3		18	30		18	30	
<sup>t</sup> PLZ	Control		CL - 5 pr,	See Note 3		16	25		16	25	ns

¶tpLH = propagation delay time, low-to-high-level output

tpHL = propagation delay time, high-to-low-level output

tpzH = output enable time to high level

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

tpzL = output enable time to low level

tpHZ = output disable time from high level

tpLZ = output disable time from low level



<sup>§</sup>Not more than one output should be shorted at a time and duration of the short-circuit should not exceed one second.

NOTE 2: ICC is measured with all outputs open and all possible inputs grounded while achieving the stated output conditions.

#### recommended operating conditions

		SN54S'			SN745	,	UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	CIVIT
Supply voltage, V <sub>CC</sub>	4.5	5	5.5	4.75	5	5.25	٧
High-level output current, IOH			-2			6.5	mΑ
Low-level output current, IOL			20			20	mA
Operating free-air temperature, TA	55		125	0		70	°C

# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

							<b>'S257</b>			'S258		UNIT
	PARAME'	TER	TEST	CONDITIONS <sup>1</sup>	ī	MIN	TYP <sup>‡</sup>	MAX	MIN	TYP <sup>‡</sup>	MAX	UNIT
VIH	High-level input	voltage				2			2			V
VIL	Low-level input						., .	0.8			0.8	V
VIK	Input clamp vol		V <sub>CC</sub> = MIN,	I <sub>I</sub> = -18 mA				1.2			-1.2	V
	•		V <sub>CC</sub> = MIN, V <sub>IL</sub> = 0.8 V,	V <sub>IH</sub> = 2 V, I <sub>OH</sub> = -1 mA	SN745'	2.7			2.7			v
VOH	High-level outpu	it voltage	V <sub>CC</sub> = MIN,	V <sub>IH</sub> = 2 V,	SN54S'	2.4	3.4		2.4	3.4		,
			$V_{IL} = 0.8 V$ ,	IOH = MAX	SN74S'	2.4	3.2		2.4	3.2		
VOL	Low-level outpu	ıt voltage	V <sub>CC</sub> = MIN, V <sub>IL</sub> = 0.8 V,	V <sub>1H</sub> = 2 V, I <sub>OL</sub> = 20 mA				0.5			0.5	٧
lozh	Off-state output	· ·	V <sub>CC</sub> = MAX, V <sub>O</sub> = 2.4 V	V <sub>IH</sub> = 2 V,				50			50	μА
lozL	Off-state output	· ·	V <sub>CC</sub> = MAX, V <sub>O</sub> = 0.5 V	V <sub>IH</sub> = 2 V,				-50			-50	μА
l <sub>1</sub>	Input current a	t maximum	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 5.5 V	.,,			1			1	mA
	High-level	Sinput		07.1				100			100	μΑ
۱нн	input current	Any other	V <sub>CC</sub> = MAX,	, V <sub>1</sub> = 2.7 V				50			50	] "
	Low-level	S input		0.5.1/				4			-4	mA
11L	input current	Any other	V <sub>CC</sub> = MAX	V I = 0.5 V				-2			-2	
los	Short-circuit ou	itput current§	V <sub>CC</sub> = MAX			-40		-100	-40		-100	mA
		All outputs high					44	68		36	56	1
ICC	Supply current	All outputs low	VCC = MAX	, See Note 2			60	93		52	81	mA
		All outputs off	]				64	99		56	87	

<sup>†</sup>For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25 ^{\circ}\text{C}$ .

NOTE 2: ICC is measured with all outputs open and all possible inputs grounded while achieving the stated output conditions.

#### switching characteristics, VCC = 5 V, $TA = 25^{\circ}\text{C}$ , $RL = 280 \Omega$

	FROM	то	TEST		'S257			'S258		
PARAMETER¶	(INPUT)	(OUTPUT)	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNIT
tPLH	6 .	^			5	7.5		4	6	ns
tPHL	Data	Any			4.5	6.5		4	6	] '''
tPLH			$C_L = 15  pF$ ,		8.5	15		8	12	ns
tPHL	Select	Any	See Note 3		8.5	15		7.5	12	113
tPZH	Output				13	19.5		13	19.5	
tPZL	Control	Any			14	21		14	21	ns
tPHZ	Output	1	$C_L = 5 pF$ ,	<b>T</b>	5.5	8.5		5.5	8.5	
tPLZ	Control	Any	See Note 3		9	14		9	14	ns

¶f<sub>max</sub> = Maximum clock frequency

tpLH = propagation delay time, low-to-high-level output

tpHL = propagation delay time, high-to-low-level output

tpZH = output enable time to high level

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

 $t_{PZL} \equiv$  output enable time to low level  $t_{PHZ} \equiv$  output disable time from high level  $t_{PLZ} \equiv$  output disable time from low level



Not more than one output should be shorted at a time and duration of the short-circuit should not exceed one second.

### **PACKAGING INFORMATION**

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/ Ball Finish	MSL Peak Temp <sup>(3)</sup>	Samples (Requires Login)
5962-7603701VEA	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
5962-7603701VFA	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
5962-7603701VFA	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
7603701EA	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
7603701EA	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
7603701FA	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
7603701FA	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
76038012A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	Purchase Samples
76038012A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	Purchase Samples
7603801EA	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
7603801EA	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
7603801FA	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
7603801FA	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
8002301EA	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
8002301EA	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
8002301FA	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
8002301FA	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
JM38510/07906BEA	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
JM38510/07906BEA	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
JM38510/07906BFA	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
JM38510/07906BFA	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
JM38510/30906B2A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	Purchase Samples
JM38510/30906B2A	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	Purchase Samples
JM38510/30906BEA	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
JM38510/30906BEA	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
JM38510/30906BFA	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
JM38510/30906BFA	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SN54LS257BJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SN54LS257BJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SN54LS258BJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples





SN54LS258BJ   ACTIVE   CDIP   J   16   1   TBD   A42   N / A for Pkg Type	Samples (Requires Login)	MSL Peak Temp <sup>(3)</sup>	Lead/ Ball Finish	Eco Plan <sup>(2)</sup>	Package Qty	Pins	Package Drawing	Package Type	Status <sup>(1)</sup>	Orderable Device
SN54S257J         ACTIVE         CDIP         J         16         1         TBD         A42         N / A for Pkg Type           SN54S258J         ACTIVE         CDIP         J         16         1         TBD         A42         N / A for Pkg Type           SN54S258J         ACTIVE         CDIP         J         16         1         TBD         A42         N / A for Pkg Type           SN74LS257BD         ACTIVE         SOIC         D         16         40         Green (RoHS & CU NIPDAU Level-1-260C-UNLIM           SN74LS257BD         ACTIVE         SOIC         D         16         40         Green (RoHS & CU NIPDAU Level-1-260C-UNLIM           SN74LS257BDE4         ACTIVE         SOIC         D         16         40         Green (RoHS & CU NIPDAU Level-1-260C-UNLIM           SN74LS257BDE4         ACTIVE         SOIC         D         16         40         Green (RoHS & CU NIPDAU Level-1-260C-UNLIM           SN74LS257BDG4         ACTIVE         SOIC         D         16         40         Green (ROHS & CU NIPDAU Level-1-260C-UNLIM           SN74LS257BDR4         ACTIVE         SOIC         D         16         40         Green (ROHS & CU NIPDAU Level-1-260C-UNLIM           SN74LS257BDR8         ACTIVE <t< td=""><td>Purchase Samples</td><td>N / A for Pkg Type</td><td>A42</td><td>TBD</td><td>1</td><td>16</td><td>J</td><td>CDIP</td><td>ACTIVE</td><td>SN54LS258BJ</td></t<>	Purchase Samples	N / A for Pkg Type	A42	TBD	1	16	J	CDIP	ACTIVE	SN54LS258BJ
SN54S258J   ACTIVE   CDIP   J   16   1   TBD   A42   N / A for Pkg Type	Purchase Samples	N / A for Pkg Type	A42	TBD	1	16	J	CDIP	ACTIVE	SN54S257J
SN54S258J   ACTIVE   CDIP   J   16   1   TBD   A42   N / A for Pkg Type	Purchase Samples	N / A for Pkg Type	A42	TBD	1	16	J	CDIP	ACTIVE	SN54S257J
SN74LS257BD	Purchase Samples	N / A for Pkg Type	A42	TBD	1	16	J	CDIP	ACTIVE	SN54S258J
SN74LS257BDR   ACTIVE   SOIC   D   16   40   Green (RoHS & no Sb/Br)	Purchase Samples	N / A for Pkg Type	A42	TBD	1	16	J	CDIP	ACTIVE	SN54S258J
SN74LS257BDE4	Purchase Samples	Level-1-260C-UNLIM	CU NIPDAU	•	40	16	D	SOIC	ACTIVE	SN74LS257BD
SN74LS257BDR4	Purchase Samples	Level-1-260C-UNLIM	CU NIPDAU		40	16	D	SOIC	ACTIVE	SN74LS257BD
SN74LS257BDR64	Purchase Samples	Level-1-260C-UNLIM	CU NIPDAU	•	40	16	D	SOIC	ACTIVE	SN74LS257BDE4
SN74LS257BDG4	Purchase Samples	Level-1-260C-UNLIM	CU NIPDAU	`	40	16	D	SOIC	ACTIVE	SN74LS257BDE4
SN74LS257BDR	Purchase Samples	Level-1-260C-UNLIM	CU NIPDAU		40	16	D	SOIC	ACTIVE	SN74LS257BDG4
SN74LS257BDR         ACTIVE         SOIC         D         16         2500         Green (RoHS & no Sb/Br)         CU NIPDAU Level-1-260C-UNLIM           SN74LS257BDRE4         ACTIVE         SOIC         D         16         2500         Green (RoHS & CU NIPDAU Level-1-260C-UNLIM & no Sb/Br)           SN74LS257BDRE4         ACTIVE         SOIC         D         16         2500         Green (RoHS & CU NIPDAU Level-1-260C-UNLIM & no Sb/Br)           SN74LS257BDRG4         ACTIVE         SOIC         D         16         2500         Green (RoHS & CU NIPDAU Level-1-260C-UNLIM & no Sb/Br)           SN74LS257BDRG4         ACTIVE         SOIC         D         16         2500         Green (RoHS & CU NIPDAU Level-1-260C-UNLIM & no Sb/Br)	Purchase Samples	Level-1-260C-UNLIM	CU NIPDAU	•	40	16	D	SOIC	ACTIVE	SN74LS257BDG4
& no Sb/Br)           SN74LS257BDRE4         ACTIVE         SOIC         D         16         2500         Green (RoHS & no Sb/Br)         CU NIPDAU Level-1-260C-UNLIM           SN74LS257BDRE4         ACTIVE         SOIC         D         16         2500         Green (RoHS & CU NIPDAU Level-1-260C-UNLIM & no Sb/Br)           SN74LS257BDRG4         ACTIVE         SOIC         D         16         2500         Green (RoHS & CU NIPDAU Level-1-260C-UNLIM & no Sb/Br)           SN74LS257BDRG4         ACTIVE         SOIC         D         16         2500         Green (RoHS & CU NIPDAU Level-1-260C-UNLIM	Purchase Samples	Level-1-260C-UNLIM	CU NIPDAU	•	2500	16	D	SOIC	ACTIVE	SN74LS257BDR
& no Sb/Br)           SN74LS257BDRE4         ACTIVE         SOIC         D         16         2500         Green (RoHS & no Sb/Br)         CU NIPDAU Level-1-260C-UNLIM           SN74LS257BDRG4         ACTIVE         SOIC         D         16         2500         Green (RoHS & CU NIPDAU Level-1-260C-UNLIM & no Sb/Br)           SN74LS257BDRG4         ACTIVE         SOIC         D         16         2500         Green (RoHS & CU NIPDAU Level-1-260C-UNLIM	Purchase Samples	Level-1-260C-UNLIM	CU NIPDAU		2500	16	D	SOIC	ACTIVE	SN74LS257BDR
SN74LS257BDRG4         ACTIVE         SOIC         D         16         2500         Green (RoHS & no Sb/Br)         CU NIPDAU Level-1-260C-UNLIM & no Sb/Br)           SN74LS257BDRG4         ACTIVE         SOIC         D         16         2500         Green (RoHS & CU NIPDAU Level-1-260C-UNLIM & C	Purchase Samples	Level-1-260C-UNLIM	CU NIPDAU	`	2500	16	D	SOIC	ACTIVE	SN74LS257BDRE4
& no Sb/Br)           SN74LS257BDRG4         ACTIVE         SOIC         D         16         2500         Green (RoHS         CU NIPDAU         Level-1-260C-UNLIM	Purchase Samples	Level-1-260C-UNLIM	CU NIPDAU	`	2500	16	D	SOIC	ACTIVE	SN74LS257BDRE4
	Purchase Samples	Level-1-260C-UNLIM	CU NIPDAU		2500	16	D	SOIC	ACTIVE	SN74LS257BDRG4
	Purchase Samples	Level-1-260C-UNLIM	CU NIPDAU	•	2500	16	D	SOIC	ACTIVE	SN74LS257BDRG4
SN74LS257BN ACTIVE PDIP N 16 25 Pb-Free (RoHS) CU NIPDAU N / A for Pkg Type	Purchase Samples	N / A for Pkg Type	CU NIPDAU	Pb-Free (RoHS)	25	16	N	PDIP	ACTIVE	SN74LS257BN
SN74LS257BN ACTIVE PDIP N 16 25 Pb-Free (RoHS) CU NIPDAU N / A for Pkg Type	Purchase Samples	N / A for Pkg Type	CU NIPDAU	Pb-Free (RoHS)	25	16	N	PDIP	ACTIVE	SN74LS257BN
SN74LS257BN3 OBSOLETE PDIP N 16 TBD Call TI Call TI	Samples Not Available	Call TI	Call TI	TBD		16	N	PDIP	OBSOLETE	SN74LS257BN3
SN74LS257BN3 OBSOLETE PDIP N 16 TBD Call TI Call TI	Samples Not Available	Call TI	Call TI	TBD		16	N	PDIP	OBSOLETE	SN74LS257BN3
SN74LS257BNE4 ACTIVE PDIP N 16 25 Pb-Free (RoHS) CU NIPDAU N / A for Pkg Type	Purchase Samples	N / A for Pkg Type	CU NIPDAU	Pb-Free (RoHS)	25	16	N	PDIP	ACTIVE	SN74LS257BNE4





Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/ Ball Finish	MSL Peak Temp <sup>(3)</sup>	Samples (Requires Login)
SN74LS257BNE4	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Purchase Samples
SN74LS257BNSR	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS257BNSR	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS257BNSRE4	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS257BNSRE4	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS257BNSRG4	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS257BNSRG4	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS258BD	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS258BD	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS258BDE4	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS258BDE4	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS258BDG4	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS258BDG4	ACTIVE	SOIC	D	16	40	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS258BDR	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS258BDR	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS258BDRE4	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS258BDRE4	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS258BDRG4	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples





Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/ Ball Finish	MSL Peak Temp <sup>(3)</sup>	Samples (Requires Login)
SN74LS258BDRG4	ACTIVE	SOIC	D	16	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS258BN	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Purchase Samples
SN74LS258BN	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Purchase Samples
SN74LS258BN3	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	Samples Not Available
SN74LS258BN3	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	Samples Not Available
SN74LS258BNE4	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Purchase Samples
SN74LS258BNE4	ACTIVE	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Purchase Samples
SN74LS258BNSR	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS258BNSR	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS258BNSRE4	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS258BNSRE4	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS258BNSRG4	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74LS258BNSRG4	ACTIVE	SO	NS	16	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SN74S257N	NRND	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Samples Not Available
SN74S257N	NRND	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Samples Not Available
SN74S257N3	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	Samples Not Available
SN74S257N3	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	Samples Not Available
SN74S257NE4	NRND	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Samples Not Available
SN74S257NE4	NRND	PDIP	N	16	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Samples Not Available
SN74S258DR	OBSOLETE	SOIC	D	16		TBD	Call TI	Call TI	Samples Not Available
SN74S258DR	OBSOLETE	SOIC	D	16		TBD	Call TI	Call TI	Samples Not Available
SN74S258N	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	Samples Not Available
SN74S258N	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	Samples Not Available
SN74S258N3	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	Samples Not Available
SN74S258N3	OBSOLETE	PDIP	N	16		TBD	Call TI	Call TI	Samples Not Available
SNJ54LS257BFK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	Purchase Samples

### **PACKAGE OPTION ADDENDUM**



www.ti.com 7-Jun-2010

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/ Ball Finish	MSL Peak Temp <sup>(3)</sup>	Samples (Requires Login)
SNJ54LS257BFK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	Purchase Samples
SNJ54LS257BJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54LS257BJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54LS257BW	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54LS257BW	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54LS258BFK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	Purchase Samples
SNJ54LS258BFK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	Purchase Samples
SNJ54LS258BJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54LS258BJ	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54LS258BW	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54LS258BW	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54S257FK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	Purchase Samples
SNJ54S257FK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	Purchase Samples
SNJ54S257J	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54S257J	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54S257W	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54S257W	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54S258FK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	Purchase Samples
SNJ54S258FK	ACTIVE	LCCC	FK	20	1	TBD	POST-PLATE	N / A for Pkg Type	Purchase Samples
SNJ54S258J	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54S258J	ACTIVE	CDIP	J	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54S258W	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples
SNJ54S258W	ACTIVE	CFP	W	16	1	TBD	A42	N / A for Pkg Type	Purchase Samples

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

### PACKAGE OPTION ADDENDUM



www.ti.com 7-Jun-2010

**Pb-Free** (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes. **Pb-Free** (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL. Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

**Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

#### OTHER QUALIFIED VERSIONS OF SN54LS257B, SN54LS257B-SP, SN54LS258B, SN54S257, SN54S258, SN74LS258B, SN74LS258B, SN74S257, SN74S258:

Catalog: SN74LS257B, SN54LS257B, SN74LS258B, SN74S257, SN74S258

Military: SN54LS257B, SN54LS258B, SN54S257, SN54S258

Space: SN54LS257B-SP

NOTE: Qualified Version Definitions:

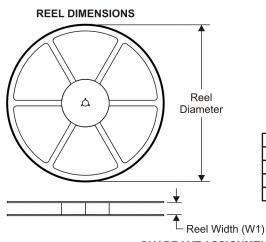
Catalog - TI's standard catalog product

Military - QML certified for Military and Defense Applications

Space - Radiation tolerant, ceramic packaging and qualified for use in Space-based application



### TAPE AND REEL INFORMATION





A0	Dimension designed to accommodate the component width
В0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



### \*All dimensions are nominal

Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN74LS257BDR	SOIC	D	16	2500	330.0	16.4	6.5	10.3	2.1	8.0	16.0	Q1
SN74LS257BNSR	SO	NS	16	2000	330.0	16.4	8.2	10.5	2.5	12.0	16.0	Q1
SN74LS258BDR	SOIC	D	16	2500	330.0	16.4	6.5	10.3	2.1	8.0	16.0	Q1
SN74LS258BNSR	SO	NS	16	2000	330.0	16.4	8.2	10.5	2.5	12.0	16.0	Q1

# PACKAGE MATERIALS INFORMATION

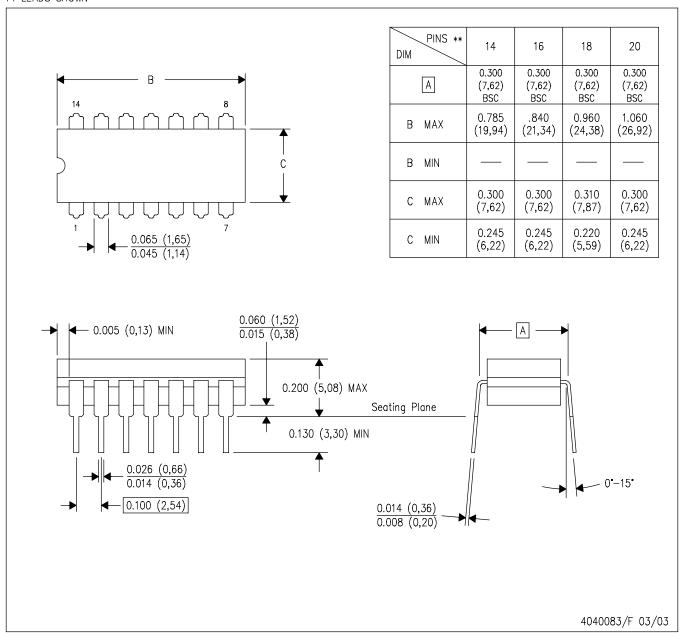
19-Mar-2008



\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN74LS257BDR	SOIC	D	16	2500	333.2	345.9	28.6
SN74LS257BNSR	SO	NS	16	2000	346.0	346.0	33.0
SN74LS258BDR	SOIC	D	16	2500	333.2	345.9	28.6
SN74LS258BNSR	SO	NS	16	2000	346.0	346.0	33.0

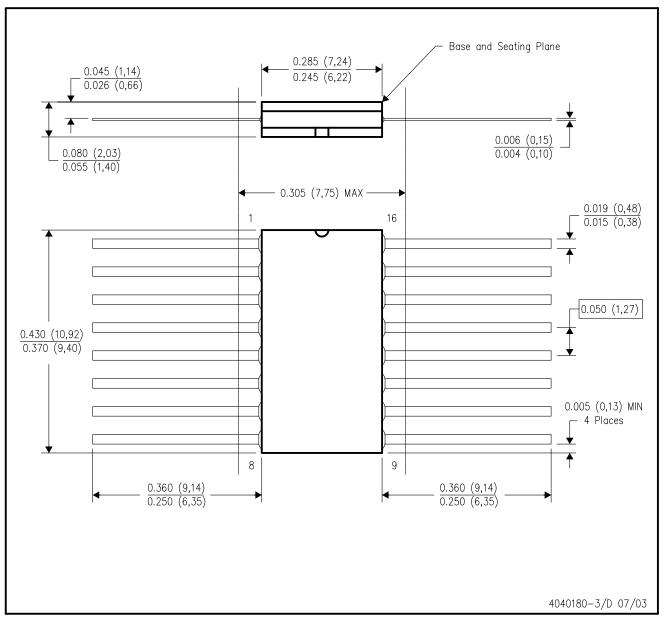
14 LEADS SHOWN



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package is hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
- E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.

# W (R-GDFP-F16)

# CERAMIC DUAL FLATPACK



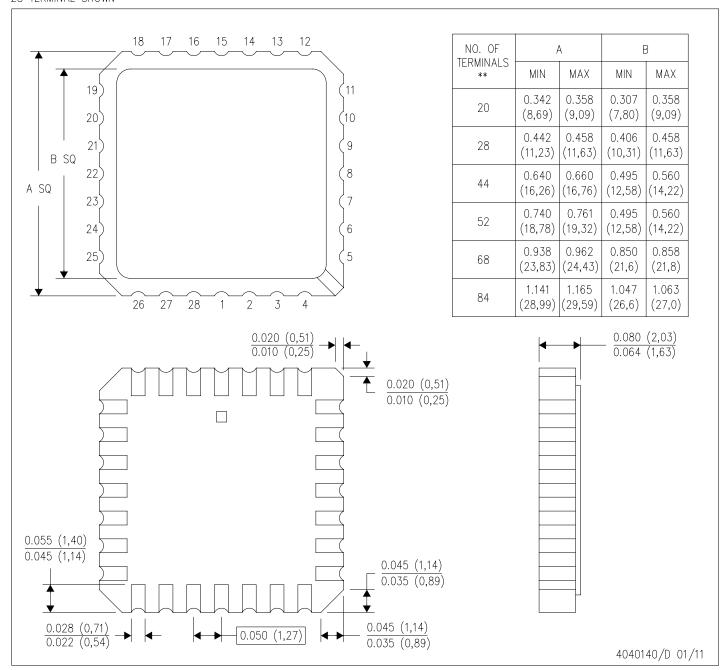
- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only.
- E. Falls within MIL STD 1835 GDFP1-F16 and JEDEC MO-092AC



# FK (S-CQCC-N\*\*)

## LEADLESS CERAMIC CHIP CARRIER

28 TERMINAL SHOWN

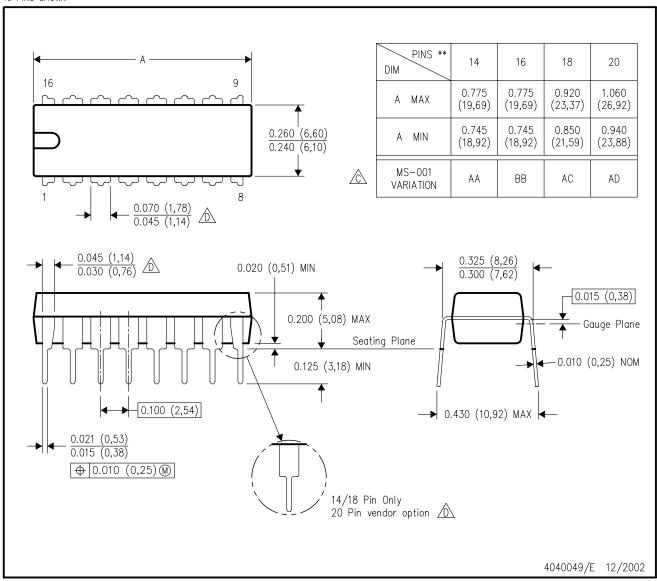


- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a metal lid.
- D. Falls within JEDEC MS-004

# N (R-PDIP-T\*\*)

### PLASTIC DUAL-IN-LINE PACKAGE

16 PINS SHOWN

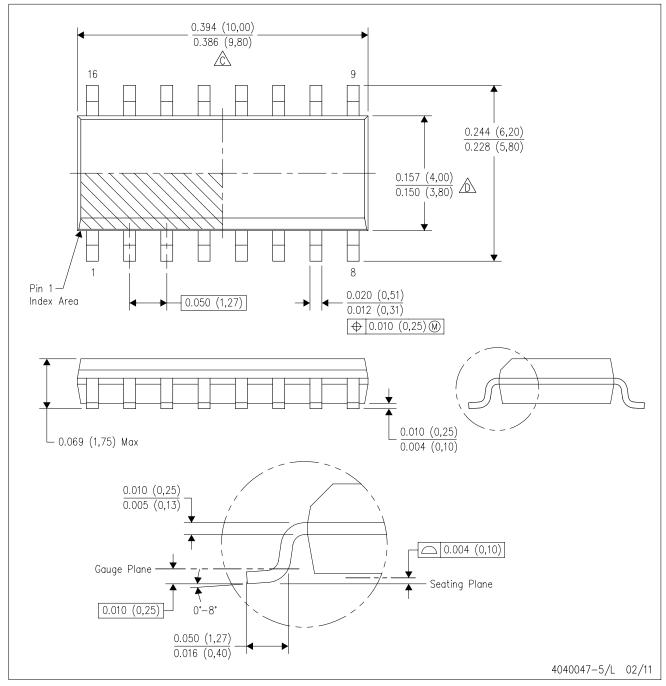


- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
- The 20 pin end lead shoulder width is a vendor option, either half or full width.



# D (R-PDSO-G16)

### PLASTIC SMALL OUTLINE

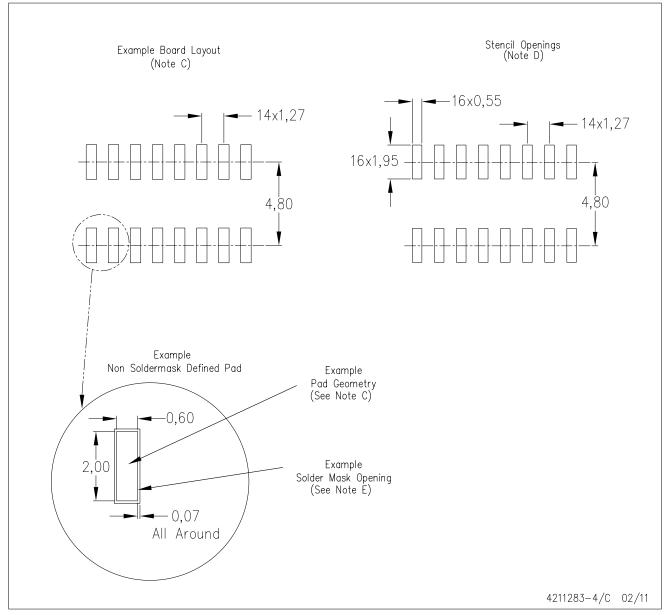


- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
- Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
- E. Reference JEDEC MS-012 variation AC.



# D (R-PDS0-G16)

## PLASTIC SMALL OUTLINE



- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Publication IPC-7351 is recommended for alternate designs.
- D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
- E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.

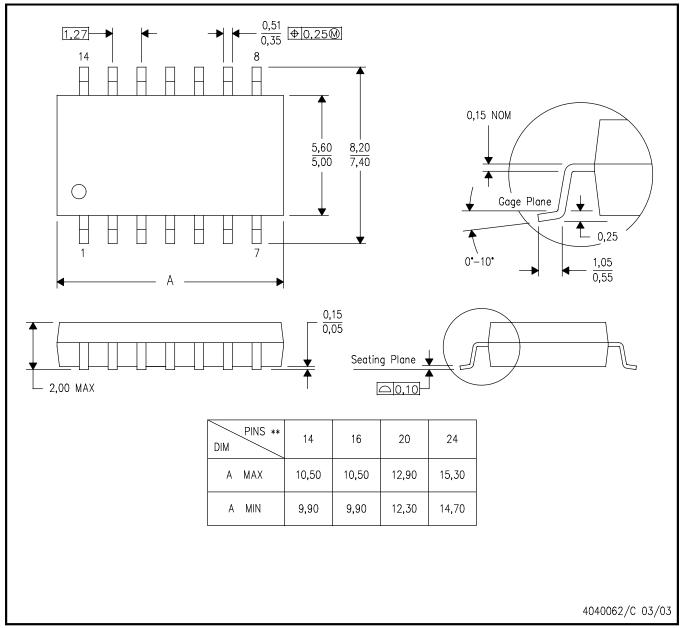


### **MECHANICAL DATA**

## NS (R-PDSO-G\*\*)

# 14-PINS SHOWN

### PLASTIC SMALL-OUTLINE PACKAGE



- a. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.



#### IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

TI products are not authorized for use in safety-critical applications (such as life support) where a failure of the TI product would reasonably be expected to cause severe personal injury or death, unless officers of the parties have executed an agreement specifically governing such use. Buyers represent that they have all necessary expertise in the safety and regulatory ramifications of their applications, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of TI products in such safety-critical applications, notwithstanding any applications-related information or support that may be provided by TI. Further, Buyers must fully indemnify TI and its representatives against any damages arising out of the use of TI products in such safety-critical applications.

TI products are neither designed nor intended for use in military/aerospace applications or environments unless the TI products are specifically designated by TI as military-grade or "enhanced plastic." Only products designated by TI as military-grade meet military specifications. Buyers acknowledge and agree that any such use of TI products which TI has not designated as military-grade is solely at the Buyer's risk, and that they are solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI products are neither designed nor intended for use in automotive applications or environments unless the specific TI products are designated by TI as compliant with ISO/TS 16949 requirements. Buyers acknowledge and agree that, if they use any non-designated products in automotive applications, TI will not be responsible for any failure to meet such requirements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

Products		Applications	
Audio	www.ti.com/audio	Communications and Telecom	www.ti.com/communications
Amplifiers	amplifier.ti.com	Computers and Peripherals	www.ti.com/computers
Data Converters	dataconverter.ti.com	Consumer Electronics	www.ti.com/consumer-apps
DLP® Products	www.dlp.com	Energy and Lighting	www.ti.com/energy
DSP	dsp.ti.com	Industrial	www.ti.com/industrial
Clocks and Timers	www.ti.com/clocks	Medical	www.ti.com/medical
Interface	interface.ti.com	Security	www.ti.com/security
Logic	logic.ti.com	Space, Avionics and Defense	www.ti.com/space-avionics-defense
Power Mgmt	<u>power.ti.com</u>	Transportation and Automotive	www.ti.com/automotive
Microcontrollers	microcontroller.ti.com	Video and Imaging	www.ti.com/video
RFID	www.ti-rfid.com	Wireless	www.ti.com/wireless-apps
RF/IF and ZigBee® Solutions	www.ti.com/lprf		

**TI E2E Community Home Page** 

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2011, Texas Instruments Incorporated

e2e.ti.com