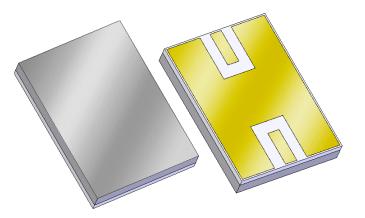


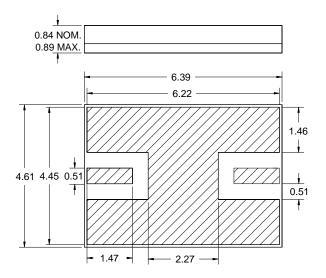
#### **Features**

- ISM Applications
- Usable bandwidth of 13 MHz
- Single-ended operation
- Ceramic Surface Mount Package
- Hermetic



### Package

#### Surface Mount 6.39 x 4.61 x 0.84 mm

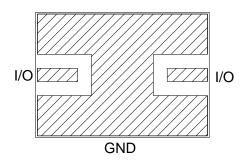


Overall width, length, and thickness are the only critical dimensions. All other dimensions are for reference only.

Dimensions shown are nominal in millimeters All tolerances are  $\pm 0.13$ mm except overall length and width  $\pm 0.25$ mm

Body: Sapphire Package: Alumina Terminations: Au plating 0.5 - 2.5μm, over a 2.0 – 6.0 μm Ni plating **Pin Configuration** 

**Bottom View** 



Pin No.	Description		
I/O	Input/Output		
GND	Ground		

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## **Electrical Specifications**<sup>(1)</sup>

**Operating Temperature Range:** <sup>(2)</sup> -40 to +85 °C

Parameter <sup>(3)</sup>	Minimum	Typical	Maximum	Unit
Center Frequency	910	915	920	MHz
Insertion Loss @ Fo	-	3	4.5	dB
3 dB Bandwidth <sup>(4)</sup>	13	18	-	MHz
40 dB Lower Frequency Edge	892.5	-	-	MHz
40 dB Upper Frequency Edge	-	-	937.5	MHz
VSWR @ Fo	-	1.8	2:1	
Source Impedance	-	50	-	Ω
Load Impedance	-	50	-	Ω

#### Notes:

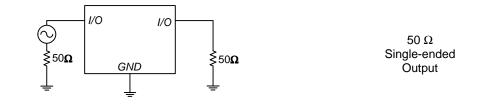
- 1. All specifications are based on the test circuit shown below
- 2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
- 3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances

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4. Referenced to the insertion loss at center frequency

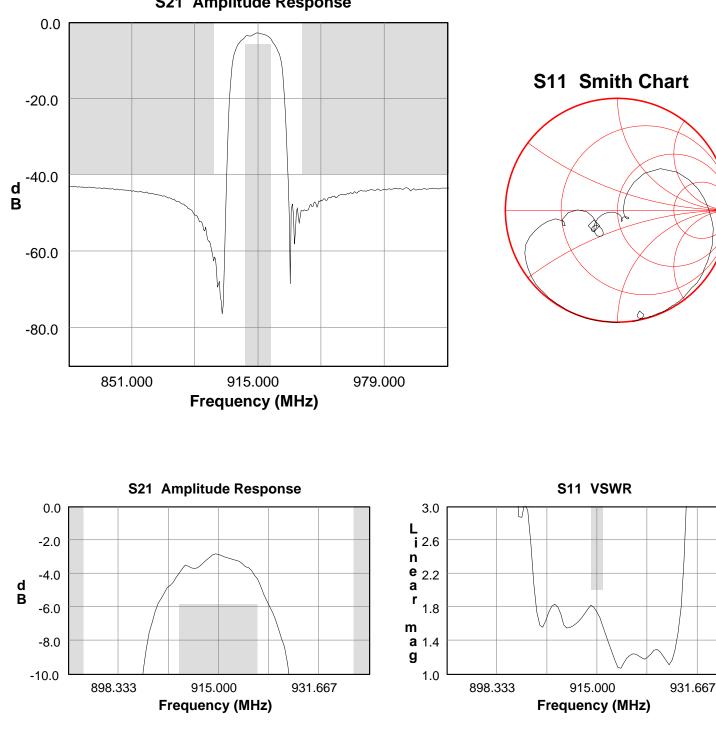
#### Test Circuit:

50 Ω Single-ended Input





## Typical Performance (at +25°C)



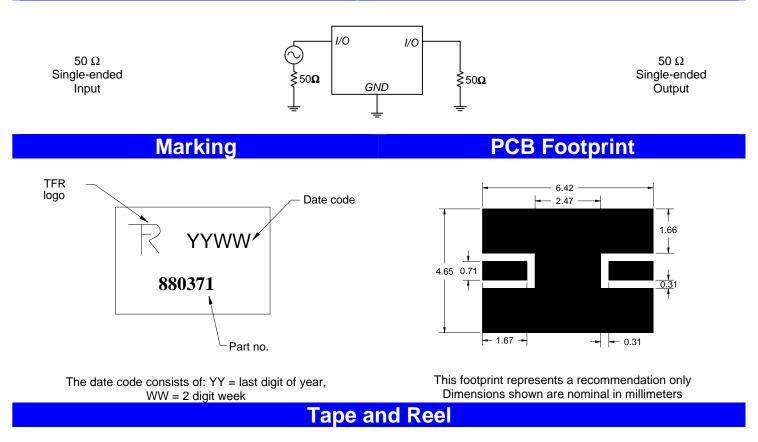
#### S21 Amplitude Response

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## **Matching Schematics**



Tape and Reel available upon request EIA-481

Tinning available per J-STD-001

ROHS compliant (no tinning) DFARS compliant

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## Data Sheet

Maximum Ratings							
Parameter	Symbol	Minimum	Maximum	Unit			
Operating Temperature Range	Т	-40	+85	°C			
Storage Temperature Range	T <sub>stg</sub>	-55	+100	О°			

#### Warnings



• Avoid ultrasonic exposure

## Links to Additional Technical Information

PCB Layout Tips

**Qualification Flowchart** 

**Soldering Profile** 

S-Parameters

Other Technical Information

Sawtek's liability is limited only to the Bulk Acoustic Wave (BAW) component(s) described in this data sheet. Sawtek does not accept any liability for applications, processes, circuits or assemblies, which are implemented using any Sawtek component described in this data sheet.

### **Contact Information**

**TriQuint** SEMICONDUCTOR 63140 Britta St. Bldg. C Bend, OR 97701 Phone: +1 (972) 994-8465 Fax: +1 (972) 994-8504 Email: <u>custservbend@tqs.com</u> Web: <u>www.triquint.com</u>

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Or contact one of our worldwide Network of <u>sales offices</u>, <u>Representatives or distributors</u>

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