Advanced semiconductor solutions for door electronics

Innovative, scalable system solutions for door electronics



October 2008



Door-actuator drivers

STMicroelectronics' actuator drivers are designed for state-of-the-art automotive door-module applications. Devices are characterised by a scalable actuator-driving concept, which includes a certain package and software compatibility to satisfy today's multiplicity of door electronics variants. The actuator drivers support all regular door-module loads such as lock motors, mirror leveling, mirror retract, defroster, and several lighting functions from incandescent bulbs to LEDs. For high-end cars a new electrochromic mirror glass control IP has been added to the family.

Actuator driver family reference table

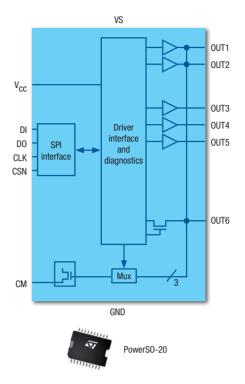
	L9949	L9950	L9951	L9953	L9953L ²	L9954	L9954L ²	L99DZ702
PowerSO-20	•	-	-	-	-	-	-	-
PowerSO-36	-	•	•	•1	•1	•1	•1	•1
PowerSSO-36	-	• XP	• XP	• XP	• XP	• XP	• XP	• XP
SPI	•	•	•	•	•	•	•	ST SPI
Reverse battery supply	-	•	•	•	•	•	•	•
PWM1	-	•	•	•	•	•	•	•
PWM2	-	•	•	•	•	•	•	•
Current monitor	Selected HS 3x	Selected HS 5x	All HS 5x	Selected HS 4x	Selected HS 4x	Selected HS 4x	Selected HS 4x	All HS 11x
Recovery mode	-	•	•	•	•	•	•	•
Heater control (R _{DS(on)} , I limit)	100 mΩ 6.3 A	100 mΩ 6 A	-	100 mΩ 6 A	$\begin{array}{c} 100 \text{ m}\Omega \\ \text{6 A} \end{array}$	100 mΩ 6 A	$\begin{array}{c} 100 \text{ m}\Omega \\ \text{6 A} \end{array}$	90 mΩ 6 A
Programmable HS R _{DS(on)}	-	-	-	-	$2x$ $500~\text{m}\Omega/$ $1800~\text{m}\Omega$	-	$2 \mathrm{x}$ 500 m Ω / 1800 m Ω	$2x$ $500 \text{ m}\Omega$ / $1800 \text{ m}\Omega$
LED mode	-	-	-	-	2 x	-	2 x	4 x
Bulb/LED control (R _{DS(on)} , I limit)	-	4x 800 mΩ 1.5 A	2x 800 mΩ 1.25 A	2x 500 mΩ 1.5 A	2x 500 mΩ 1.5 A	2x 500 mΩ 1.5 A	2x 500 mΩ 1.5 A	$\begin{array}{c} 4x \\ 500 \text{ m}\Omega \\ 1600 \text{ m}\Omega \\ 1800 \text{ m}\Omega \end{array}$
Lock control (R _{DS(on)} , I limit)	150 mΩ 6 A	150 mΩ 6 A	150 mΩ 7.4 A	150 mΩ 6 A	$\begin{array}{c} \text{150 m}\Omega \\ \text{6 A} \end{array}$	-	-	150 mΩ 6 A
Deadlock control (R _{DS(on)} , I limit)	-	$300~\text{m}\Omega$ $3~\text{A}$	$200~\text{m}\Omega$ $5~\text{A}$	-	-	-	-	$300~\text{m}\Omega$ $3~\text{A}$
Mirror adjustment (R _{DS(on)} , I limit)	800 mΩ 1.6 A	800 mΩ 1.5 A	-	800 mΩ 1.5 A	1600 mΩ 0.75 A	800 mΩ 1.5 A	1600 mΩ 0.75 A	$\begin{array}{c} \text{1600 m}\Omega \\ \text{0.75 A} \end{array}$
Mirror fold (R _{DS(on)} , I limit)	-	300 mΩ 3 A	-	-	-	-	-	300 mΩ 3 A
EC control	-	-	-	-	-	-	-	6-bit resolution 1.2 V/1.5 V

^{1.} On request only

^{2.} Product in development

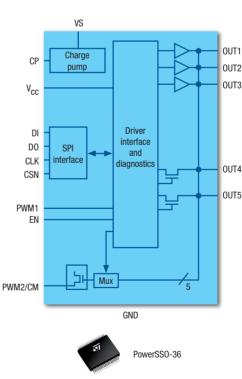
L9949 features

- One full bridge for 6 A load ($R_{DS(on)} = 150 \text{ m}\Omega$)
- Three half bridges for 1.6 A load ($R_{DS(on)} = 800 \text{ m}\Omega$)
- One high-side driver for 6 A load ($R_{DS(on)} = 100 \text{ m}\Omega$)
- Very low current consumption in standby mode $(I_s < 6 \mu A, typical T_i < 85 °C)$
- All outputs short-circuit protected
- All outputs over-temperature protected
- Open load diagnostics for all outputs
- Overload diagnostics for all outputs
- Separated half bridges for door lock motor
- Serial peripheral interface (SPI) to microcontroller
- Current monitor output for high-side drivers OUT1, OUT2 and OUT6
- Undervoltage and overvoltage switch off



L9951 features

- One half bridge for 7.4 A load ($R_{DS(on)} = 150 \text{ m}\Omega$)
- Two half bridges for 5 A load ($R_{DS(on)} = 200 \text{ m}\Omega$)
- Two high-side drivers for 1.25 A load ($R_{DS(on)} = 800 \text{ m}\Omega$)
- Programmable softstart function to drive loads with higher inrush currents (> 7.4 A, > 1.25 A)
- Very low current consumption in standby mode $(I_s < 3 \mu A, typical T_i < 85 °C)$
- All outputs short-circuit protected
- Current monitor output for high-side drivers OUT1, OUT2, OUT3, OUT4 and OUT5
- All outputs over-temperature protected
- Open load diagnostics for all outputs
- Overload diagnostics for all outputs
- PWM control of all outputs
- Charge pump output for reverse polarity protection
- Serial peripheral interface (SPI) to microcontroller
- Undervoltage and overvoltage switch off

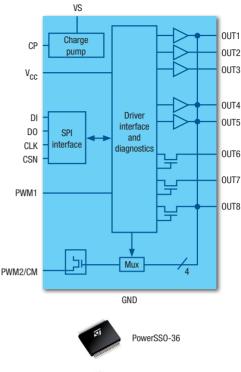






L9953 features

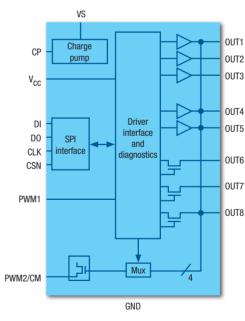
- One full bridge for 6 A load ($R_{DS(on)} = 150 \text{ m}\Omega$)
- Three half bridges for 1.5 A load ($R_{DS(on)} = 800 \text{ m}\Omega$)
- One high-side driver for 6 A load ($R_{DS(on)} = 100 \text{ m}\Omega$)
- Two high-side drivers for 1.5 A load ($R_{DS(on)} = 500 \text{ m}\Omega$)
- Programmable softstart function to drive loads with higher inrush currents (> 6 A, > 1.5 A)
- Very low current consumption in standby mode (I_s < 6 μA, typical T_i < 85 °C)</p>
- All outputs short-circuit protected
- Current monitor output for high-side drivers OUT1, OUT4, OUT5 and OUT8
- All outputs over-temperature protected
- Open load diagnostics for all outputs
- Overload diagnostics for all outputs
- Separated half bridges for door lock motor
- PWM control of all outputs
- Charge pump output for reverse polarity protection
- Serial peripheral interface (SPI) to microcontroller
- Undervoltage and overvoltage switch off





L9953LXP features

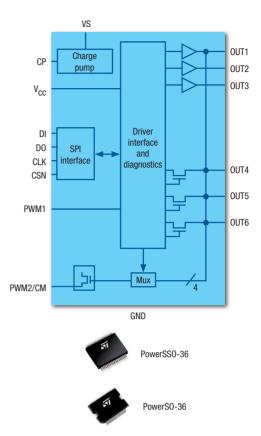
- One full bridge for 6 A load ($R_{DS(on)} = 150 \text{ m}\Omega$)
- Three half bridges for 0.75 A load ($R_{DS(on)} = 1600 \text{ m}\Omega$)
- One high-side driver for 6 A load ($R_{DS(on)} = 100 \text{ m}\Omega$)
- Two configurable high-side drivers for up to 1.5 A load $(R_{DS(on)} = 500 \text{ m}\Omega)$ or 0.35 A load $(R_{DS(on)} = 1800 \text{ m}\Omega)$
- Programmable softstart function to drive loads with higher inrush currents (> 6 A, > 1.5 A)
- Very low current consumption in standby mode (l_s < 6 µA, typical T_i < 85 °C)</p>
- All outputs short-circuit protected
- Current monitor output for high-side drivers OUT1, OUT4, OUT5 and OUT8
- All outputs over-temperature protected
- Open load diagnostics for all outputs
- Overload diagnostics for all outputs
- Separated half bridges for door lock motor
- PWM control of all outputs
- Charge pump output for reverse polarity protection
- Serial peripheral interface (SPI) to microcontroller
- Undervoltage and overvoltage switch off





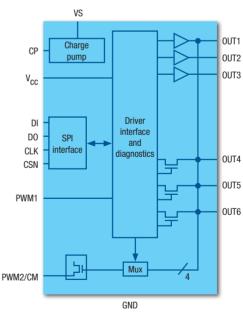
L9954 features

- Three half bridges for 1.5 A load ($R_{DS(on)} = 800 \text{ m}\Omega$)
- One high-side driver for 6 A load ($R_{DS(an)} = 100 \text{ m}\Omega$)
- Two high-side drivers for 1.5 A load ($R_{DS(on)} = 500 \text{ m}\Omega$)
- Programmable softstart function to drive loads with higher inrush currents (> 6 A, > 1.5 A)
- Very low current consumption in standby mode
 (I_s < 6 μA, typical T_i < 85 °C)
- All outputs short-circuit protected
- Current monitor output for high-side drivers OUT1, OUT4, OUT5 and OUT6
- All outputs over-temperature protected
- Open load diagnostics for all outputs
- Overload diagnostics for all outputs
- PWM control of all outputs
- Charge pump output for reverse polarity protection
- Serial peripheral interface (SPI) to microcontroller
- Undervoltage and overvoltage switch off



L9954LXP features

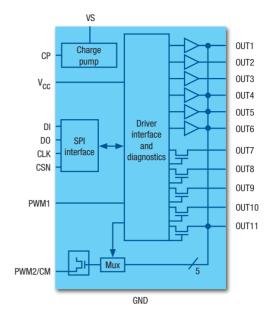
- Three half bridges for 0.75 A load ($R_{DS(on)} = 1600 \text{ m}\Omega$)
- One high-side driver for 6 A load ($R_{DS(on)} = 100 \text{ m}\Omega$)
- Two configurable high-side drivers for up to 1.5 A load $(R_{DS(n)} = 500 \text{ m}\Omega)$ or 0.35 A load $(R_{DS(n)} = 1800 \text{ m}\Omega)$
- Programmable softstart function to drive loads with higher inrush currents (> 6 A, > 1.5 A)
- Very low current consumption in standby mode (I_s < 6 µA, typical T_i < 85 °C)</p>
- All outputs short-circuit protected
- Current monitor output for high-side drivers OUT1, OUT4, OUT5 and OUT6
- All outputs over-temperature protected
- Open load diagnostics for all outputs
- Overload diagnostics for all outputs
- PWM control of all outputs
- Charge pump output for reverse polarity protection
- Serial peripheral interface (SPI) to microcontroller
- Undervoltage and overvoltage switch off





L9950 features

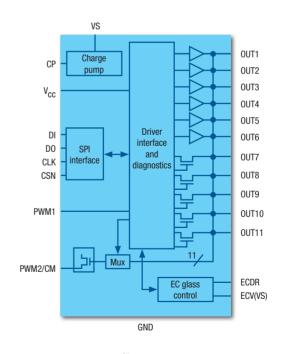
- One full bridge for 6 A load ($R_{DS(on)} = 150 \text{ m}\Omega$)
- Two half bridges for 3 A load ($R_{DS(on)}$ = 300 m Ω)
- Two half bridges for 1.5 A load ($R_{DS(on)} = 800 \text{ m}\Omega$)
- One high-side driver for 6 A load ($R_{DS(n)} = 100 \text{ m}\Omega$)
- Four high-side drivers for 1.5 A load ($R_{DS(nn)} = 800 \text{ m}\Omega$)
- Programmable softstart function to drive loads with higher inrush currents (> 6 A, > 1.5 A)
- Very low current consumption in standby mode (I_s < 6 μ A, I_{cc} < 25 μ A, typical T_i < 85 °C)
- All outputs short-circuit protected
- Current monitor output for high-side drivers OUT1, OUT4, OUT5, OUT6 and OUT11
- All outputs over-temperature protected
- Open load diagnostics for all outputs
- Overload diagnostics for all outputs
- Separated half bridges for door lock motor
- PWM control of all outputs
- Charge pump output for reverse polarity protection
- Serial peripheral interface (SPI) to microcontroller
- Undervoltage and overvoltage switch off





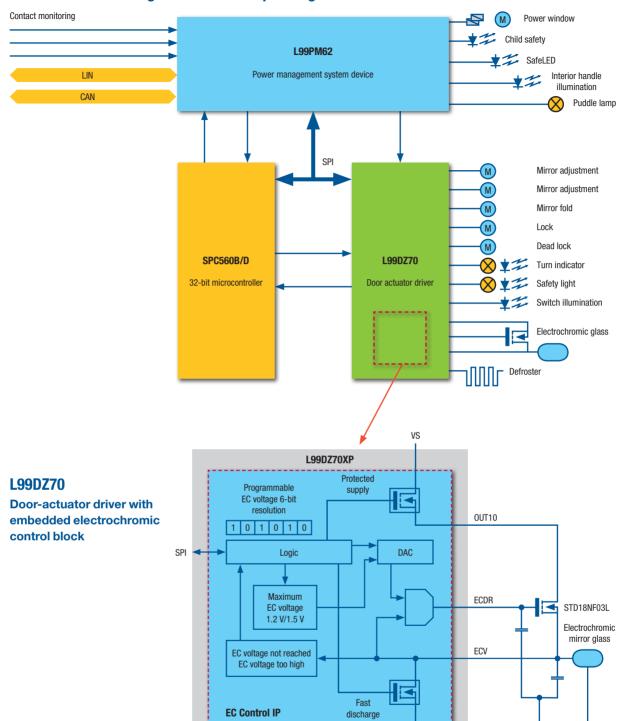
L99DZ70XP features

- Embedded electrochromic glass control block
- One full bridge for 6 A load ($R_{DS(on)} = 150 \text{ m}\Omega$)
- Two half bridges for 3 A load ($R_{DS(on)} = 300 \text{ m}\Omega$)
- Two half bridges for 0.75 A load ($R_{DS(on)} = 1600 \text{ m}\Omega$)
- One high-side driver for 6 A load ($R_{DS(on)} = 90 \text{ m}\Omega$)
- Two configurable high-side drivers for up to 1.5 A load $(R_{DS(on)} = 500 \text{ m}\Omega)$ or 0.4 A load $(R_{DS(on)} = 1800 \text{ m}\Omega)$
- Two high-side drivers for 0.5 A load ($R_{DS(on)} = 1600 \text{ m}\Omega$)
- Programmable softstart function to drive loads with higher inrush currents (> 6 A, > 1.5 A)
- Very low current consumption in standby mode (I_s < 6 μ A, I_{cc} < 5 μ A, typical T_i < 85 °C)
- All outputs short-circuit protected
- Current monitor output for all high-side drivers
- All outputs over-temperature protected
- Open load and overcurrent protection and diagnostics for all outputs
- PWM control of all outputs
- Charge pump output for reverse polarity protection
- Undervoltage and overvoltage switch off





L99DZ70/L99PM62: high-end driver and passenger door



L99DZ70XP electrochromic (EC) control block features

- EC voltage control with 6-bit resolution (max voltage = 1.5 V)
- Maximum voltage limitation programmable at 1.2 V and 1.5 V to protect the external EC glass
- All programming and diagnosis via SPI
- Protected supply for external drop regulator MOSFET (STD18NF03L)
- Fast discharge switch to GND for fast brightening of EC glass
- Voltage-not-reached diagnosis
- Voltage-too-high diagnosis

GND

Power management devices

STMicroelectronics' advanced power management devices contain LIN and HS-CAN physical layers. They enable the application to achieve an ultra low quiescent current down to $7\,\mu\text{A}$, while providing comprehensive fail-safe functionality to satisfy safety requirements for applications such as power window control.

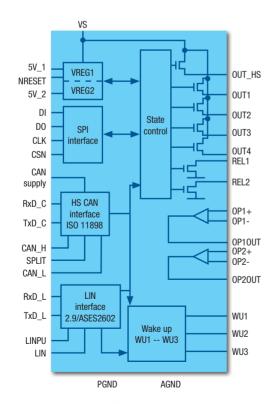
Power management family reference table

		PowerSS0-36	SO-16 narrow	SPI	LIN 2.1 SAEJ 2602 compliant	HS CAN ISO 11898	V1 regulator (mA)	V2 regulator (mA)	Window watchdog (ms)	Fail-safe output	Contact monitoring inputs	Cyclic contact monitoring	Cyclic wake	Direct drive for high-side drivers	Timers	PWM inputs	V _s /temp monitoring output	High-side drivers	Low-side drivers	Digital outputs	Operational amplifiers	Advanced thermal protection	V _s over/undervoltage detection
	L9952GXP	•	_	•	•	-	250	100	10	•	4	•	-	-	2	2	-	1 x 1 Ω 4 x 7 Ω	2 x 2 Ω	2	2	•	•
L	99PM62XP1	•	-	ST SPI	•	•	250	100	•2	•	3	•	•	-	2	4 x internal	-	1 x 1 Ω 4 x 7 Ω	2 x 2 Ω	-	2	•	•
	L99PM60S1	-	•	ST SPI	•	-	100	-	10	•	-	Direct drive	-	•	-	Direct drive	•	2 x 7 Ω	2 x 2 Ω	-	-	•	•

^{1.} Product in development

L99PM62

Companion chip with embedded HS-CAN and LIN physical layers

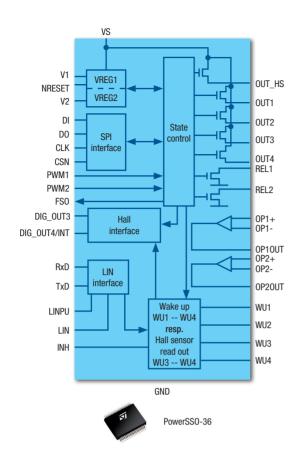


PowerSS0-36

L99PM62 features

- 5 V low-drop voltage regulator for microcontroller supply (250 mA)
- 5 V low-drop voltage regulator for peripheral supply (100 mA)
- Voltage regulator stable with 220 nF load capacitor
- Very low standby current
 - 12 μA in V_{bat} standby
 - 52 μA in V1 standby
 - 63 μA in cyclic wake-up (50 ms)
- Programmable window watchdog with fail-safe functionality
- Programmable V1 reset generator
- Programmable timeout interrupt
- Wake-up logic with cyclic contact monitoring
- Three configurable wake-up inputs
- LIN 2.1 compliant (SAEJ2602 compatible) physical layer
- HS-CAN (ISO 11898) physical layer
- Separate GND pin for physical layer
- 24-bit SPI interface for mode control and diagnostics
- ST SPI with fail-safe functionality
- Seven output drivers
 - Four high-side drivers for LED or hall sensor supply ($R_{DS(on)} = 7 \Omega$)
 - One high-side driver ($R_{DS(on)} = 1 \Omega$)
 - Two low-side relay drivers ($R_{DS(on)} = 2 \Omega$)
- Four internal PWM timers with phase shift
- All outputs short-circuit and temperature protected
- Two op-amps for GND compatible current sensing
- Temperature warning and thermal shutdown
- Programmable undervoltage and overvoltage switch off

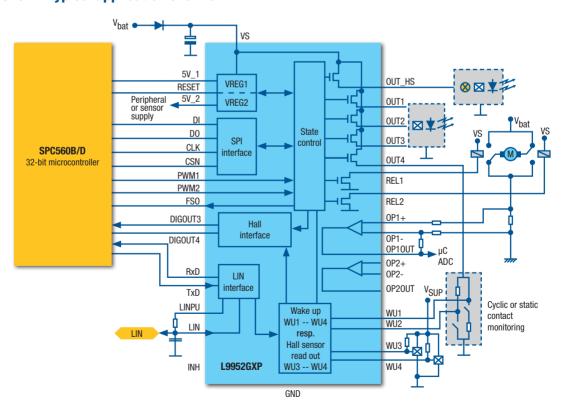
^{2.} Programmable



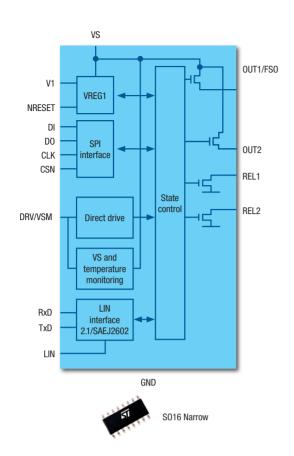
L9952GXP features

- 5 V low-drop voltage regulator for microcontroller supply (250 mA)
- 5 V low-drop voltage regulator for peripheral supply (100 mA)
- Voltage regulator stable with 220 nF load capacitor
- Very low standby current
 - 7 μA in V_{hat} standby
 - 45 μA in V1 standby
 - 75 μA in cyclic wake-up (50 ms)
- Window watchdog with fail-safe functionality
- Fail-safe output
- Wake-up logic with cyclic contact monitoring
- Four configurable wake-up inputs
- LIN 2.1 compliant (SAEJ2602 compatible) physical layer
- Seven output drivers
 - Four high-side drivers for LED or hall sensor supply ($R_{DS(nn)} = 7 \Omega$)
 - One high-side driver ($R_{DS(on)} = 1 \Omega$)
 - Two low side relay drivers $(R_{DS(on)} = 2 \Omega)$
- All outputs short-circuit and temperature protected
- Two op-amps for GND compatible current sensing
- Temperature warning and thermal shutdown
- Undervoltage and overvoltage switch off

L9952GXP: typical application overview



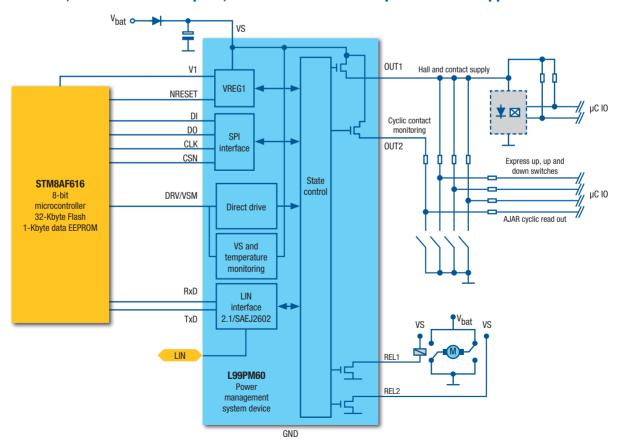
The companion chip L9952GXP provides power management for the entire door system and also controls several loads from the power window relay to PWMed LEDs. Furthermore, wake-up is provided via contact monitoring and Hall sensor inputs, as well as via the CAN and LIN transceivers.



L99PM60 features

- 5 V low-drop voltage regulator (100 mA)
- Voltage regulator stable with 220 nF load capacitor
- Programmable V1 reset generator
- Very low standby current
 - 7 μA in V_{bat} standby
 - 45 µA in V1 standby
- Window watchdog with fail-safe functionality
- Fail-safe output
- 16-bit SPI interface with fail-safe functionality
- Supply voltage monitoring
- LIN 2.1 compliant (SAEJ2602 compatible) physical layer
- High-speed LIN Flash mode up to 100 Kbit/s
- Two high-side drivers for LED, Hall sensor or contact supplies (R_{DS(on)} = 7 Ω)
- Two relay drivers ($R_{DS(on)} = 2 \Omega$)
- All outputs short-circuit and temperature protected
- Temperature warning and thermal shutdown
- Analog temperature monitoring output
- Direct drive feature for high-side drivers
- Undervoltage and overvoltage switch off

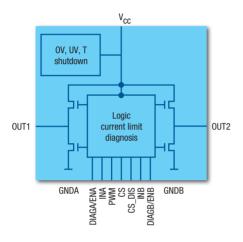
L99PM60, STM8A: flexible chip set, tailored for mechatronic power window applications



H-bridge motor drivers for power windows, door locks and mirror adjustment

STMicroelectronics' single-package power bridges (VNH3ASP30 and VNH5019) are able to support a load current of up to 30 A – ideal candidates for semiconductor-driven power window applications. They support PWM up to 20 kHz to allow soft start up and soft shutdown, increasingly required by the market. Door locks may be driven in standalone configuration using a device from the VN(5)77x H-bridge driver family.

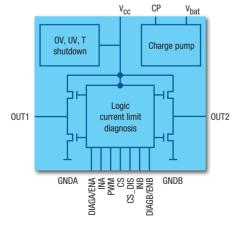
The L99MC6 is a new device for standalone mirror adjustment. A key benefit of this circuit is the tiny PowerSSO-16 package, following the trend toward smaller PCB outlines.





VNH3ASP30 features

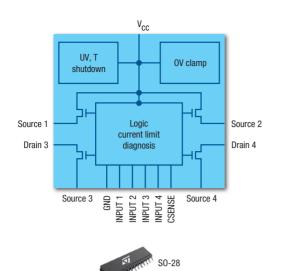
- High side: 2 x 30 mΩ
- Low side: $2 \times 12 \text{ m}\Omega$
- 20 kHz PWM operation at low side
- Output current: 30 A
- Current sense output
- Current limitation
- 5 V logic level compatible inputs
- Undervoltage and overvoltage shutdown
- Thermal shutdown
- Cross-conduction protection
- Low standby power consumption
- Protection against loss of ground and loss of V
- Package: MultiPowerSO-30





VNH5019 features

- High side: 2 x 14 mΩ
- Low side: 2 x 5 mΩ
- 20 kHz PWM operation at low side
- Output current: 30 A
- Charge pump output to control reverse polarity MOSFET
- Current sense output
- Current limitation
- 3 V logic level compatible inputs
- Undervoltage and overvoltage shutdown
- Thermal shutdown
- Cross-conduction protection
- Low standby power consumption
- Protection against loss of ground and loss of V
- Package: MultiPowerSO-30



VN5772AK features

High-side: 2 x 50 mΩ
 Low-side: 2 x 50 mΩ
 Output current: 18 A
 Current sense output
 Current limitation

5 V logic level compatible inputs

Undervoltage shutdown

Overvoltage clamp

Thermal shutdown

Low standby power consumption

 \blacksquare Protection against loss of ground and loss of V_{cc}

Package: SO-28

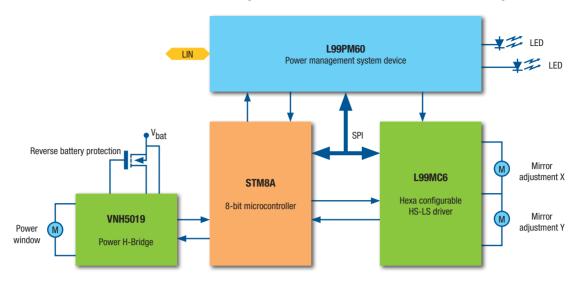


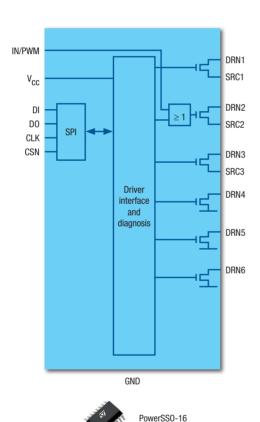
Part number	R _{DS(on)} (mΩ) per leg	I _{ым} (A) typical	Operating range (V)	Status	Thermal shutdown	Package
VN770K	220	10	5.5 to 36	Digital	HS/LS	SO-28
VN771K	95	9	5.5 to 36	Digital	HS/LS	SO-28
VN772K	120	9	5.5 to 36	Digital	HS/LS	SO-28
VN5770AK	280	10	4.5 to 36	Analog	HS/LS	SO-28
VN5772AK	100	18	4.5 to 36	Analog	HS/LS	SO-28

H-bridge family reference table (high-current)

Part number	R _{DS(on)} (mΩ) per leg	I _{lim} (A) typical	Operating range (V)	Status	Thermal shutdown	Max PWM frequency (kHz)	Package
VNH2SP30	19	30	5.5 to 16	Analog	HS	20	MultiPowerSO-30
VNH3SP30	45	30	5.5 to 36	Digital	HS	10	MultiPowerSO-30
VNH3ASP30	45	30	5.5 to 16	Analog	HS	20	MultiPowerS0-30
VNH5019	19	30	4.5 to 24	Analog	HS/LS	20	MultiPowerSO-30

VNH5019/L99MC6: semiconductor driven power window and standalone mirror adjustment





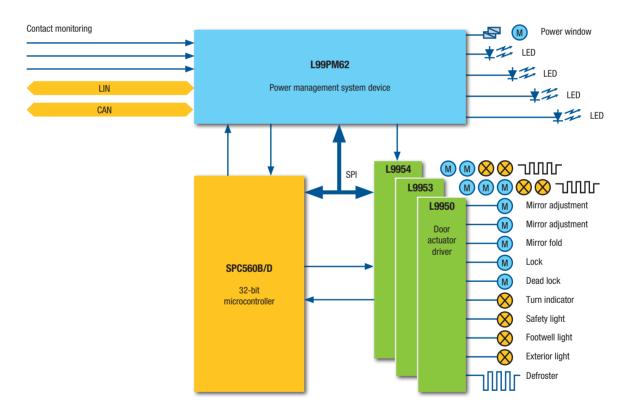
L99MC6 features

- Configurable up to three high-side or six low-side drivers
- \blacksquare R_{DS(on)} max = 1.0 Ω @ T_i = 25 °C
- Current limit of each output: 0.6 A min
- Direct input for channel 2, configurable for other outputs
- ST SPI interface (16-bit) for data communication and diagnostics
- Diagnosis of open-load detection in off state
- Diagnosis of shorted-load detection in on state
- Programmable bridge, LED, bulb mode
- Logic supply voltage compatible with 3.3 V and 5 V
- Output voltage clamping
- Temperature pre-warning and thermal shutdown
- Package: PowerSSO-16

L9950, L9953, L9954 family approach

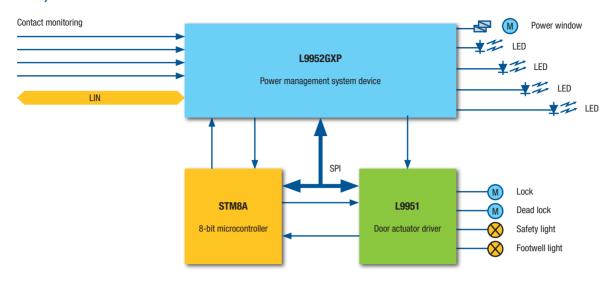
- Actuator driver family covering various front-door variants
- Scalable family approach with:
 - Hardware (HW) compatibility, pin-to-pin
 - Software (SW) compatibility
- Devices can be changed using the same SW
 - Each physical pin/output is controlled by the same SPI bit
 - Each diagnostic data for the same physical pin/output is indicated by the same status bit
- Same PCB can be used for different door variants
 - To be defined at customer line end with the corresponding driver
 - Only 1 PCB to be developed, qualified and maintained by the customer
- Different packages available: PowerSO-36 and PowerSSO-36

L9950, L9953, L9954 family approach for front-door modules

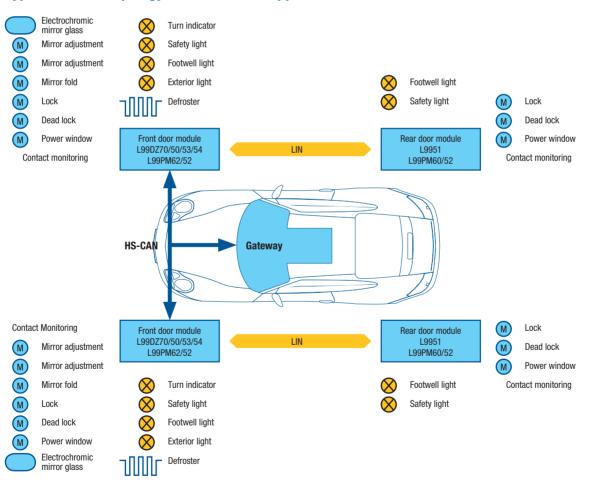


The actuator drivers L9950, L9953 and L9954 are interchangeable based on a common PC-board layout and wiring. A unique PC-board design may thus be used for the entire door module for a car. Even for software driving via the SPI interface, a modular structure, valid for all actuator drivers, may be developed.

L9952, L9951: low-end rear door



Typical network topology for door module applications



The diagram shows a typical networking topology for how to interconnect four door modules to a central gateway. For a fast access time, front-door modules may best be addressed via a CAN bus, either high or low speed. For the rear-door modules, a cost-effective LIN-bus wiring is sufficient. STMicroelectronics offers a flexible, cost-effective driver approach for each system partitioning which supports the entire actuator portfolio available for all door-module applications.

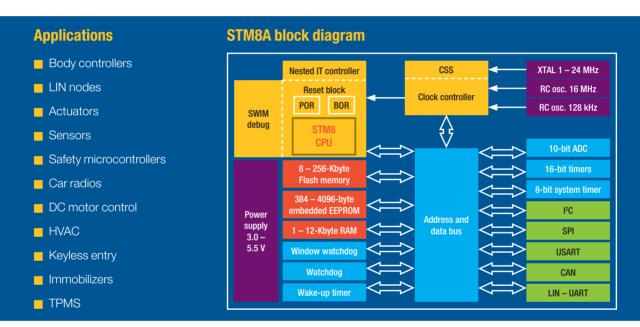
STM8A MCU family: automotive 8-bit Flash microcontrollers

STMicroelectronics introduces the STM8A, a new product line of 8-bit Flash microcontrollers dedicated to the specific needs of automotive applications. From product specifications, on through design and manufacturing, our focus is on reliability, application robustness and low system cost.

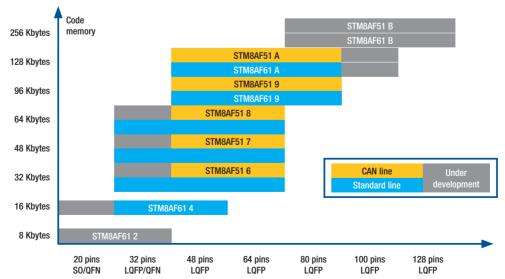
The modularity and compatibility of this product family offer the perfect platform solution in an environment where requirements change and the ability to reuse design concepts leads to competitive cost advantages.

STM8A devices are particularly suited for applications requiring non-volatile data storage. The integrated true data EEPROM features state-of-the-art endurance and data retention throughout the full temperature range. Complex EEPROM emulation strategies are no longer required.

All products are optimized for reduced power and low EMC. The efficient STM8 core delivers leading-edge execution speed even at low CPU clock frequencies. Powertrain applications benefit from the extended temperature range up to 145 °C. The STM8A is the ideal and economic solution for the growing market of automotive 8-bit applications.



STM8A product lines



STM8A family overview

Part number		Prog. (bytes)	Data EEPROM (bytes)	RAM (bytes)	10-bit A/D	Timers (IC/OC/PWM)	Serial interfaces	CAN¹	Other	VDD
128-pin LQFP	STM8AF61BC	256 K	4 K	12K		1 x 8-bit	LIN-UART,			
	STM8AF61BB	256 K	4 K	12 K	28	4 x 16-bit	2 x USART,			
100-pin LQFP	STM8AF61AB	128 K	2 K	6 K		(12/12/12)	UART, 2 x SPI, 2 x I ² C			
	STM8AF61BA	256 K	4 K	12 K						
80-pin LQFP	STM8AF61AA	128 K	2 K	6 K		1 x 8-bit 3 x 16-bit (9/9/9)	LIN-UART USART SPI, I ² C	Optional	Window and standard watchdogs 16 MHz RC oscillator 128 kHz RC	3 V to 5.5 V
	STM8AF619A	96 K	2 K	6 K						
	STM8AF61A9	128 K	2 K	6 K	16					
	STM8AF6199	96 K	2 K	6 K	10					
64-pin LQFP	STM8AF6189	64 K	1.5 K	4 K						
	STM8AF6179	48 K	1.5 K	3 K						
	STM8AF6169	32 K	1 K	2 K						
	STM8AF61A8	128 K	2 K	6 K						
	STM8AF6198	96 K	2 K	6 K						
48-pin	STM8AF6188	64 K	1.5 K	4 K	10					
LQFP/QFN	STM8AF6178	48 K	1.5 K	3 K	10				oscillator	
	STM8AF6168	32 K	1 K	2 K					CSS	
	STM8AF6148	16 K	0.5 K	1 K						
	STM8AF6186	64 K	1.5 K	4 K		1 x 8-bit,	LIN-UART			
32-pin	STM8AF6176	48 K	1.5 K	3 K	7	3 x 16-bit	SPI, I ² C	In design		
LQFP/QFN	STM8AF6166	32 K	1 K	2 K		(8/8/8)	c, 1 o			
	STM8AF6146	16 K	0.5 K	1 K		1 x 8-bit,				
20-pin	STM8AF6143	16 K	0.5 K	1 K	5	2 x 16-bit	LIN-UART SPI	No		
SO/QFN	STM8AF6142	8 K	384	0.5 K	3	(6/6/6)				

^{1.} The CAN version order-code prefix is STM8AF51xx

	Product	Description	Order code
Development software	ST MCU toolset	Toolset for building, debugging and programming applications includes assembler-linker, ST Visual Develop (STVD) IDE, integrated control of Cosmic and Raisonance C compilers, and ST Visual Programmer (STVP)	Free download
	RIDE	Raisonance IDE with C compiler, RBuilder and RFlasher programming software	(note 1)
Commiles	Cosmic	C Compiler for ST 8-bit microcontrollers. Available in free version that outputs up to 16 Kbytes of code	(note 1)
Compiler	Raisonance	C Compiler for ST 8-bit microcontrollers. Available in free version that outputs up to 16 Kbytes of code	(note 1)
	CAN driver	Certified CAN driver from Vector Software GmbH	(note 1)
Libraries and drivers	STM8 Library	ST standard firmware library covering all standard device peripherals	Free download
	LIN driver	ST LIN driver and package supporting STM8A LIN-UART	(note 3)
Emulation system	STICE	Advanced emulation system for ST microcontrollers provides configurable breakpoints, Trace, Code Coverage² and Profiling², plus in-circuit debugging/programming capability (SWIM)	CB-STICE-S001
In-circuit debugger/ programmer	RLink	Raisonance in-circuit debugger/programmer for ST microcontrollers supports SWIM interfaces	CB-STX-RLINK
Evaluation board	CB-8/128-EVAL	ST full-featured evaluation board for STM8A microcontrollers	CB-8/128-EV/WS

^{1.} For ordering information pricing and order codes, refer to the respective third-party tool provider

^{2.} Under development

^{3.} Available free of charge, contact your nearest sales office

SPC56xD/B MCU family: automotive 32-bit Flash microcontrollers

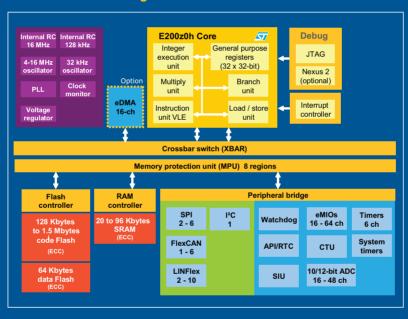
STMicroelectronics' SPC56xB/D family of 32-bit Flash microcontrollers is dedicated to the specific needs of body and convenience applications.

The modularity and compatibility of the product family portfolio is set to an unmatched level. A new state-of-the-art technology combined with a high-performance core and tailor-made peripherals, make this family of products the perfect platform solution, achieving the best equilibrium between system cost and performance.

From product specification, on through design and manufacturing, focus is on reliability, application robustness and added value.

The use of an industry standard PowerPC® core, sharing standard peripherals with similar products across all application fields, increases integration, maximizes design reuse and shortens time to market.

SPC56xB/D block diagram





32-bit SPC56xB/D automotive microcontroller family overview

		S	ystem	Code	Code Data				Serial interfaces		
Part number	Package	Core	Maximum frequency	Flash (Kbytes)	Flash (Kbytes)	RAM (Kbytes)	A/D	Timed I/Os	SPI/LIN/CAN/I ² C	Other¹	
SPC560D30	LQFP64 ²	Z0h	48	128	64	16	16	6	2/2/1/0		
SPC560D30	LQFP100	Z0h	48	128	64	16	32	24	2/3/1/0	10 bit ADC CTU	
SPC560D40	LQFP64 ²	Z0h	48	256	64	20	16	6	2/2/1/0	12-bit ADC, CTU	
SPC560D40	LQFP100	Z0h	48	256	64	20	32	24	2/3/1/0		
SPC560B40	LQFP100	Z0h	64	256	64	24	28	28	3/3/2/1		
SPC560B40	LQFP144	Z0h	64	256	64	24	36	56	3/3/2/1		
SPC560B44	LQFP100	Z0h	64	384	64	28	28	28	3/4/3/1	10 bit ADC CTIL MOU	
SPC560B44	LQFP144	Z0h	64	384	64	28	36	56	3/4/3/1	10-bit ADC, CTU, MPU	
SPC560B50	LQFP100	Z0h	64	512	64	32	28	28	3/4/3/1		
SPC560B50	LQFP144	Z0h	64	512	64	32	36	56	3/4/3/1		
SPC560B54 ²	LQFP100	Z0h	64	768	64	64	26	34	3/4/6/1		
SPC560B54 ²	LQFP144	Z0h	64	768	64	64	34	64	4/6/6/1		
SPC560B54 ²	LQFP176	Z0h	64	768	64	64	48	64	6/8/6/1	Dual ADC 10/12-bit CTU,	
SPC560B60 ²	LQFP144	Z0h	64	1024	64	80	34	64	4/6/6/1	MPU, eDMA	
SPC560B60 ²	LQFP176	Z0h	64	1024	64	80	48	64	6/8/6/1		
SPC560B64 ²	LQFP176	Z0h	64	1536	64	96	48	64	6/10/6/1		

^{1.} All products include 32 MHz oscillator, real-time clock, clock monitoring and register protection. Operating temperature range from - 40° C to 85° C up to - 40° C to 125° C. 2. In development. Contact your sales office.

Power management

A sophisticated low-power management allows for a quantum leap in power saving, avoiding the use of a secondary microcontroller. The low-power and wake-up concepts support LIN and CAN communication from standby mode with identification of the CAN message ID triggering wake-up.

Benefits Reduced system cost Improved time to market Lighting module with diagnostics Standard core for maximum reuse ■ EEPROM emulation support Designed for AUTOSAR Improved EMI Memory/pinout/performance scalability Innovative power management concept Compatibility of product family Dual on-chip RC oscillators Existing tools ecosystem and know-how **Power and robustness** Reduced cost of non-quality Z0h - Z4d PowerPC Core Zero defect strategy from design to production ECC on all memories Latest 90 nm automotive focused technology Memory/register protections Co-development of technology with Freescale Clock security system/backup oscillator Supply chain strengthened via dual source CPU clock independent watchdog capability Injection robust I/Os



A comprehensive development tools offer

The SPC56 product family is supported by a wide range of development tools using a vast network of 3rd parties. This includes classical C compilers/debuggers/emulators, as well as advanced tools such as configuration tools or auto-code generators. Evaluation boards are available from ST.

Designed for AUTOSAR

All products are designed to fulfill AUTomotive Open System ARchitecture requirements. Available AUTOSAR packages include MCAL, basic software, OS, configuration tools and on-site support.



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