

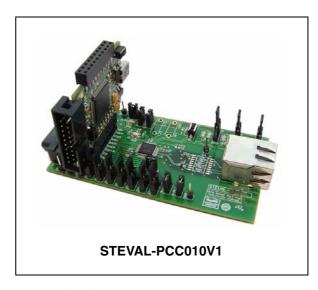
## STEVAL-PCC010V1

# ST802RT1A Ethernet PHY demonstration board with STM32F107 controller add-on board

Data brief

#### **Features**

- ST802RT1A Ethernet PHY demonstration board:
  - ST802RT1A fast Ethernet physical layer transceiver
  - On-board 3.3 V LDO regulator
  - On-board 25 MHz crystal
  - 2 jumpers for boot-strap configuration (MII address, auto-negotiation, loopback, power down, MII/RMII configuration)
  - Several GND test points and jumpers for power consumption measurement
  - Connectors: 20-pin full pitch header connector for debug purposes (compatible with the STM32F107 controller board); 40pin connector compatible with Spirent Communications SmartBits 200/2000 (SMB-200/ SMB-2000) analysis system
  - RJ45 connector with embedded transformer
  - RoHS compliant
- STM32F107 controller add-on board:
  - STM32F107 connectivity line Cortex<sup>™</sup>-M3 based microcontroller with embedded Ethernet MAC
  - On-board 3.3 V LDO regulator
  - On-board 25 MHz crystal
  - Reset button, power LED, general-purpose button and two LEDs
  - Connectors: 20-pin full-pitch header connector for debug purposes, compatible with ST802RT1A demonstration board; 20pin JTAG connector
  - Additional general-purpose 20-pin full pitch header connector; USB device connector (+5 V power supply)
  - RoHS compliant



#### Description

The STEVAL-PCC010V1 demonstration kit consists of two boards: the ST802RT1A Ethernet PHY demonstration board and the STM32F107 controller add-on board.

The ST802RT1A demonstration board is designed as an evaluation platform for the ST802RT1A device. It allows the user to easily select the PHY boot options to evaluate the power consumption of the chip, and to attach the device to professional test equipment.

The STM32F107 controller board can extend the ST802RT1A demonstration board with the STM32F107 microcontroller and its embedded MAC.

The system allows immediate evaluation of an Internet appliance based on the embedded microcontroller and Ethernet PHY.

By default, the controller board is pre-Flashed with a web server application for demonstration purposes.

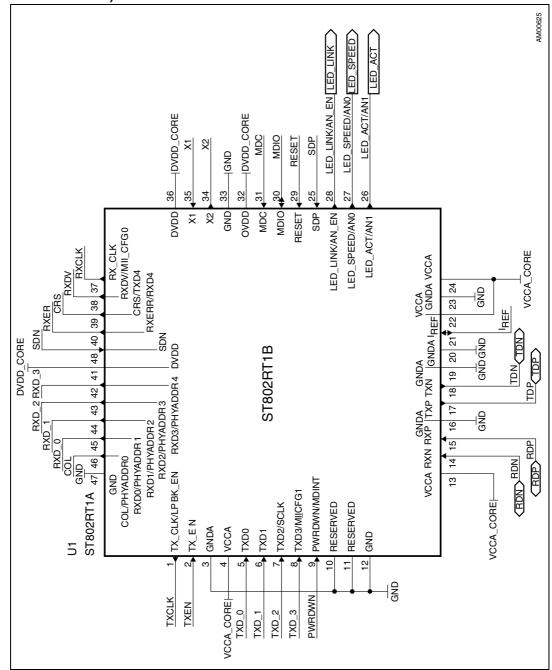
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Schematic diagrams STEVAL-PCC010V1

## 1 Schematic diagrams

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Figure 1. ST802RT1 TX mode Ethernet PHY demonstration board schematic (part 1 of 4)



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R8 750 Ω NA AM00626 25 P GND .MHz 5X-3225, c-238, 25 MHz R5 ]R6 ]750 \\ \text{NA} R7 1.2 ΚΩ QM SDN GND D SE GND B1 RESET 」 13 KS KS GND -02 -10 nF GND DVDD\_CORE GND VCCA\_CORE 10 KB 74 **PWRDWN** 

Figure 2. ST802RT1 TX mode Ethernet PHY demonstration board schematic (part 2 of 4)

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3.3 V R30 2 K□ R26 2 K□ GNDGND C15 100 nF LD1117S33 GND C14 100 nF GND GND GND GND GND C13 <sup>-</sup> 10 µF / 6.3 V D-DVDD\_CORE DVDD NFE31PT222Z1E9L NFE31PT222Z1E9L GND 3.3 V

Figure 3. ST802RT1 TX mode Ethernet PHY demonstration board schematic (part 3 of 4)



J9 RJ-45 AM00629 RJ-45 GND GND GND GND GND 1/01 13 6 VBUS GND GND GND 4 俗 쬬 9 O, RDP\_C RDN\_( Typically n t needed at all (terminati nf r unused lines) GND GND 100 m<sup>-1</sup> , SB, VCCA C19 75 NA R34 S Ider nly with embedded transf rmer Ϋ́ at all
Pr perly the resist r
sh uld be in series
with capacit r Typically n t needed R33 GND GND VBUS GND I/O1 I/O1 DVIULC6-2P6 75 □ NA R31 1/02 ပ TDP\_C 16 15 NDL 4 H1300 H1300 NA S Ider nly with embedded transf rmer RX+ CT  $\Box$ 100 nF  $RD_{\uparrow}$ AP-TD+ 4  $\Box$ CT S Ider nly with embedded transf rmer ω N 9 100 nF NA \f |-|-|-₹ VCCA 100 nF NA -85 -----D SB4 ž VCCA GNB S Ider nly with = : standal ne transf rmer S Ider nly with standal ne transf rmer O S RDN\_C RDP\_ TP\_ NOT

Figure 4. ST802RT1 TX mode Ethernet PHY demonstration board schematic (part 4 of 4)

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Schematic diagrams STEVAL-PCC010V1

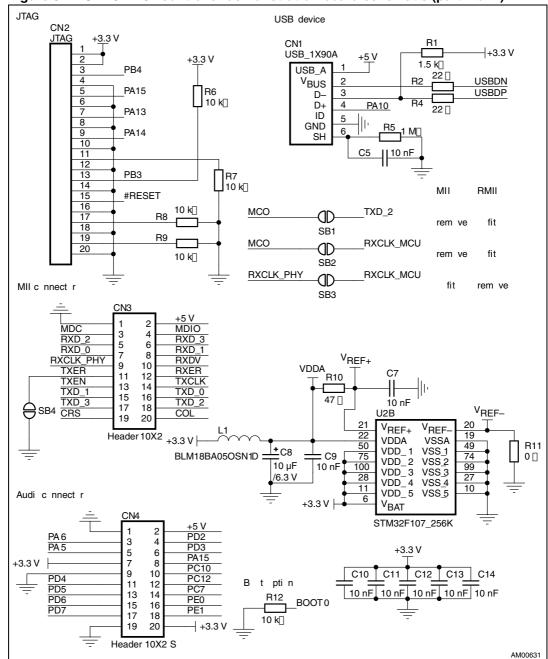
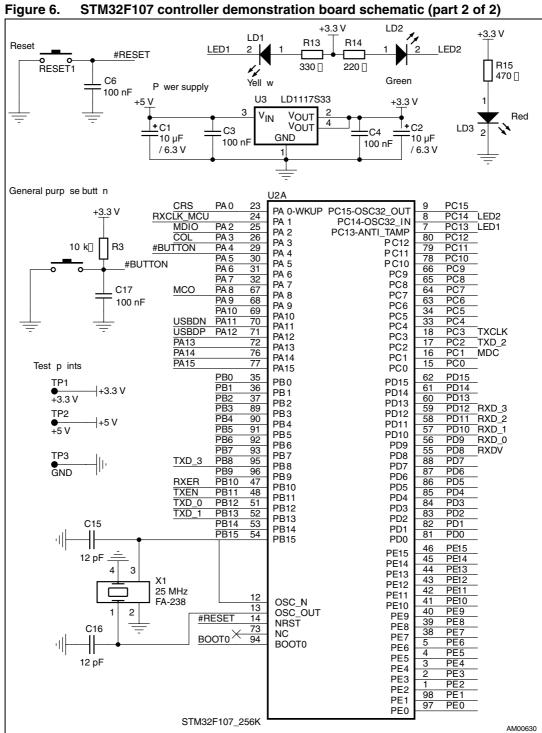


Figure 5. STM32F107 controller demonstration board schematic (part 1 of 2)







Revision history STEVAL-PCC010V1

# 2 Revision history

Table 1. Document revision history

Date	Revision	Changes
08-Mar-2010	1	Initial release.

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