14-/12-Bit 310Msps Dual/Single ADC Family



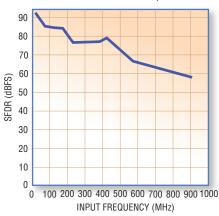
High Undersampling Performance to 900MHz

The LTC®2153 and LTC2158 are a family of single and dual, high IF sampling 14-/12-bit, 170Msps to 310Msps ADCs that maintain excellent SFDR performance at input frequencies up to 900MHz. These ADCs have been specifically designed to meet the needs of today's communications systems, where high undersampling capability saves cost by eliminating downconversion stages.

Features

- 1.8V Single-Supply Operation
- DDR LVDS Outputs
- Easy-to-Drive 1.5V_{P-P} Input Range
 - 1.32V_{P-P} for LTC2158/LTC2153
- 1.25GHz Full Power Bandwidth S/H
- Optional Clock Duty Cycle Stabilizer
- Low Power Sleep and Nap Modes
- Serial SPI Port for Configuration
- Easy Evaluation Using PScope[™] Tool





LTC2157-14: SNR vs Input Frequency, -1dBFS, 250Msps

