New 12-Bit Data Acquisition Systems Communicate with Microprocessors Over 4 Wires

As board space and semiconductor package pins become more valuable, serial data transfer methods between microprocessors (MPUs) and their peripherals become more and more attractive. Not only does this save lines in the transmission medium, but, because of the savings in package pins, more function can be packed into both the MPU and the peripheral. Users are increasingly able to take advantage of these savings as more MPU manufacturers develop serial ports for their products. However, peripherals which are able to communicate with these MPUs must be available in order for users to take full advantage. Also, MPU serial formats are not standardized so not all peripherals can talk to all MPUs.

The LTC1290 Family

A new family of 12-bit data acquisition circuits has been developed to communicate over just 4 wires to the recently developed MPU synchronous serial formats as well as to MPUs which do not have serial ports. These circuits feature software configurable analog circuitry including analog multiplexers, sample and holds, bipolar and unipolar conversion modes and the ability to shut power completely off. They also have serial ports which can be software configured to communicate with virtually any MPU. Even the lowest grade device features guaranteed ±0.5LSB linearity over the full operating temperature range. Reduced span operation, accuracy over a wide temperature range and low power single supply operation make it possible to locate these circuits near remote sensors and transmit digital data back through noisy media to the MPU. Figure 1 shows a typical hookup of the LTC1290, the first member of this data acquisition family. For more detail, refer to the LTC1290 data sheet.

Included are eight analog inputs which can common-mode to both supply rails. Each can be configured for unipolar or bipolar conversions and for single-ended or differential inputs by sending a data input (DIN) word from the MPU to the LTC1290 (Figure 1).

Both the power supplies are bypassed to analog ground. The V- supply allows the device to operate with inputs which swing below ground. In single supply applications it can be tied to ground.

The span of the A/D converter is set by the reference inputs which, in this case, are driven by a 2.5V LT1009 which gives an LSB step size of 0.61mV. However, any reference voltage within the power supply range can be used.

The 4 wire serial interface consists of an active low chip select pin (CS), a shift clock (SCLK) for synchronizing the data bits, a data input (DIN) and a data output (DOUT). Data is transmitted and received simultaneously (full duplex), minimizing the transfer time required.

The external ACLK input controls the conversion rate and can be tied to SCLK as in Figure 1. Alternatively, it can be derived from the MPU system clock (e.g., the 8051 ALE pin) or run asynchronously. When the ACLK pin is driven at 4MHz, the conversion time is 13μs.

Figure 1. A Typical Hookup of the LTC1290

Advantages of Serial Communications

The LTC1290 can be located near the sensors and serial data can be transmitted back from remote locations through isolation barriers or through noisy media.
Talking to MPUs without Serial Ports

The LTC1290 talks to serial port processors but works equally well with MPUs which do not have serial ports. In these cases, CS, SCLK and DIN are generated with software on 3 port lines. DOUT is read on a fourth. Figure 4 shows the appropriate DIN word for communicating with MPU parallel ports. Figure 1 shows a 4 wire interface to the popular Intel 8051. A complete transfer takes only 33 lines of code.

Sharing the Serial Interface

No matter what processor is used, the serial port can be shared by several LTC1290s or other peripherals (see Figure 2). A separate CS line for each peripheral determines which is being addressed.

Conclusions

The LTC1290 family provides data acquisition systems which communicate via a simple 4 wire serial interface to virtually any microprocessor. By eliminating the parallel data bus they are able to provide more function in smaller packages, right down to 8 pin DIPs. Because of the serial approach, remote location of the A/D circuitry is possible and digital transmission through noisy media or isolation boundaries is made easier without a great loss in speed.

<table>
<thead>
<tr>
<th>Type of Interface</th>
<th>LTC1290 Data Format</th>
<th>Analog Configuration</th>
<th>MSBF</th>
<th>WL1</th>
<th>WL0</th>
</tr>
</thead>
<tbody>
<tr>
<td>All Parallel Port MPUs</td>
<td>LSB First 12 Bits</td>
<td>X X X X X X 1 1 0</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>National MICROWIRE+</td>
<td>LSB First 12 Bits</td>
<td>X X X X X X 1 1 0</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>National MICROWIRE/PLUS</td>
<td>LSB First 16 Bits</td>
<td>X X X X X X 1 1 1</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Motorola SPI</td>
<td>LSB First 16 Bits</td>
<td>X X X X X X 0 1 1</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Hitachi Synchronous SCI</td>
<td>LSB First 16 Bits</td>
<td>X X X X X X 0 1 1</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>TI TMS7000 Serial Port</td>
<td>LSB First 16 Bits</td>
<td>X X X X X X 0 1 1</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Figure 4. The LTC1290 Accommodates Both Parallel and Serial Ports

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Bibliography

For LTC1290 literature, call 800-637-5545. For help with an application call 432-1900, Ext. 445.