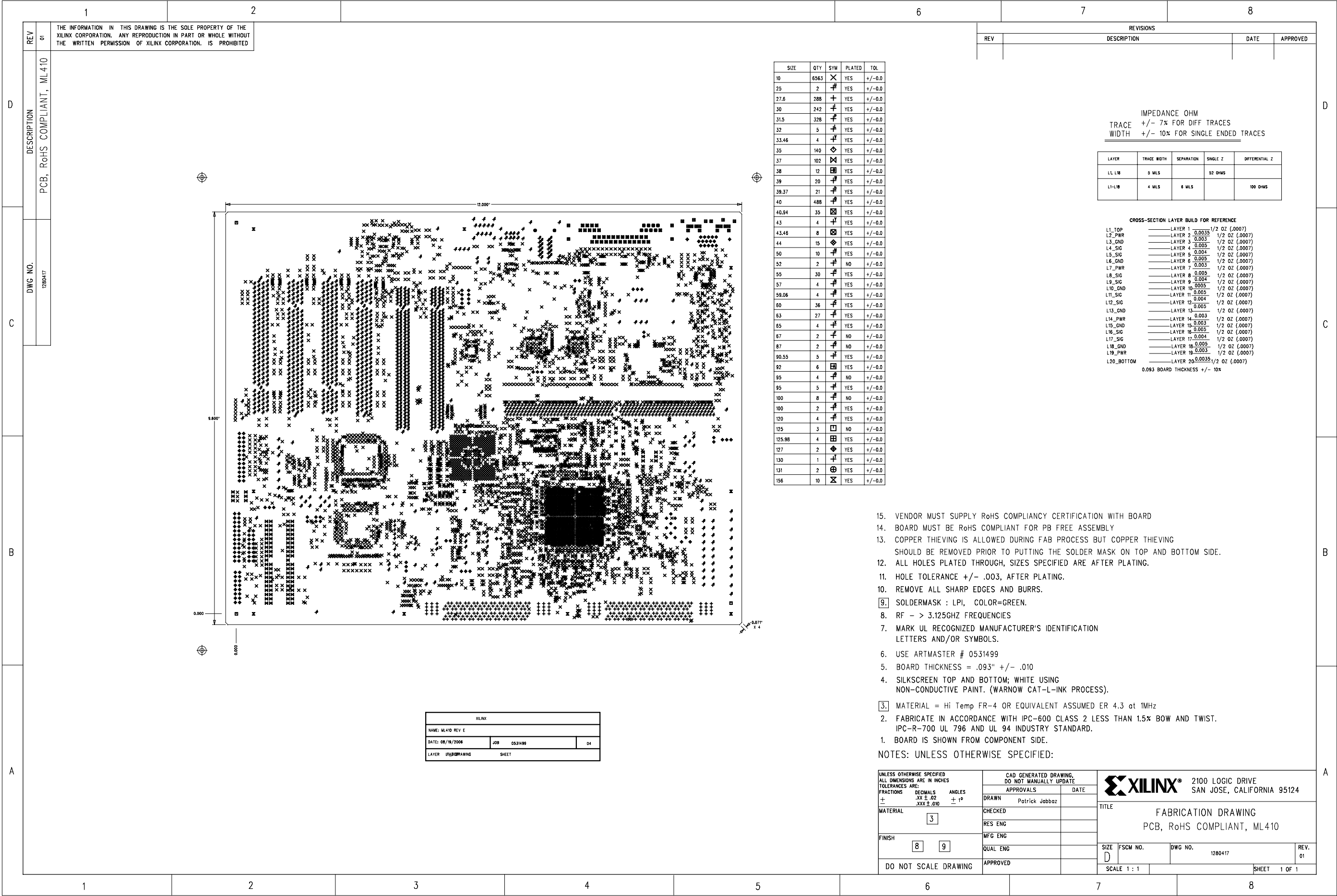




CROSS-SECTION LAYER BUILD FOR REFERENCE

L1\_TOP LAYER 1 1/2 OZ (.0007)

L2\_PWR LAYER 2 0.0035 1/2 OZ (.0007)

L3\_GND LAYER 3 0.003 1/2 OZ (.0007)

L4\_SIG LAYER 4 0.005 1/2 OZ (.0007)

L5\_SIG LAYER 5 0.005 1/2 OZ (.0007)

L6\_GND LAYER 6 0.005 1/2 OZ (.0007)

L7\_PWR LAYER 7 0.003 1/2 OZ (.0007)

L8\_SIG LAYER 8 0.005 1/2 OZ (.0007)

L9\_SIG LAYER 9 0.004 1/2 OZ (.0007)

L10\_GND LAYER 10 0.005 1/2 OZ (.0007)

L11\_SIG LAYER 11 0.005 1/2 OZ (.0007)

L12\_SIG LAYER 12 0.004 1/2 OZ (.0007)

L13\_GND LAYER 13 0.005 1/2 OZ (.0007)

L14\_PWR LAYER 14 0.003 1/2 OZ (.0007)

L15\_GND LAYER 15 0.003 1/2 OZ (.0007)

L16\_SIG LAYER 16 0.005 1/2 OZ (.0007)

L17\_SIG LAYER 17 0.004 1/2 OZ (.0007)

L18\_GND LAYER 18 0.005 1/2 OZ (.0007)

L19\_PWR LAYER 19 0.003 1/2 OZ (.0007)

L20\_BOTTOM LAYER 20 0.0035 1/2 OZ (.0007)

0.093 BOARD THICKNESS +/- 10%

15. VENDOR MUST SUPPLY RoHS COMPLIANCY CERTIFICATION WITH BOARD

14. BOARD MUST BE RoHS COMPLIANT FOR PB FREE ASSEMBLY

13. COPPER THEIVING IS ALLOWED DURING FAB PROCESS BUT COPPER THEIVING SHOULD BE REMOVED PRIOR TO PUTTING THE SOLDER MASK ON TOP AND BOTTOM SIDE.

12. ALL HOLES PLATED THROUGH, SIZES SPECIFIED ARE AFTER PLATING.

11. HOLE TOLERANCE +/- .003, AFTER PLATING.

10. REMOVE ALL SHARP EDGES AND BURRS.

9. SOLDERMASK : LPI, COLOR=GREEN.

8. RF - > 3.125GHZ FREQUENCIES

7. MARK UL RECOGNIZED MANUFACTURER'S IDENTIFICATION LETTERS AND/OR SYMBOLS.

6. USE ARTMASTER # 0531499

5. BOARD THICKNESS = .093" +/- .010

4. SILKSCREEN TOP AND BOTTOM; WHITE USING NON-CONDUCTIVE PAINT. (WARNOW CAT-L-INK PROCESS).

3. MATERIAL = Hi Temp FR-4 OR EQUIVALENT ASSUMED ER 4.3 at 1MHz

2. FABRICATE IN ACCORDANCE WITH IPC-600 CLASS 2 LESS THAN 1.5% BOW AND TWIST. IPC-R-700 UL 796 AND UL 94 INDUSTRY STANDARD.

1. BOARD IS SHOWN FROM COMPONENT SIDE.

NOTES: UNLESS OTHERWISE SPECIFIED:

UNLESS OTHERWISE SPECIFIED  
ALL DIMENSIONS ARE IN INCHES  
DO NOT MANUALLY UPDATE

TOLERANCES ARE:  
FRACTIONS DECIMALS ANGLES  
± .XX ± .02 ± 1°  
± .XXX ± .010

CAD GENERATED DRAWING.  
DO NOT MANUALLY UPDATE

APPROVALS DATE

DRAWN Patrick Jabbar

CHECKED

RES ENG

MFG ENG

QUAL ENG

APPROVED

DO NOT SCALE DRAWING

XILINX® 2100 LOGIC DRIVE  
SAN JOSE, CALIFORNIA 95124

TITLE  
FABRICATION DRAWING  
PCB, RoHS COMPLIANT, ML410

SIZE D

FSCM NO.

DWG NO. 1280417

REV. 01

SCALE 1 : 1

SHEET 1 OF 1